

Schematic Symbol for XC6SLX100T-FGG676

The symbol consists of 11 heterogeneous parts, each of them listed below:

1. I/O Bank 0

U?A			
H7	IO_L1P_HSWAPEN_0	IO_L86N_SCP0_0	D22
G7	IO_L1N_VREF_0	IO_L86P_SCP1_0	D21
H8	IO_L2P_0	IO_L85N_SCP2_0	A23
G8	IO_L2N_0	IO_L85P_SCP3_0	B23
F7	IO_L3P_0	IO_L84N_SCP4_0	F19
F6	IO_L3N_0	IO_L84P_SCP5_0	G19
C3	IO_L4P_0	IO_L83N_SCP6_0	A22
B3	IO_L4N_0	IO_L83P_SCP7_0	B22
G6	IO_L5P_0	IO_L62N_VREF_0	H19
F5	IO_L5N_0	IO_L62P_0	H18
E6	IO_L8P_0	IO_L59N_0	B21
E5	IO_L8N_VREF_0	IO_L59P_0	C21
H9	IO_L13P_0	IO_L58N_0	G17
G9	IO_L13N_0	IO_L58P_0	H17
A3	IO_L14P_0	IO_L57N_0	E20
A2	IO_L14N_0	IO_L57P_0	F20
F9	IO_L14N_0	IO_L56N_0	F17
NC		IO_L56P_0	G16
E8	NC		E18
D5	NC		F18
C5	NC		NC
H10	NC		F15
G10	NC		NC
NC			G15
B4	IO_L22P_0		NC
A4	IO_L22N_0		NC
F10	NC		NC
E10	NC	IO_L48N_0	J17
B5	NC	IO_L48P_0	J16
A6	IO_L24P_0	IO_L43N_0	H15
E13	IO_L24N_0	IO_L43P_0	J15
D13	IO_L34P_GCLK19_0		H14
C13	IO_L34N_GCLK18_0		NC
A13	IO_L35P_GCLK17_0		NC
B12	IO_L35N_GCLK16_0		NC
A12	IO_L36P_GCLK15_0		NC
B14	IO_L36N_GCLK14_0		NC
A14	IO_L37P_GCLK13_0	IO_L38N_VREF_0	J13
G12	IO_L37N_GCLK12_0	IO_L38P_0	J12
F11	NC	IO_L33N_0	K12
F12	NC	IO_L33P_0	G13
NC		IO_L32N_0	H12
E12	NC	IO_L32P_0	G11
			J11

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2. MGTs BANK0

U?B			
A6	MGTTXND_101	MGTTXP1_123	B20
B6	MGTTXP0_101	MGTTXN1_123	A20
B11	MGTAVCCPLL0_101	MGTREFCLK1P_123	B16
A10	MGTREFCLKDN_101	MGTREFCLK1N_123	A16
B10	MGTREFCLKDP_101	MGTAVCCPLL1_123	B15
C7	MGTRXND_101	MGTRXP1_123	D19
D7	MGTRXP0_101	MGTRXN1_123	C19
E9	MGTRREF_101	MGTRXP0_123	D17
C9	MGTRXN1_101	MGTRXND_123	C17
E11	MGTAVTTRCAL_101	MGTREFCLKDP_123	D15
D9	MGTRXP1_101	MGTREFCLKDN_123	C15
C12	MGTAVCCPLL1_101	MGTAVCCPLL0_123	C14
C11	MGTREFCLK1N_101	MGTTXP0_123	B18
D11	MGTREFCLK1P_101	MGTTXND_123	A18
A8	MGTTXN1_101		
B8	MGTTXP1_101		

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3. I/O Bank 5

U?C		
H20	IO_L1P_A25_5	IO_L27N_5
G20	IO_L1N_A24_VREF_5	IO_L27P_5
B24	IO_L2P_M5A13_5	IO_L26N_VREF_5
A25	IO_L2N_M5A14_5	IO_L26P_5
K18	IO_L3P_M5RESET_5	IO_L25N_M5DQ15_5
K19	IO_L3N_M5A11_5	IO_L25P_M5DQ14_5
D23	IO_L4P_M5CKE_5	IO_L24N_M5DQ13_5
C24	IO_L4N_M5A12_5	IO_L24P_M5DQ12_5
H21	IO_L5P_M5A8_5	IO_L23N_M5UDQS_N_5
H22	IO_L5N_M5A9_5	IO_L23P_M5UDQS_5
F22	IO_L6P_M5A10_5	IO_L22N_M5DQ11_5
G23	IO_L6N_M5A4_5	IO_L22P_M5DQ10_5
J20	IO_L7P_M5WE_5	IO_L21N_M5DQ9_5
J22	IO_L7N_M5BA2_5	IO_L21P_M5DQ8_5
E23	IO_L8P_M5A7_5	IO_L20N_M5DQ1_5
E24	IO_L8N_M5A2_5	IO_L20P_M5DQ0_5
L19	IO_L9P_M5BA0_5	IO_L19N_M5DQ3_5
K20	IO_L9N_M5BA1_5	IO_L19P_M5DQ2_5
C25	IO_L10P_M5A0_5	IO_L18N_M5LDQS_N_5
C26	IO_L10N_M5A1_5	IO_L18P_M5LDQS_5
B25	IO_L11P_M5CLK_5	IO_L17N_M5DQ7_5
B26	IO_L11N_M5CLKN_5	IO_L17P_M5DQ6_5
K21	IO_L12P_M5A3_5	IO_L16N_M5DQ5_5
K22	IO_L12N_M5ODT_5	IO_L16P_M5DQ4_5
M18	IO_L13P_M5A6_5	IO_L15N_M5LDM_5
M19	IO_L13N_M5A6_5	IO_L15P_M5UDM_5
F23	IO_L14P_M5RASN_5	
G24	IO_L14N_M5CASN_5	

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4. I/O Bank 1

U?D		
N17	IO_L28P_1	IO_L74N_DOUT_BUSY_1
N18	IO_L28N_VREF_1	IO_L74P_AWAKE_1
L23	IO_L29P_A23_M1A13_1	IO_L89N_VREF_1
L24	IO_L29N_A22_M1A14_1	IO_L89P_1
N19	IO_L30P_A21_M1RESET_1	IO_L88N_1
N20	IO_L30N_A20_M1A11_1	IO_L88P_1
N21	IO_L31P_A19_M1CKE_1	IO_L87N_1
N22	IO_L31N_A18_M1A12_1	IO_L87P_1
P17	IO_L32P_A17_M1A8_1	IO_L86N_1
P19	IO_L32N_A16_M1A9_1	IO_L86P_1
N23	IO_L33P_A15_M1A10_1	IO_L53N_VREF_1
N24	IO_L33N_A14_M1A4_1	IO_L53P_1
R18	IO_L34P_A13_M1WE_1	IO_L52N_M1DQ15_1
R19	IO_L34N_A12_M1BA2_1	IO_L52P_M1DQ14_1
P21	IO_L35P_A11_M1A7_1	IO_L51N_M1DQ13_1
P22	IO_L35N_A10_M1A2_1	IO_L51P_M1DQ12_1
R20	IO_L36P_A9_M1BA0_1	IO_L50N_M1UDQS_N_1
R21	IO_L36N_A8_M1BA1_1	IO_L50P_M1UDQS_1
P24	IO_L37P_A7_M1A0_1	IO_L49N_M1DQ11_1
P26	IO_L37N_A6_M1A1_1	IO_L49P_M1DQ10_1
B23	IO_L38P_A6_M1CLR_1	IO_L48N_M1DQ9_1
R24	IO_L38N_A4_M1CLKN_1	IO_L48P_HDC_M1DQ8_1
T22	IO_L39P_M1A3_1	IO_L47N_LDC_M1DQ1_1
T23	IO_L39N_M1ODT_1	IO_L47P_FWE_B_M1DQ0_1
U23	IO_L40P_GCLK1T_M1A6_1	IO_L46N_FOE_B_M1DQ3_1
U24	IO_L40N_GCLK10_M1A6_1	IO_L46P_FCS_B_M1DQ2_1
R25	IO_L41P_GCLK9_TRDY1_M1RASN_L46N_A0_M1LDQS_N_1	
R26	IO_L41N_GCLK8_M1CASN_1	IO_L45P_A1_M1LDQS_1
V23	IO_L42P_GCLK7_M1UDM_1	IO_L44N_A2_M1DQ7_1
W24	IO_L42N_GCLK6_TRDY1_M1LDM0_L44P_A3_M1DQ6_1	
U25	IO_L43P_GCLK5_M1DQ4_1	
U26	IO_L43N_GCLK4_M1DQ5_1	

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5. I/O Bank2 (Contains the Programming Interface)

U?E			
Y19	CMPCS_B_2	TDO	G21
AF25	DONE_2	TMS	F21
AE24	IO_L1P_CCLK_2	TDI	C23
AF24	IO_L1N_M0_CMPMISO_2	TCK	A24
Y21	IO_L2P_CMPCLK_2		
AA22	IO_L2N_CMPMOSI_2		
AD23	IO_L3P_D0_DIN_MISO_MISO1_2		
AF23	IO_L3N_MOSI_CSI_B_MISO0_2	SUSPEND	Y22
W20	IO_L4P_2	VBATT	V22
Y20	IO_L4N_VREF_2	RFUSE	V19
AB22	IO_L5P_2	VFS	W22
AC22	IO_L5N_2		
V18	IO_L12P_D1_MISO2_2		
W19	IO_L12N_D2_MISO3_2	PROGRAM_B_2	AF2
AD22	IO_L13P_M1_2	IO_L65N_CS0_B_2	AF3
AF22	IO_L13N_D10_2	IO_L65P_INIT_B_2	AE3
W17	IO_L14P_D11_2	IO_L64N_D9_2	AA6
W18	IO_L14N_D12_2	IO_L64P_D8_2	AA7
AA21	IO_L15P_2	IO_L63N_2	AF4
AB21	IO_L15N_2	IO_L63P_2	AD4
Y17	IO_L16P_2	IO_L62N_D6_2	W7
AA17	IO_L16N_VREF_2	IO_L62P_D5_2	W8
U15	IO_L17P_2	IO_L61N_VREF_2	AD5
V16	IO_L17N_2	IO_L61P_2	AC5
AA19	NC	NC	AC6
AB19	NC	NC	AB7
W16	NC	NC	AA8
Y16	NC	NC	Y9
AA18	NC	NC	AF5
AB17	NC	NC	AE5
Y15	NC	NC	W9
AA16	NC	NC	W10
V14	NC	NC	AF6
V15	NC	IO_L49N_D4_2	AD6
U13	NC	IO_L49P_D3_2	AB11
V13	NC	IO_L48N_RDWR_B_VREF_2	AA10
AA15	NC	IO_L48P_D7_2	AB9
AB15	IO_L28P_2	IO_L47N_2	AA9
AE15	IO_L28N_2	IO_L47P_2	V10
AF15	IO_L29P_GCLK3_2	IO_L46N_2	V11
AB14	IO_L29N_GCLK2_2	IO_L46P_2	AA11
AC14	IO_L30P_GCLK1_D13_2	IO_L41N_VREF_2	Y11
AE13	IO_L30N_GCLK0_USERCCLK_2	IO_L41P_2	AA13
AF13	IO_L31P_GCLK31_D14_2	NC	AB13
AD14	IO_L31N_GCLK30_D15_2	NC	W12
AF14	IO_L32P_GCLK29_2	NC	V12
Y12	IO_L32N_GCLK28_2	NC	Y13
AA12	IO_L33P_2	IO_L34N_2	W14
	IO_L33N_2	IO_L34P_2	

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6. MGTs I/O Bank 2

U?F			
AE21	MGTTXP1_267	MGTTXND_245	AF7
AF21	MGTTXN1_267	MGTTXPD_245	AE7
AE16	MGTAVCCPLL1_267	MGTREFCLK0N_245	AF11
AE17	MGTREFCLK1P_267	MGTREFCLK0P_245	AE11
AF17	MGTREFCLK1N_267	MGTAVCCPLL0_245	AE12
AC20	MGTRXP1_267	MGTRXND_245	AD8
AD20	MGTRXN1_267	MGTRXPD_245	AC8
AC18	MGTRXPD_267	MGTRREF_245	AB10
AD18	MGTRXND_267	MGTRXN1_245	AD10
AD15	MGTAVCCPLL0_267	MGTAVTRCAL_245	AB12
AC16	MGTREFCLK0P_267	MGTRXP1_245	AC10
AD16	MGTREFCLK0N_267	MGTREFCLK1N_245	AD12
AE19	MGTTXPD_267	MGTREFCLK1P_245	AC12
AF19	MGTTXND_267	MGTAVCCPLL1_245	AD13
		MGTTXN1_245	AF9
		MGTTXP1_245	AE9

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7. I/O Bank 3

U?G			
AB5	IO_L1P_3	IO_L57N_VREF_3	M9
AC4	IO_L1N_VREF_3	IO_L57P_3	M10
AA4	IO_L2P_3	IO_L55N_M3A14_3	N9
AA3	IO_L2N_3	IO_L55P_M3A13_3	P10
Y6	IO_L7P_3	IO_L54N_M3A11_3	N4
Y5	IO_L7N_3	IO_L54P_M3RESET_3	N5
AB4	IO_L8P_3	IO_L53N_M3A12_3	P8
AC3	IO_L8N_3	IO_L53P_M3CKE_3	R9
V7	IO_L9P_3	IO_L52N_M3A9_3	R3
V6	IO_L9N_3	IO_L52P_M3A8_3	R4
U4	IO_L10P_3	IO_L51N_M3A4_3	N7
U3	IO_L10N_3	IO_L51P_M3A10_3	N8
V5	IO_L17P_3	IO_L50N_M3BA2_3	R5
W5	IO_L17N_VREF_3	IO_L50P_M3WE_3	P5
U9	IO_L18P_3	IO_L49N_M3A2_3	P6
U8	IO_L18N_3	IO_L49P_M3A7_3	N6
U7	IO_L31P_3	IO_L48N_M3BA1_3	P1
T6	IO_L31N_VREF_3	IO_L48P_M3BA0_3	P3
AB3	IO_L32P_M3DQ14_3	IO_L47N_M3A1_3	T9
AB1	IO_L32N_M3DQ15_3	IO_L47P_M3A0_3	R10
AD3	IO_L33P_M3DQ12_3	IO_L46N_M3CLKN_3	T4
AD1	IO_L33N_M3DQ13_3	IO_L46P_M3CLK_3	U5
AC2	IO_L34P_M3UDQS_3	IO_L45N_M3ODT_3	T8
AC1	IO_L34N_M3UDQS_N_3	IO_L45P_M3A3_3	R8
AE2	IO_L35P_M3DQ10_3	IO_L44N_GCLK20_M3A6_3	R1
AE1	IO_L35N_M3DQ11_3	IO_L44P_GCLK21_M3A5_3	R2
AA2	IO_L36P_M3DQ8_3	IO_L43N_GCLK22_IRDY2_M3CASN_3	R6
AA1	IO_L36N_M3DQ9_3	IO_L43P_GCLK23_M3RASN_3	R7
Y3	IO_L37P_M3DQ0_3	IO_L42N_GCLK24_M3LDM_3	W3
Y1	IO_L37N_M3DQ1_3	IO_L42P_GCLK25_TRDY2_M3UDM_3	V4
W2	IO_L38P_M3DQ2_3	IO_L41N_GCLK26_M3DQ5_3	T1
W1	IO_L38N_M3DQ3_3	IO_L41P_GCLK27_M3DQ4_3	T3
V3	IO_L39P_M3LDQS_3	IO_L40N_M3DQ7_3	U1
V1	IO_L39N_M3LDQS_N_3	IO_L40P_M3DQ6_3	U2

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8. I/O Bank 4

U?H			
M4	IO_L58P_4	IO_L83N_VREF_4	H5
N3	IO_L58N_VREF_4	IO_L83P_4	H6
N2	IO_L59P_M4DQ0T4_4	IO_L82N_M4A14_4	K6
N1	IO_L59N_M4DQ15_4	IO_L82P_M4A13_4	K7
M3	IO_L60P_M4DQ12_4	IO_L81N_M4A11_4	E3
M1	IO_L60N_M4DQ13_4	IO_L81P_M4RESET_4	E4
L2	IO_L61P_M4UDQS_4	IO_L80N_M4A12_4	K8
L1	IO_L61N_M4UDQSN_4	IO_L80P_M4CKE_4	K9
K3	IO_L62P_M4DQ10_4	IO_L79N_M4A9_4	C1
K1	IO_L62N_M4DQ11_4	IO_L79P_M4A8_4	C2
J2	IO_L63P_M4DQ8_4	IO_L78N_M4A4_4	J7
J1	IO_L63N_M4DQ9_4	IO_L78P_M4A10_4	J9
H3	IO_L64P_M4DQ0_4	IO_L77N_M4B2_4	G3
H1	IO_L64N_M4DQ1_4	IO_L77P_M4BVE_4	G4
G2	IO_L65P_M4DQ2_4	IO_L76N_M4A2_4	K10
G1	IO_L65N_M4DQ3_4	IO_L76P_M4A7_4	L10
F3	IO_L66P_M4LDQS_4	IO_L75N_M4B1_4	B1
F1	IO_L66N_M4LDQSN_4	IO_L75P_M4B10_4	B2
E2	IO_L67P_M4DQ6_4	IO_L74N_M4A1_4	L6
E1	IO_L67N_M4DQ7_4	IO_L74P_M4A0_4	L7
D3	IO_L68P_M4DQ4_4	IO_L73N_M4CLKN_4	J5
D1	IO_L68N_M4DQ5_4	IO_L73P_M4CLK_4	K5
J4	IO_L69P_M4UDM_4	IO_L72N_M4D0T_4	M6
J3	IO_L69N_M4LDM_4	IO_L72P_M4A3_4	M8
L9	IO_L70P_M4RASN_4	IO_L71N_M4A6_4	L3
L8	IO_L70N_M4CASN_4	IO_L71P_M4A6_4	L4

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9. GND

U?1			
A1	GND	GND	Y7
A11	GND	GND	Y4
A15	GND	GND	Y23
A17	GND	GND	Y14
A21	GND	GND	Y10
A26	GND	GND	V8
A9	GND	GND	V25
AB16	GND	GND	V2
AB2	GND	GND	U17
AB20	GND	GND	U11
AB25	GND	GND	T5
AC11	GND	GND	T21
AC13	GND	GND	T18
AC15	GND	GND	T16
AC17	GND	GND	T14
AD19	GND	GND	T12
AD21	GND	GND	T10
AD7	GND	GND	P7
AD9	GND	GND	P25
AE10	GND	GND	P20
AE18	GND	GND	P2
AE20	GND	GND	P16
AE22	GND	GND	P13
AE6	GND	GND	N14
AE8	GND	GND	N11
AF1	GND	GND	M5
AF10	GND	GND	M22
AF12	GND	GND	L17
AF16	GND	GND	L15
AF18	GND	GND	L13
AF26	GND	GND	L11
B17	GND	GND	K25
B19	GND	GND	K2
B7	GND	GND	K16
B9	GND	GND	J8
C18	GND	GND	J19
C20	GND	GND	H4
C6	GND	GND	H23
C8	GND	GND	G14
D10	GND	GND	F25
D12	GND	GND	F2
D14	GND	GND	E7
D16	GND	GND	E22
D4	GND	GND	E19
E15	GND	GND	
	GND		

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10. Power

U?J

AA6	VCCAUX	VCCINT	U16
AB18	VCCAUX	VCCINT	U10
AB8	VCCAUX	VCCINT	T17
AC21	VCCAUX	VCCINT	T15
AC7	VCCAUX	VCCINT	T13
D20	VCCAUX	VCCINT	T11
D6	VCCAUX	VCCINT	R17
E17	VCCAUX	VCCINT	R16
G5	VCCAUX	VCCINT	R15
J10	VCCAUX	VCCINT	R14
J18	VCCAUX	VCCINT	R13
K13	VCCAUX	VCCINT	R12
K15	VCCAUX	VCCINT	R11
L18	VCCAUX	VCCINT	P15
L22	VCCAUX	VCCINT	P14
L5	VCCAUX	VCCINT	P12
M17	VCCAUX	VCCINT	P11
N10	VCCAUX	VCCINT	N16
R22	VCCAUX	VCCINT	N15
U12	VCCAUX	VCCINT	N13
U14	VCCAUX	VCCINT	N12
U18	VCCAUX	VCCINT	M16
U6	VCCAUX	VCCINT	M15
V17	VCCAUX	VCCINT	M14
V9	VCCAUX	VCCINT	M13
W13	VCCAUX	VCCINT	M12
		VCCINT	M11
		VCCINT	L16
		VCCINT	L14
		VCCINT	L12
		VCCINT	K17
		VCCINT	K11

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U?K

C10	MGTAVCC_101	VCC0_5	M25
C16	MGTAVCC_123	VCC0_5	M20
AD11	MGTAVCC_245	VCC0_5	K23
AD17	MGTAVCC_267	VCC0_5	J21
		VCC0_5	H25
D8	MGTAVTRX_10	VCC0_5	G22
D18	MGTAVTRX_123	VCC0_5	D25
AC9	MGTAVTRX_245		
AC19	MGTAVTRX_267	VCC0_4	M7
		VCC0_4	M2
A7	MGTAVTTX_10	VCC0_4	K4
A19	MGTAVTTX_123	VCC0_4	J6
AF8	MGTAVTTX_245	VCC0_4	H2
AF20	MGTAVTTX_267	VCC0_4	F4
		VCC0_4	D2
B13	VCC0_0		
C22	VCC0_0	VCC0_3	Y2
C4	VCC0_0	VCC0_3	W6
E21	VCC0_0	VCC0_3	W4
F13	VCC0_0	VCC0_3	T7
F8	VCC0_0	VCC0_3	T2
G18	VCC0_0	VCC0_3	P9
H11	VCC0_0	VCC0_3	P4
H16	VCC0_0	VCC0_3	AD2
J14	VCC0_0		
		VCC0_2	Y8
		VCC0_2	Y18
AB23	VCC0_1	VCC0_2	W15
AD25	VCC0_1	VCC0_2	W11
P18	VCC0_1	VCC0_2	AE4
P23	VCC0_1	VCC0_2	AE23
T25	VCC0_1	VCC0_2	AE14
W21	VCC0_1	VCC0_2	AB6
W23	VCC0_1	VCC0_2	AA20
Y25	VCC0_1	VCC0_2	AA14

XC6SLX75-FGG676

Notes:

1. The dedicated DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX75T, LX100T and LX150T. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Apr 12, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.