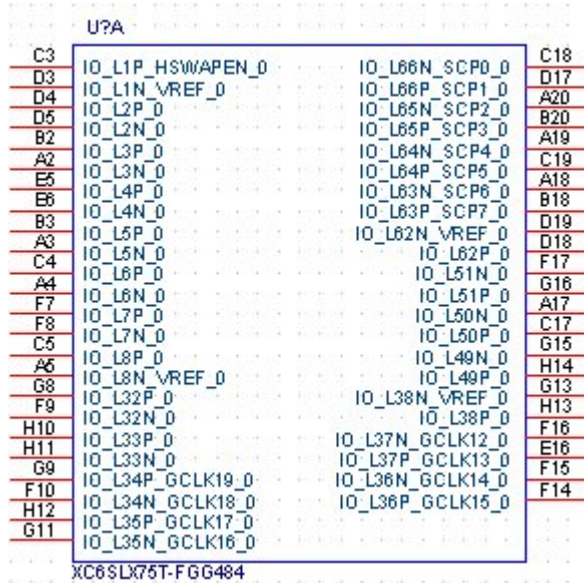


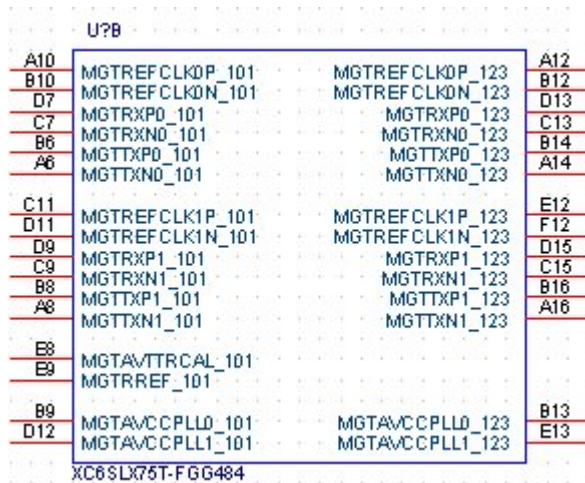
Schematic Symbol for XC6SLX75T-FGG484

The symbol consists of 7 heterogeneous parts, each of them listed below:

1. I/O Bank 0



2. MGT 101 and 123 (BANK0)



4. I/O Bank2 (Contains the Programming Interface)

U?D		
V18	CMPCS_B_2	TDO
AB21	DONE_2	TMS
Y20	IO_L1P_CCLK_2	TDI
AA21	IO_L1N_M0_CMPMISO_2	TCK
V17	IO_L2P_CMPCLK_2	
W18	IO_L2N_CMPMOSI_2	
AA20	IO_L3P_D0_DIN_MISO_MISO1_2	SUSPEND
AB20	IO_L3N_MOSI_CSI_B_MISOD_2	VBATT
U16	IO_L4P_2	RFUSE
V15	IO_L4N_VREF_2	VFS
W17	IO_L5P_2	
Y18	IO_L5N_2	PROGRAM_B_2
AA14	IO_L6P_2	IO_L65N_CSO_B_2
AB14	IO_L6N_2	IO_L65P_INIT_B_2
R13	IO_L12P_D1_MISO2_2	IO_L64N_D9_2
T14	IO_L12N_D2_MISO3_2	IO_L64P_D8_2
Y19	IO_L13P_M1_2	IO_L63N_2
AB19	IO_L13N_D10_2	IO_L63P_2
AA18	IO_L14P_D11_2	IO_L62N_D6_2
AB18	IO_L14N_D12_2	IO_L62P_D5_2
Y17	IO_L15P_2	NC
AB17	IO_L15N_2	NC
U14	IO_L16P_2	NC
U13	IO_L16N_VREF_2	NC
Y16	NC	NC
W15	NC	NC
V13	NC	NC
W13	NC	NC
AA16	IO_L19P_2	NC
AB16	IO_L19N_2	NC
W14	NC	IO_L49N_D4_2
Y14	NC	IO_L49P_D3_2
Y15	IO_L21P_2	IO_L48N_RDWR_B_VREF_2
AB15	IO_L21N_2	IO_L48P_D7_2
R11	NC	IO_L47N_2
T11	NC	IO_L47P_2
T15	NC	NC
U15	NC	NC
T12	IO_L29P_GCLK3_2	IO_L45N_2
U12	IO_L29N_GCLK2_2	IO_L45P_2
Y13	IO_L30P_GCLK1_D13_2	NC
AB13	IO_L30N_GCLK0_USERCCLK_2	NC
AA12	IO_L31P_GCLK3T_D14_2	IO_L43N_2
AB12	IO_L31N_GCLK3D_D15_2	IO_L43P_2
Y11	IO_L32P_GCLK29_2	NC
AB11	IO_L32N_GCLK28_2	NC
W12	IO_L40P_2	IO_L41N_VREF_2
Y12	IO_L40N_2	IO_L41P_2
G17		
D20		
E18		
A21		
AA22		
T16		
P15		
U17		
AB2		
AA3		
Y4		
U6		
T7		
AB4		
AA4		
AB5		
Y5		
Y6		
W6		
R8		
R9		
W8		
V7		
U8		
T8		
V9		
U9		
AB6		
AA6		
Y8		
W9		
AB7		
Y7		
U10		
T10		
AB8		
AA8		
Y10		
W10		
AB9		
Y9		
W11		
V11		
AB10		
AA10		

XC6SLX75T-FGG484

5. I/O Bank 3

U?E			
R7	IO_L1P_3	IO_L83N_VREF_3	B1
P8	IO_L1N_VREF_3	IO_L83P_3	C1
W4	IO_L2P_3	IO_L82N_3	G6
Y3	IO_L2N_3	IO_L82P_3	F5
T6	IO_L7P_3	IO_L81N_3	H8
T5	IO_L7N_3	IO_L81P_3	J7
V5	IO_L8P_3	IO_L80N_3	G7
V3	IO_L8N_3	IO_L80P_3	H6
P5	IO_L9P_3	IO_L60N_3	E4
P4	IO_L9N_3	IO_L60P_3	F3
AA2	IO_L10P_3	IO_L59N_3	D1
AA1	IO_L10N_3	IO_L59P_3	D2
N6	IO_L23P_3	IO_L58N_3	G4
N7	IO_L23N_3	IO_L58P_3	H4
U4	IO_L24P_3	IO_L57N_VREF_3	K8
T4	IO_L24N_3	IO_L57P_3	K7
P6	IO_L25P_3	IO_L55N_M3A14_3	H5
P7	IO_L25N_3	IO_L55P_M3A13_3	J6
T3	IO_L26P_3	IO_L54N_M3A11_3	E1
R4	IO_L26N_3	IO_L54P_M3RESET_3	E3
M7	IO_L31P_3	IO_L53N_M3A12_3	F1
M6	IO_L31N_VREF_3	IO_L53P_M3CKE_3	F2
Y2	IO_L32P_M3DQ14_3	IO_L52N_M3A8_3	G1
Y1	IO_L32N_M3DQ15_3	IO_L52P_M3A8_3	G3
W3	IO_L33P_M3DQ12_3	IO_L51N_M3A4_3	H3
W1	IO_L33N_M3DQ13_3	IO_L51P_M3A10_3	J4
V2	IO_L34P_M3UDQ5_3	IO_L50N_M3BA2_3	H1
V1	IO_L34N_M3UDQ5N_3	IO_L50P_M3WE_3	H2
U3	IO_L35P_M3DQ10_3	IO_L49N_M3A2_3	K5
U1	IO_L35N_M3DQ11_3	IO_L49P_M3A7_3	K6
T2	IO_L36P_M3DQ8_3	IO_L48N_M3BA1_3	J1
T1	IO_L36N_M3DQ9_3	IO_L48P_M3BA0_3	J3
R3	IO_L37P_M3DQ0_3	IO_L47N_M3A1_3	K1
R1	IO_L37N_M3DQ1_3	IO_L47P_M3A0_3	K2
P2	IO_L38P_M3DQ2_3	IO_L46N_M3CLKN_3	K3
P1	IO_L38N_M3DQ3_3	IO_L46P_M3CLK_3	K4
N3	IO_L39P_M3LDQ5_3	IO_L45N_M3DDT_3	L6
N1	IO_L39N_M3LDQ5N_3	IO_L45P_M3A3_3	M6
M2	IO_L40P_M3DQ6_3	IO_L44N_GCLK20_M3A6_3	L4
M1	IO_L40N_M3DQ7_3	IO_L44P_GCLK21_M3A6_3	M3
L3	IO_L41P_GCLK27_M3DQ4	IO_L43N_GCLK22_IRDY2_M3CASN_3	M4
L1	IO_L41N_GCLK26_M3DQ5_3	IO_L43P_GCLK23_M3RASN_3	M5
P3	IO_L42P_GCLK25_TRDY2_M3UDM	IO_L42N_GCLK24_M3LDM_3	N4

XC6SLX75T-FGG484

6. GND

U?F			
A1	GND	GND	W7
A11	GND	GND	W19
A13	GND	GND	W16
A22	GND	GND	V4
A9	GND	GND	V14
AA13	GND	GND	V10
AA17	GND	GND	U7
AA6	GND	GND	U21
AA9	GND	GND	U2
AB1	GND	GND	R5
AB22	GND	GND	R18
B11	GND	GND	P14
B15	GND	GND	P12
B17	GND	GND	P10
B5	GND	GND	N9
B7	GND	GND	N21
C12	GND	GND	N2
C14	GND	GND	
C16	GND	GND	N17
C6	GND	GND	N13
C8	GND	GND	N11
D10	GND	GND	M14
D16	GND	GND	M12
D6	GND	GND	M10
E11	GND	GND	L9
E14	GND	GND	L5
E15	GND	GND	L18
E2	GND	GND	L13
E21	GND	GND	L11
E7	GND	GND	K14
F13	GND	GND	K12
G18	GND	GND	K10
G5	GND	GND	J9
H7	GND	GND	J21
J11	GND	GND	J2
J13	GND	GND	J15

XC6SLX75T-FGG484

7. Power

U?G	
C10	MGTAVCC_101
E10	MGTAVCC_123
D8	MGTAVTRX_101
D14	MGTAVTRX_123
A7	MGTAVTTX_101
A15	MGTAVTTX_123
	VCC0_3
	VCC0_3
	VCC0_3
	VCC0_3
F11	VCCAUX VCC0_3
G12	VCCAUX VCC0_3
H15	VCCAUX VCC0_3
H9	VCCAUX VCC0_3
K15	VCCAUX VCC0_3
L8	VCCAUX VCC0_3
M15	VCCAUX VCC0_3
N8	VCCAUX VCC0_2
R10	VCCAUX VCC0_2
R12	VCCAUX VCC0_2
R6	VCCAUX VCC0_2
U11	VCCAUX VCC0_2
V6	VCCAUX VCC0_2
	VCC0_2
	VCC0_2
J10	VCCINT VCC0_2
J12	VCCINT VCC0_2
J14	VCCINT VCC0_2
J8	VCCINT VCC0_1
K11	VCCINT VCC0_1
K13	VCCINT VCC0_1
K9	VCCINT VCC0_1
L10	VCCINT VCC0_1
L12	VCCINT VCC0_1
L14	VCCINT VCC0_1
M11	VCCINT VCC0_1
M13	VCCINT VCC0_1
M9	VCCINT VCC0_1
N10	VCCINT VCC0_0
N12	VCCINT VCC0_0
N14	VCCINT VCC0_0
P11	VCCINT VCC0_0
P13	VCCINT VCC0_0
P9	VCCINT VCC0_0
R14	VCCINT VCC0_0

XC6SLX75T-FGG484

Notes:

1. The dedicated DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX25T, LX45T, LX75T, LX100T and LX150T. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the UG385, “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 15, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.