

2. I/O Bank 1

U?B

B23	IO_L1P_A25_1	IO_L39N_M1ODT_1	R24
A23	IO_L1N_A24_VREF_1	IO_L39P_M1A3_1	R23
B24	IO_L10P_1	IO_L38N_A4_M1CLKN_1	N18
A25	IO_L10N_1	IO_L38P_A5_M1CLK_1	N17
C25	IO_L11P_1	IO_L37N_A6_M1A1_1	N23
C26	IO_L11N_1	IO_L37P_A7_M1A0_1	N22
B25	IO_L12P_1	IO_L36N_A8_M1BA1_1	N19
B26	IO_L12N_1	IO_L36P_A9_M1BA0_1	M18
E25	IO_L16P_1	IO_L35N_A10_M1A2_1	R26
E26	IO_L16N_1	IO_L35P_A11_M1A7_1	R25
D24	IO_L17P_1	IO_L34N_A12_M1BA2_1	L18
D26	IO_L17N_1	IO_L34P_A13_M1WE_1	M19
F24	IO_L18P_1	IO_L33N_A14_M1A4_1	P26
F26	IO_L18N_1	IO_L33P_A15_M1A10_1	P24
H24	IO_L19P_1	IO_L32N_A16_M1A9_1	K18
H26	IO_L19N_1	IO_L32P_A17_M1A8_1	L17
G25	IO_L20P_1	IO_L31N_A18_M1A12_1	N24
G26	IO_L20N_1	IO_L31P_A19_M1CKE_1	M23
K24	IO_L21P_1	IO_L30N_A20_M1A11_1	N21
K26	IO_L21N_1	IO_L30P_A21_M1RESET_1	P20
J25	IO_L22P_1	IO_L29N_A22_M1A14_1	L24
J26	IO_L22N_1	IO_L29P_A23_M1A13_1	L23
M24	IO_L23P_1	IO_L28N_VREF_1	K19
M26	IO_L23N_1	IO_L28P_1	L19
L25	IO_L24P_1	IO_L25N_1	N26
L26	IO_L24N_1	IO_L25P_1	N25

XC6SLX45-FGG676

U?C

N20	IO_L40P_GCLK11_M1A5_1	IO_L74N_DOUT_BUSY_1	AF24
M21	IO_L40N_GCLK10_M1A6_1	IO_L74P_AWAKE_1	AE23
P21	IO_L41P_GCLK9_IRDY1_M1RASN_1	IO_L73N_1	AA22
P22	IO_L41N_GCLK8_M1CASN_1	IO_L73P_1	Y22
V23	IO_L42P_GCLK7_M1UDM_1	IO_L72N_1	Y21
W24	IO_L42N_GCLK6_TRDY1_M1LDM_1	IO_L72P_1	Y20
U25	IO_L43P_GCLK5_M1DQ4_1	IO_L71N_1	W22
U26	IO_L43N_GCLK4_M1DQ5_1	IO_L71P_1	V22
W25	IO_L44P_A3_M1DQ6_1	IO_L70N_1	V20
W26	IO_L44N_A2_M1DQ7_1	IO_L70P_1	U19
V24	IO_L45P_A1_M1LDQS_1	IO_L62N_1	V17
V26	IO_L45N_A0_M1LDQSN_1	IO_L62P_1	U17
T24	IO_L46P_FCS_B_M1DQ2_1	IO_L68N_1	W19
T26	IO_L46N_FOE_B_M1DQ3_1	IO_L68P_1	W18
Y24	IO_L47P_FWE_B_M1DQ0_1	IO_L67N_1	AF25
Y26	IO_L47N_LDC_M1DQ1_1	IO_L67P_1	AE24
AD24	IO_L48P_HDC_M1DQ8_1	IO_L66N_1	V19
AD26	IO_L48N_M1DQ9_1	IO_L66P_1	V18
AB24	IO_L49P_M1DQ10_1	IO_L65N_1	AC24
AB26	IO_L49N_M1DQ11_1	IO_L65P_1	AC23
AC25	IO_L50P_M1UDQS_1	IO_L64N_1	U20
AC26	IO_L50N_M1UDQSN_1	IO_L64P_1	T20
AA25	IO_L51P_M1DQ12_1	IO_L63N_1	AA24
AA26	IO_L51N_M1DQ13_1	IO_L63P_1	AA23
AE25	IO_L52P_M1DQ14_1	IO_L61N_1	U22
AE26	IO_L52N_M1DQ15_1	IO_L61P_1	U21
T23	IO_L53P_1	IO_L60N_1	T19
U24	IO_L53N_VREF_1	IO_L60P_1	T18
R20	IO_L57P_1	IO_L59N_1	U23
R19	IO_L57N_1	IO_L59P_1	T22

XC6SLX45-FGG676

3. I/O Bank2 (Contains the Programming Interface)

U?D

AC22	CMPCS_B_2	IO_L32N_GCLK28_2	AF12
AF23	DONE_2	IO_L32P_GCLK29_2	AD12
AD22	IO_L1P_CCLK_2	IO_L31N_GCLK30_D15_2	AD13
AF22	IO_L1N_M0_CMPMISO_2	IO_L31P_GCLK31_D14_2	AC13
AE21	IO_L2P_CMPCLK_2	IO_L30N_GCLK0_USERCCLK_2	AF13
AF21	IO_L2N_CMPMOSI_2	IO_L30P_GCLK1_D13_2	AE13
AD20	IO_L3P_D0_DIN_MISO_MISO1_2	IO_L29N_GCLK2_2	AF14
AF20	IO_L3N_MOSI_CSI_B_MISO0_2	IO_L29P_GCLK3_2	AD14
AE19	IO_L4P_2	IO_L28N_2	AA14
AF19	IO_L4N_VREF_2	IO_L28P_2	Y14
AC20	IO_L5P_2	IO_L20N_2	AA15
AD21	IO_L5N_2	IO_L20P_2	Y15
Y18	IO_L6P_2	IO_L19N_2	AC14
AA19	IO_L6N_2	IO_L19P_2	AB15
AC19	IO_L7P_2	IO_L18N_2	W16
AD19	IO_L7N_2	IO_L18P_2	V15
V16	IO_L8P_2	IO_L17N_2	AD17
W17	IO_L8N_2	IO_L17P_2	AC16
AD18	IO_L9P_2	IO_L16N_2	AD15
AF18	IO_L9N_2	IO_L16P_2	AC15
Y16	IO_L10P_2	IO_L15N_2	AC17
AA17	IO_L10N_2	IO_L15P_2	AB17
AA18	IO_L11P_2	IO_L14N_D12_2	AF15
AB18	IO_L11N_2	IO_L14P_D11_2	AE15
AE17	IO_L12P_D1_MISO2_2	IO_L13N_D10_2	AF16
AF17	IO_L12N_D2_MISO3_2	IO_L13P_M1_2	AD16

XC6SLX45-FGG676

U?E

AA13	IO_L34P_2	TDO	A24
AB13	IO_L34N_2	TMS	C23
AA12	IO_L41P_2	TDI	F20
AC12	IO_L41N_VREF_2	TCK	E21
U15	IO_L42P_2		AD23
V14	IO_L42N_2	SUSPEND	
AA11	IO_L43P_2		
AB11	IO_L43N_2		
V13	IO_L44P_2		
W14	IO_L44N_2	PROGRAM_B_2	AF3
AC11	IO_L45P_2	IO_L65N_CSO_B_2	AF4
AD11	IO_L45N_2	IO_L65P_INIT_B_2	AE4
V12	IO_L46P_2	IO_L64N_D9_2	AF5
W12	IO_L46N_2	IO_L64P_D8_2	AE5
AE11	IO_L47P_2	IO_L63N_2	AF6
AF11	IO_L47N_2	IO_L63P_2	AD6
AE9	IO_L48P_D7_2	IO_L62N_D6_2	AF7
AF9	IO_L48N_RDWR_B_VREF_2	IO_L62P_D5_2	AE7
AD10	IO_L49P_D3_2	IO_L53N_2	AF8
AF10	IO_L49N_D4_2	IO_L53P_2	AD8
U13	IO_L50P_2	IO_L58N_2	AD9
U12	IO_L50N_2	IO_L58P_2	AC9
Y10	IO_L51P_2	IO_L52N_2	W11
AB10	IO_L51N_2	IO_L52P_2	V11

XC6SLX45-FGG676

4. I/O Bank 3

U?G		
T3		C3
T1	IO_L41P_GCLK27_M3DQ4_3	C4
V4	IO_L41N_GCLK26_M3DQ5_3	B1
W3	IO_L42P_GCLK25_TRDY2_M3UDM_3	B2
N8	IO_L42N_GCLK24_M3LDM_3	C1
P8	IO_L43P_GCLK23_M3RASN_3	C2
R2	IO_L43N_GCLK22_IRDY2_M3CASN_3	E3
R1	IO_L44P_GCLK21_M3A5_3	E4
P7	IO_L44N_GCLK20_M3A6_3	D1
P6	IO_L45P_M3A3_3	D3
R4	IO_L45N_M3ODT_3	E1
R3	IO_L46P_M3CLK_3	E2
N7	IO_L46N_M3CLKN_3	F1
N6	IO_L47P_M3A0_3	F3
P3	IO_L47N_M3A1_3	G1
P1	IO_L48P_M3BA0_3	G2
P10	IO_L48N_M3BA1_3	H1
R9	IO_L49P_M3A7_3	H3
P5	IO_L49N_M3A2_3	J1
N5	IO_L50P_M3VE_3	J2
M10	IO_L50N_M3BA2_3	K1
N9	IO_L51P_M3A10_3	K3
N4	IO_L51N_M3A4_3	L1
N3	IO_L52P_M3A8_3	L2
M9	IO_L52N_M3A9_3	M1
M8	IO_L53P_M3CKE_3	M3
L4	IO_L53N_M3A12_3	N1
L3	IO_L54P_M3RESET_3	N2
M6	IO_L54N_M3A11_3	L6
M4	IO_L55P_M3A13_3	L7
	IO_L55N_M3A14_3	
	IO_L83N_VREF_3	
	IO_L83P_3	
	IO_L81N_3	
	IO_L81P_3	
	IO_L79N_3	
	IO_L79P_3	
	IO_L77N_3	
	IO_L77P_3	
	IO_L68N_3	
	IO_L68P_3	
	IO_L67N_3	
	IO_L67P_3	
	IO_L66N_3	
	IO_L66P_3	
	IO_L65N_3	
	IO_L65P_3	
	IO_L64N_3	
	IO_L64P_3	
	IO_L63N_3	
	IO_L63P_3	
	IO_L62N_3	
	IO_L62P_3	
	IO_L61N_3	
	IO_L61P_3	
	IO_L60N_3	
	IO_L60P_3	
	IO_L59N_3	
	IO_L59P_3	
	IO_L57N_VREF_3	
	IO_L57P_3	

XC6SLX45-FGG676

5. GND

U?H	
A1	GND
A26	GND
AB12	GND
AB16	GND
AB2	GND
AB20	GND
AB25	GND
AC8	GND
AE10	GND
AE14	GND
AE18	GND
AE22	GND
AE6	GND
AF1	GND
AF26	GND
B13	GND
B17	GND
B21	GND
B5	GND
B9	GND
D4	GND
E11	GND
E15	GND
E22	GND
E7	GND
F19	GND
F2	GND
F25	GND
H11	GND
H23	GND
H4	GND
J19	GND
J8	GND
K16	GND
K2	GND
K25	GND
Y7	GND
Y4	GND
Y23	GND
Y11	GND
W20	GND
W15	GND
V25	GND
V2	GND
U11	GND
T5	GND
T21	GND
T16	GND
T14	GND
T12	GND
R8	GND
R15	GND
R13	GND
R11	GND
P25	GND
P2	GND
P19	GND
P16	GND
P14	GND
P12	GND
N15	GND
N13	GND
N11	GND
M5	GND
M22	GND
M16	GND
M14	GND
M12	GND
L15	GND
L13	GND
L11	GND

XC6SLX45-FGG676

6. Power

U?I

AA10	VCCAUX	VCCINT	U16
AA16	VCCAUX	VCCINT	U10
AA21	VCCAUX	VCCINT	T17
AA6	VCCAUX	VCCINT	T15
F21	VCCAUX	VCCINT	T13
F6	VCCAUX	VCCINT	T11
G12	VCCAUX	VCCINT	R16
G15	VCCAUX	VCCINT	R14
J18	VCCAUX	VCCINT	R12
J9	VCCAUX	VCCINT	P15
K13	VCCAUX	VCCINT	P13
L22	VCCAUX	VCCINT	P11
L5	VCCAUX	VCCINT	N16
M17	VCCAUX	VCCINT	N14
N10	VCCAUX	VCCINT	N12
U14	VCCAUX	VCCINT	M15
U6	VCCAUX	VCCINT	M13
V9	VCCAUX	VCCINT	M11
Y19	VCCAUX	VCCINT	L16
	VCCAUX	VCCINT	L14
		VCCINT	L12
		VCCINT	L10
		VCCINT	K17
		VCCINT	K11

XC6SLX45-FGG676

U?J

B11	VCCO_0	VCCO_3	M2
B15	VCCO_0	VCCO_3	K4
B19	VCCO_0	VCCO_3	J6
B3	VCCO_0	VCCO_3	H2
B7	VCCO_0	VCCO_3	F4
C22	VCCO_0	VCCO_3	D2
D17	VCCO_0	VCCO_3	Y2
D9	VCCO_0	VCCO_3	W6
E13	VCCO_0	VCCO_3	W4
G10	VCCO_0	VCCO_3	T7
G18	VCCO_0	VCCO_3	T2
H14	VCCO_0	VCCO_3	P9
	VCCO_0	VCCO_3	P4
AB23	VCCO_1	VCCO_3	M7
AD25	VCCO_1	VCCO_3	AD2
M20	VCCO_1	VCCO_3	AC6
P23	VCCO_1	VCCO_3	
T25	VCCO_1		Y17
U18	VCCO_1	VCCO_2	Y12
V21	VCCO_1	VCCO_2	AE8
W23	VCCO_1	VCCO_2	AE20
Y25	VCCO_1	VCCO_2	AE16
D25	VCCO_1	VCCO_2	AE12
F23	VCCO_1	VCCO_2	AC21
H25	VCCO_1	VCCO_2	AC18
J21	VCCO_1	VCCO_2	AC10
K23	VCCO_1	VCCO_2	AB14
M25	VCCO_1	VCCO_2	
	VCCO_1		

XC6SLX45-FGG676

7. Not Connected Pins

U?K	
A10	NC
AA20	NC
AA8	NC
AB19	NC
AB8	NC
C10	NC
C12	NC
C16	NC
C24	NC
C8	NC
B10	NC
D10	NC
D11	NC
D12	NC
D13	NC
D15	NC
D16	NC
D19	NC
D20	NC
D22	NC
D23	NC
D5	NC
D7	NC
D8	NC
E10	NC
E12	NC
E14	NC
E16	NC
E17	NC
E18	NC
E19	NC
E20	NC
E23	NC
E24	NC
E5	NC
E6	NC
H12	NC
H10	NC
G9	NC
G8	NC
G7	NC
G6	NC
G5	NC
G4	NC
G3	NC
G24	NC
G23	NC
G22	NC
G21	NC
G20	NC
G19	NC
G17	NC
G16	NC
G14	NC
G13	NC
G11	NC
F9	NC
F8	NC
F7	NC
F5	NC
F22	NC
F18	NC
F17	NC
F16	NC
F15	NC
F14	NC
F13	NC
F12	NC
F11	NC
F10	NC
E9	NC
E8	NC

XC6SLX45-FGG676

U?L			
H13	NC	NC	AB22
H15	NC	NC	AB21
H16	NC	NC	V8
H17	NC	NC	V7
H18	NC	NC	V6
H19	NC	NC	Y9
H20	NC	NC	Y8
H21	NC	NC	Y13
H22	NC	NC	W9
H5	NC	NC	W21
H6	NC	NC	W13
H7	NC	NC	W10
H8	NC	NC	V10
H9	NC	NC	U9
J10	NC	NC	AB9
J11	NC	NC	AA9
J12	NC	NC	T9
J13	NC	NC	T10
J14	NC	NC	R22
J15	NC	NC	R21
J16	NC	NC	R18
J17	NC	NC	R17
J20	NC	NC	R10
J22	NC	NC	P18
J23	NC	NC	P17
J24	NC	NC	L9
J5	NC	NC	L8
J7	NC	NC	J4
K10	NC	NC	J3
K12	NC	NC	L21
K14	NC	NC	L20
K15	NC	NC	K9
K20	NC	NC	K8
K21	NC	NC	K7
K22	NC	NC	K6
K5	NC	NC	

XC6SLX45-FGG676

Notes:

1. The dedicated DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. For details please check the “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 17, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.