

Schematic Symbol for XC6SLX25-FGG484

The symbol consists of 6 heterogeneous parts, each of them listed below:

1. I/O Bank 0

U?A		
A3	IO_L1P_HSWAPEN_0	IO_L66N_SCP0_0
A4	IO_L1N_VREF_0	IO_L66P_SCP1_0
C5	IO_L2P_0	IO_L65N_SCP2_0
A5	IO_L2N_0	IO_L65P_SCP3_0
D6	IO_L3P_0	IO_L64N_SCP4_0
C6	IO_L3N_0	IO_L64P_SCP5_0
B6	IO_L4P_0	IO_L63N_SCP6_0
A6	IO_L4N_0	IO_L63P_SCP7_0
C7	IO_L5P_0	IO_L62N_VREF_0
A7	IO_L5N_0	IO_L62P_0
B8	IO_L6P_0	IO_L51N_0
A8	IO_L6N_0	IO_L51P_0
D9	IO_L7P_0	IO_L50N_0
C8	IO_L7N_0	IO_L50P_0
C9	IO_L8P_0	IO_L49N_0
A9	IO_L8N_VREF_0	IO_L49P_0
E8	NC	IO_L48N_0
F8	NC	IO_L48P_0
G8	NC	IO_L47N_0
F9	NC	IO_L47P_0
G9	NC	IO_L46N_0
H10	NC	IO_L46P_0
E10	NC	IO_L45N_0
F10	NC	IO_L45P_0
G11	NC	IO_L44N_0
H11	NC	IO_L44P_0
D7	IO_L32P_0	IO_L43N_0
D8	IO_L32N_0	IO_L43P_0
D10	IO_L33P_0	IO_L38N_VREF_0
C10	IO_L33N_0	IO_L38P_0
B10	IO_L34P_GCLK19_0	IO_L37N_GCLK12_0
A10	IO_L34N_GCLK18_0	IO_L37P_GCLK13_0
C11	IO_L35P_GCLK17_0	IO_L36N_GCLK14_0
A11	IO_L35N_GCLK16_0	IO_L36P_GCLK15_0

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2. I/O Bank 1

U?B			
C19	IO_L1P_A25_1	IO_L74N_DOUT_BUSY_1	T20
B20	IO_L1N_A24_VREF_1	IO_L74P_AWAKE_1	T19
G16	NC	NC	R19
G17	NC	NC	P18
F16	NC	NC	N16
F17	NC	NC	P17
B21	NC	NC	M18
B22	IO_L19P_1	NC	M17
A20	IO_L19N_1	NC	V20
A21	IO_L20P_1	NC	U19
K16	IO_L20N_1	NC	K18
J16	NC	IO_L61N_1	L17
H16	NC	IO_L61P_1	W22
H17	NC	IO_L60N_1	W20
D19	NC	IO_L60P_1	P20
D20	IO_L29P_A23_M1A13_1	IO_L59N_1	P19
F18	IO_L29N_A22_M1A14_1	IO_L59P_1	L15
F19	IO_L30P_A21_M1RESET_1	NC	M16
D21	IO_L30N_A20_M1A11_1	NC	N19
D22	IO_L31P_A19_M1CKE_1	IO_L53N_VREF_1	M19
C20	IO_L31N_A18_M1A12_1	IO_L53P_1	V22
C22	IO_L32P_A17_M1A8_1	IO_L52N_M1DQ15_1	V21
G19	IO_L32N_A16_M1A9_1	IO_L52P_M1DQ14_1	U22
F20	IO_L33P_A15_M1A10_1	IO_L51N_M1DQ13_1	U20
H19	IO_L33N_A14_M1A4_1	IO_L51P_M1DQ12_1	T22
H18	IO_L34P_A13_M1WE_1	IO_L50N_M1UDQSN_1	T21
E20	IO_L34N_A12_M1BA2_1	IO_L50P_M1UDQS_1	R22
E22	IO_L35P_A11_M1A7_1	IO_L49N_M1DQ11_1	R20
J17	IO_L35N_A10_M1A2_1	IO_L49P_M1DQ10_1	P22
K17	IO_L36P_A9_M1BA0_1	IO_L48N_M1DQ9_1	P21
F21	IO_L36N_A8_M1BA1_1	IO_L48P_HDC_M1DQ8_1	N22
F22	IO_L37P_A7_M1A0_1	IO_L47N_LDC_M1DQ1_1	N20
H20	IO_L37N_A6_M1A1_1	IO_L47P_FWE_B_M1DQ0_1	M22
J19	IO_L38P_A5_M1CLK_1	IO_L46N_FOE_B_M1DQ3_1	M21
G20	IO_L38N_A4_M1CLKN_1	IO_L46P_FCS_B_M1DQ2_1	L22
G22	IO_L39P_M1A3_1	IO_L45N_A0_M1LDQSN_1	L20
K20	IO_L39N_M1ODT_1	IO_L45P_A1_M1LDQS_1	K22
K19	IO_L40P_GCLK11_M1A5_1	IO_L44N_A2_M1DQ7_1	K21
H21	IO_L40N_GCLK10_M1A6_1	IO_L44P_A3_M1DQ6_1	
H22	IO_L41P_GCLK9_IRDY1_M1RASN_1		J22
M20	IO_L41N_GCLK8_M1CASN_1	IO_L43N_GCLK4_M1DQ5_1	J20
L19	IO_L42P_GCLK7_M1UDM_1	IO_L43P_GCLK5_M1DQ4_1	
	IO_L42N_GCLK6_TRDY1_M1LDM_1		

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3. I/O Bank2 (Contains the Programming Interface)

U?C			
Y20	CMPCS_B_2	TDO	A19
Y22	DONE_2	TMS	C18
Y21	IO_L1P_CCLK_2	TDI	E18
AA22	IO_L1N_M0_CMPMISO_2	TCK	G15
AA21	IO_L2P_CMPCLK_2		
AB21	IO_L2N_CMPMOSI_2		
AA20	IO_L3P_D0_DIN_MISO_MISO1_2	NC	P16
AB20	IO_L3N_MOSI_CSI_B_MISO0_2	NC	P15
T18	NC	NC	R17
T17	NC		
Y19	NC		
AB19	IO_L5P_2	SUSPEND	N15
W18	IO_L5N_2	PROGRAM_B_2	AA1
Y18	IO_L6P_2		
T16	IO_L6N_2		
T15	NC	IO_L65N_CSO_B_2	T5
U17	NC	IO_L65P_INIT_B_2	T6
U16	NC	IO_L64N_D9_2	AB2
V19	NC	IO_L64P_D8_2	AA2
V18	NC	IO_L63N_2	V5
R16	NC	IO_L63P_2	U6
R15	NC	IO_L62N_D6_2	Y4
V17	NC	IO_L62P_D5_2	W4
W17	NC	IO_L60N_2	R7
U14	NC	IO_L60P_2	T7
U13	IO_L12P_D1_MISO2_2	IO_L59N_2	R8
U15	IO_L12N_D2_MISO3_2	IO_L59P_2	R9
V15	IO_L13P_M1_2	IO_L58N_2	AB3
AA18	IO_L13N_D10_2	IO_L58P_2	Y3
AB18	IO_L14P_D11_2	IO_L57N_2	AB4
Y17	IO_L14N_D12_2	IO_L57P_2	AA4
AB17	IO_L15P_2	IO_L54N_2	AB5
AA14	IO_L15N_2	IO_L54P_2	Y5
AB14	IO_L16P_2	IO_L53N_2	Y6
Y16	IO_L16N_VREF_2	IO_L53P_2	W6
W15	IO_L17P_2	NC	U10
V13	IO_L17N_2	NC	T10
W13	IO_L18P_2	NC	U8
AA16	IO_L18N_2	NC	T8
AB16	IO_L19P_2	NC	V9
W14	IO_L19N_2	IO_L50N_2	U9
Y14	IO_L20P_2	IO_L50P_2	AB6
Y15	IO_L20N_2	IO_L49N_D4_2	AA6
AB15	IO_L21P_2	IO_L49P_D3_2	AB7
T12	IO_L21N_2	IO_L48N_RDWR_B_VREF_2	Y7
U12	NC	IO_L48P_D7_2	Y8
T14	NC	IO_L47N_2	W9
R13	NC	IO_L47P_2	V7
W12	NC	IO_L46N_2	W8
Y12	IO_L29P_GCLK3_2	IO_L46P_2	AB8
Y13	IO_L29N_GCLK2_2	IO_L45N_2	AA8
AB13	IO_L30P_GCLK1_D13_2	IO_L45P_2	Y10
AA12	IO_L30N_GCLK0_USERCCLK_2	IO_L44N_2	W10
AB12	IO_L31P_GCLK31_D14_2	IO_L44P_2	AB9
Y11	IO_L31N_GCLK30_D15_2	IO_L43N_2	Y9
AB11	IO_L32P_GCLK29_2	IO_L43P_2	W11
R11	IO_L32N_GCLK28_2	IO_L42N_2	V11
T11	IO_L40P_2	IO_L42P_2	AB10
	IO_L40N_2	IO_L41N_VREF_2	AA10
		IO_L41P_2	

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4. I/O Bank 3

U?D		
Y2	IO_L1P_3	IO_L83N_VREF_3
Y1	IO_L1N_VREF_3	IO_L83P_3
W3	IO_L2P_3	NC
W1	IO_L2N_3	NC
P8	NC	NC
P7	NC	NC
P6	NC	NC
P5	NC	NC
T4	NC	NC
T3	IO_L9P_3	IO_L60N_3
U4	IO_L9N_3	IO_L60P_3
V3	IO_L10P_3	IO_L59N_3
N6	IO_L10N_3	IO_L59P_3
N7	NC	NC
M7	NC	NC
M8	NC	NC
R4	NC	NC
P4	NC	IO_L55N_M3A14_3
M6	NC	IO_L55P_M3A13_3
L6	NC	IO_L54N_M3A11_3
P3	NC	IO_L54P_M3RESET_3
N4	NC	IO_L53N_M3A12_3
M5	NC	IO_L53P_M3CKE_3
M4	IO_L31P_3	IO_L52N_M3A9_3
V2	IO_L31N_VREF_3	IO_L52P_M3A8_3
V1	IO_L32P_M3DQ14_3	IO_L51N_M3A4_3
U3	IO_L32N_M3DQ15_3	IO_L51P_M3A10_3
U1	IO_L33P_M3DQ12_3	IO_L50N_M3BA2_3
T2	IO_L33N_M3DQ13_3	IO_L50P_M3WE_3
T1	IO_L34P_M3UDQS_3	IO_L49N_M3A2_3
R3	IO_L34N_M3UDQSN_3	IO_L49P_M3A7_3
R1	IO_L35P_M3DQ10_3	IO_L48N_M3BA1_3
P2	IO_L35N_M3DQ11_3	IO_L48P_M3BA0_3
P1	IO_L36P_M3DQ8_3	IO_L47N_M3A1_3
N3	IO_L36N_M3DQ9_3	IO_L47P_M3A0_3
N1	IO_L37P_M3DQ0_3	IO_L46N_M3CLKN_3
M2	IO_L37N_M3DQ1_3	IO_L46P_M3CLK_3
M1	IO_L38P_M3DQ2_3	IO_L45N_M3ODT_3
L3	IO_L38N_M3DQ3_3	IO_L45P_M3A3_3
L1	IO_L39P_M3LDQS_3	IO_L44N_GCLK20_M3A6_3
K2	IO_L39N_M3LDQSN_3	IO_L44P_GCLK21_M3A5_3
K1	IO_L40P_M3DQ6_3	IO_L43N_GCLK22_IRDY2_M3CASN_3
J3	IO_L40N_M3DQ7_3	IO_L43P_GCLK23_M3RASN_3
J1	IO_L41P_GCLK27_M3DQ4_3	
M3	IO_L41N_GCLK26_M3DQ5_3	
L4	IO_L42P_GCLK25_TRDY2_M3UDM_3	
	IO_L42N_GCLK24_M3LDM_3	
		B3
		A2
		E6
		E5
		C4
		D3
		F7
		G7
		B1
		B2
		H8
		J7
		E4
		D5
		K8
		K7
		F5
		G6
		C1
		C3
		D1
		D2
		E1
		E3
		F3
		G4
		F1
		F2
		H5
		H6
		G1
		G3
		H1
		H2
		H3
		H4
		J6
		K6
		J4
		K3
		K4
		K5

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6. Power

U?F			
D16	VCCAUX	VCCO_3	W2
F11	VCCAUX	VCCO_3	U5
G12	VCCAUX	VCCO_3	R2
H15	VCCAUX	VCCO_3	N5
H9	VCCAUX	VCCO_3	L7
K15	VCCAUX	VCCO_3	L2
L8	VCCAUX	VCCO_3	J5
M15	VCCAUX	VCCO_3	G2
N8	VCCAUX	VCCO_3	F6
R10	VCCAUX	VCCO_3	F4
R12	VCCAUX	VCCO_3	C2
R6	VCCAUX	VCCO_3	
U11	VCCAUX		W5
V6	VCCAUX	VCCO_2	V8
	VCCAUX	VCCO_2	V16
		VCCO_2	V12
J10	VCCINT	VCCO_2	T9
J12	VCCINT	VCCO_2	T13
J14	VCCINT	VCCO_2	AA7
J8	VCCINT	VCCO_2	AA3
K11	VCCINT	VCCO_2	AA19
K13	VCCINT	VCCO_2	AA15
K9	VCCINT	VCCO_2	AA11
L10	VCCINT	VCCO_2	
L12	VCCINT	VCCO_2	
L14	VCCINT		W21
M11	VCCINT	VCCO_1	U18
M13	VCCINT	VCCO_1	R21
M9	VCCINT	VCCO_1	N18
N10	VCCINT	VCCO_1	L21
N12	VCCINT	VCCO_1	L16
N14	VCCINT	VCCO_1	J18
P11	VCCINT	VCCO_1	G21
P13	VCCINT	VCCO_1	E19
P9	VCCINT	VCCO_1	C21
R14	VCCINT	VCCO_1	
	VCCINT		
B11	VCCO_0	VCCO_0	G14
B15	VCCO_0	VCCO_0	G10
B19	VCCO_0	VCCO_0	E9
B4	VCCO_0	VCCO_0	E17
B7	VCCO_0	VCCO_0	E13
	VCCO_0	VCCO_0	

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Notes:

1. The dedicated DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX25, LX45, LX75, LX100 and LX150. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 14, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.