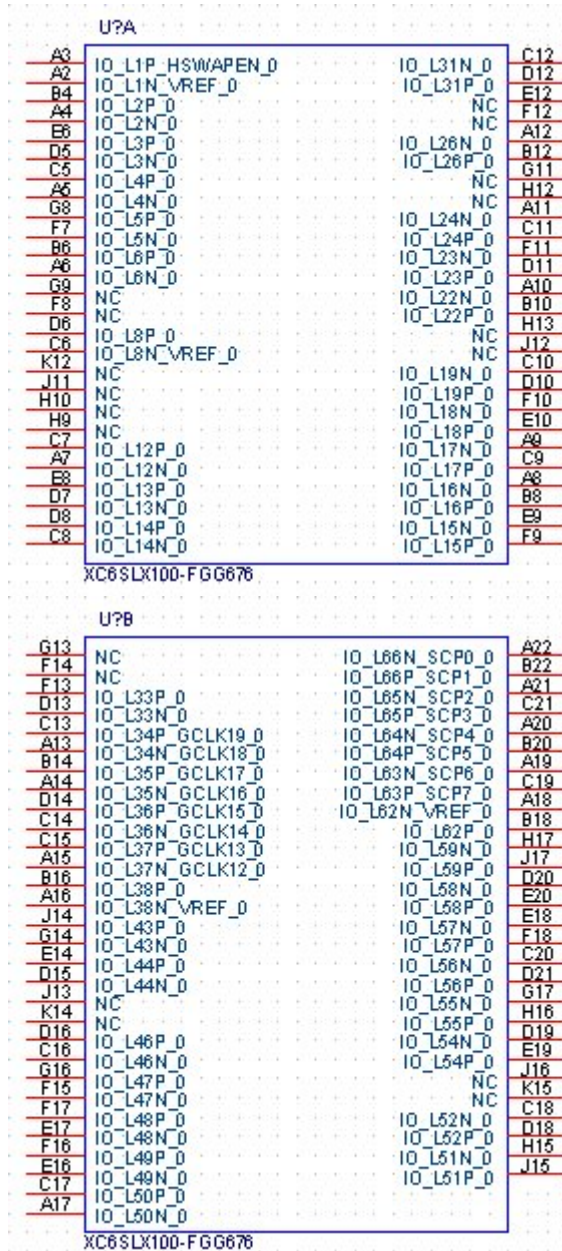


Schematic Symbol for XC6SLX100-FGG676

The symbol consists of 13 heterogeneous parts, each of them listed below:

1. I/O Bank 0



2. I/O Bank 5

U?C			
B23	IO_L1P_A25_5	IO_L27N_5	J20
A23	IO_L1N_A24_VREF_5	IO_L27P_5	H19
G20	IO_L2P_M5A13_5	IO_L26N_VREF_5	L21
G21	IO_L2N_M5A14_5	IO_L26P_5	L20
D23	IO_L3P_M5RESET_5	IO_L25N_M5DQ15_5	N26
C24	IO_L3N_M5A11_5	IO_L25P_M5DQ14_5	N25
F22	IO_L4P_M5CKE_5	IO_L24N_M5DQ13_5	L26
D22	IO_L4N_M5A12_5	IO_L24P_M5DQ12_5	L25
H20	IO_L5P_M5A8_5	IO_L23N_M5UDQSN_5	M26
H21	IO_L5N_M5A9_5	IO_L23P_M5UDQS_5	M24
H22	IO_L6P_M5A10_5	IO_L22N_M5DQ11_5	J26
G22	IO_L6N_M5A4_5	IO_L22P_M5DQ10_5	J25
E23	IO_L7P_M5WE_5	IO_L21N_M5DQ9_5	K26
E24	IO_L7N_M5BA2_5	IO_L21P_M5DQ8_5	K24
G23	IO_L8P_M5A7_5	IO_L20N_M5DQ1_5	G26
G24	IO_L8N_M5A2_5	IO_L20P_M5DQ0_5	G25
H18	IO_L9P_M5BA0_5	IO_L19N_M5DQ3_5	H26
G19	IO_L9N_M5BA1_5	IO_L19P_M5DQ2_5	H24
B24	IO_L10P_M5A0_5	IO_L18N_M5LDQSN_5	F26
A25	IO_L10N_M5A1_5	IO_L18P_M5LDQS_5	F24
C25	IO_L11P_M5CLK_5	IO_L17N_M5DQ7_5	D26
C26	IO_L11N_M5CLKN_5	IO_L17P_M5DQ6_5	D24
B25	IO_L12P_M5A3_5	IO_L16N_M5DQ5_5	E26
B26	IO_L12N_M5ODT_5	IO_L16P_M5DQ4_5	E25
K20	IO_L13P_M5A6_5	IO_L15N_M5LDM_5	J24
K21	IO_L13N_M5A6_5	IO_L15P_M5UDM_5	J23
K22	IO_L14P_M5RASN_5	IO_L14N_M5CASN_5	J22

XC6SLX100-FGG676

3. I/O Bank 1

U?D			
L19	IO_L28P_1	IO_L53N_VREF_1	U24
K19	IO_L28N_VREF_1	IO_L53P_1	T23
L23	IO_L29P_A23_M1A13_1	IO_L52N_M1DQ15_1	AE26
L24	IO_L29N_A22_M1A14_1	IO_L52P_M1DQ14_1	AE25
P20	IO_L30P_A21_M1RESET_1	IO_L51N_M1DQ13_1	AA26
N21	IO_L30N_A20_M1A11_1	IO_L51P_M1DQ12_1	AA25
M23	IO_L31P_A19_M1CKE_1	IO_L50N_M1UDQSN_1	AC26
N24	IO_L31N_A18_M1A12_1	IO_L50P_M1UDQS_1	AC25
L17	IO_L32P_A17_M1A8_1	IO_L49N_M1DQ11_1	AB26
K18	IO_L32N_A16_M1A9_1	IO_L49P_M1DQ10_1	AB24
P24	IO_L33P_A15_M1A10_1	IO_L48N_M1DQ9_1	AD26
P26	IO_L33N_A14_M1A4_1	IO_L48P_HDC_M1DQ8_1	AD24
M19	IO_L34P_A13_M1WE_1	IO_L47N_LDC_M1DQ1_1	Y26
L18	IO_L34N_A12_M1BA2_1	IO_L47P_FWE_B_M1DQ0_1	Y24
R25	IO_L35P_A11_M1A7_1	IO_L46N_FOE_B_M1DQ3_1	T26
B26	IO_L35N_A10_M1A2_1	IO_L46P_FCS_B_M1DQ2_1	T24
M18	IO_L36P_A9_M1BA0_1	IO_L45N_A0_M1LDQSN_1	V26
N19	IO_L36N_A8_M1BA1_1	IO_L45P_A1_M1LDQS_1	V24
N22	IO_L37P_A7_M1A0_1	IO_L44N_A2_M1DQ7_1	W26
N23	IO_L37N_A6_M1A1_1	IO_L44P_A3_M1DQ6_1	W25
N17	IO_L38P_A6_M1CLK_1	IO_L43N_GCLK4_M1DQ5_1	U26
N18	IO_L38N_A4_M1CLKN_1	IO_L43P_GCLK5_M1DQ4_1	U25
R23	IO_L39P_M1A3_1	IO_L42N_GCLK6_TRDY1_M1LDM_1	W24
B24	IO_L39N_M1ODT_1	IO_L42P_GCLK7_M1UDM_1	V23
N20	IO_L40P_GCLK11_M1A6_1	IO_L41N_GCLK8_M1CASN_1	P22
M21	IO_L40N_GCLK10_M1A8_1	IO_L41P_GCLK9_IRDY1_M1RASN_1	P21

XC6SLX100-FGG676

U?E			
R22	IO_L55P_1	IO_L74N_DOUT_BUSY_1	AF24
R21	IO_L55N_1	IO_L74P_AWAKE_1	AE23
P17	IO_L56P_1	IO_L73N_1	AA22
P18	IO_L56N_1	IO_L73P_1	Y22
R20	IO_L57P_1	IO_L72N_1	Y21
R19	IO_L57N_1	IO_L72P_1	Y20
R17	IO_L58P_1	IO_L71N_1	W22
R18	IO_L58N_1	IO_L71P_1	V22
T22	IO_L59P_1	IO_L70N_1	V20
U23	IO_L59N_1	IO_L70P_1	U19
T18	IO_L60P_1	IO_L69N_VREF_1	AB22
T19	IO_L60N_1	IO_L69P_1	AB21
U21	IO_L61P_1	IO_L68N_1	W19
U22	IO_L61N_1	IO_L68P_1	W18
U17	IO_L62P_1	IO_L67N_1	AF25
V17	IO_L62N_1	IO_L67P_1	AE24
AA23	IO_L63P_1	IO_L66N_1	V19
AA24	IO_L63N_1	IO_L66P_1	V18
T20	IO_L64P_1	IO_L65N_1	AC24
U20	IO_L64N_1	IO_L65P_1	AC23

XC6SLX100-FGG676

4. I/O Bank2 (Contains the Programming Interface)

U?F

AC22	CMPCS_B_2	IO_L32N_GCLK28_2	AF12
AF23	DONE_2	IO_L32P_GCLK29_2	AD12
AD22			AD13
AF22	IO_L1P_CCLK_2	IO_L31N_GCLK30_D15_2	AC13
AE21	IO_L1N_M0_CMPMISO0_2	L31P_GCLK31_D14_2	AF13
AE21	IO_L2P_CMPCLK_2	IO_L30N_GCLK0_USERCCLK_2	AF13
AE21	IO_L2N_CMPMOSI_2	IO_L30P_GCLK1_D13_2	AE13
AD20	IO_L3P_D0_DIN_MISO_MISO0_2	IO_L29N_GCLK2_2	AF14
AF20			AD14
AE19	IO_L3N_MOSI_CSI_B_MISO00_2	L29P_GCLK3_2	AA14
AF19	IO_L4P_2	IO_L28N_2	Y14
AC20	IO_L4N_VREF_2	IO_L28P_2	AA15
AD21	IO_L5P_2	IO_L20N_2	Y15
Y18	IO_L5N_2	IO_L20P_2	AC14
AA19	IO_L6P_2	IO_L19N_2	AB15
AC19	IO_L6N_2	IO_L19P_2	W16
AD19	IO_L7P_2	IO_L18N_2	V15
V16	IO_L7N_2	IO_L18P_2	AD17
W17	IO_L8P_2	IO_L17N_2	AC16
AD18	IO_L8N_2	IO_L17P_2	AD15
AF18	IO_L9P_2	IO_L16N_VREF_2	AC15
Y16	IO_L9N_2	IO_L16P_2	AC17
AA17	IO_L10P_2	IO_L15N_2	AB17
AA18	IO_L10N_2	IO_L15P_2	AF15
AB18	IO_L11P_2	IO_L14N_D12_2	AE15
AE17	IO_L11N_2	IO_L14P_D11_2	AF16
AF17	IO_L12P_D1_MISO2_2	IO_L13N_D10_2	AD16
	IO_L12N_D2_MISO3_2	IO_L13P_M1_2	

XC6SLX100-FGG676

U?G

W13	IO_L33P_2	TDO	A24
Y13	IO_L33N_2	TMS	C23
AA13	IO_L34P_2	TDI	F20
AB13	IO_L34N_2	TCK	E21
AA12	IO_L41P_2		
AC12	IO_L41N_VREF_2		
U15	IO_L42P_2		
V14	IO_L42N_2	SUSPEND	AD23
AA11	IO_L43P_2	VBATT	W21
AB11	IO_L43N_2	RFUSE	AA20
V13	IO_L44P_2	VFS	AB19
W14	IO_L44N_2		
AC11	IO_L45P_2		
AD11	IO_L45N_2	PROGRAM_B_2	AF3
V12	IO_L46P_2	IO_L65N_CSO_B_2	AF4
W12	IO_L46N_2	IO_L65P_INIT_B_2	AE4
AE11	IO_L47P_2	IO_L64N_D9_2	AF5
AF11	IO_L47N_2	IO_L64P_D8_2	AE5
AE9	IO_L48P_D7_2	IO_L63N_2	AF6
AF9	IO_L48N_RDWR_B_VREF_2	IO_L63P_2	AD6
AD10	IO_L49P_D3_2	IO_L62N_D6_2	AF7
AF10	IO_L49N_D4_2	IO_L62P_D5_2	AE7
U13	IO_L50P_2	IO_L61N_VREF_2	AB9
U12	IO_L50N_2	IO_L61P_2	AA9
Y10	IO_L51P_2	IO_L58N_2	AD9
AB10	IO_L51N_2	IO_L58P_2	AC9
V11	IO_L52P_2	IO_L53N_2	AF8
W11	IO_L52N_2	IO_L53P_2	AD8

XC6SLX100-FGG676

5. I/O Bank 3

U?H

AC7	IO_L1P_3	IO_L31N_VREF_3	R6
AD7	IO_L1N_VREF_3	IO_L31P_3	R7
AE3	IO_L2P_3	IO_L30N_3	T4
AF2	IO_L2N_3	IO_L30P_3	R5
AC4	IO_L3P_3	IO_L29N_3	T6
AD4	IO_L3N_3	IO_L29P_3	T8
AA8	IO_L4P_3	IO_L28P_3	U3
AB8	IO_L4N_3	IO_L28P_3	U4
AA7	IO_L7P_3	IO_L27N_3	T9
Y6	IO_L7N_3	IO_L27P_3	R10
AB7	IO_L8P_3	IO_L25N_3	U9
AB6	IO_L8N_3	IO_L25P_3	T10
Y9	IO_L9P_3	IO_L24N_3	V5
Y8	IO_L9N_3	IO_L24P_3	U5
AC5	IO_L10P_3	IO_L23N_3	U7
AD5	IO_L10N_3	IO_L23P_3	U8
W8	IO_L15P_3	IO_L22P_3	Y5
W7	IO_L15N_3	IO_L22P_3	W5
AA6	IO_L16P_3	IO_L21N_3	V10
AB5	IO_L16P_3	IO_L21N_3	W10
V7	IO_L16N_3	IO_L21P_3	AA3
V6	IO_L17P_3	IO_L20N_3	AA4
AB4	IO_L17N_VREF_3	IO_L20P_3	V8
AC3	IO_L18P_3	IO_L19N_3	W9
	IO_L18N_3	IO_L19P_3	

XC6SLX100-FGG676

U?I

AB3	IO_L32P_M3DQ14_3	IO_L57N_VREF_3	L6
AB1	IO_L32N_M3DQ15_3	IO_L57P_3	L7
AD3	IO_L33P_M3DQ12_3	IO_L55N_M3A14_3	M4
AD1	IO_L33N_M3DQ13_3	IO_L55P_M3A13_3	M6
AC2	IO_L34P_M3UDQ5_3	IO_L54N_M3A11_3	L3
AC1	IO_L34N_M3UDQ5N_3	IO_L54P_M3RESET_3	L4
AE2	IO_L35P_M3DQ10_3	IO_L53N_M3A12_3	M8
AE1	IO_L35N_M3DQ11_3	IO_L53P_M3CKE_3	M9
AA2	IO_L36P_M3DQ8_3	IO_L52N_M3A9_3	N3
AA1	IO_L36N_M3DQ9_3	IO_L52P_M3A8_3	N4
Y3	IO_L37P_M3DQ0_3	IO_L51N_M3A4_3	N9
Y1	IO_L37N_M3DQ1_3	IO_L51P_M3A10_3	M10
W2	IO_L38P_M3DQ2_3	IO_L50N_M3BA2_3	N5
W1	IO_L38N_M3DQ3_3	IO_L50P_M3WE_3	P5
V3	IO_L39P_M3LDQ5_3	IO_L49N_M3A2_3	R9
V1	IO_L39N_M3LDQ5N_3	IO_L49P_M3A7_3	P10
U2	IO_L40P_M3DQ6_3	IO_L48N_M3BA1_3	P1
U1	IO_L40N_M3DQ7_3	IO_L48P_M3BA0_3	P3
T3	IO_L41P_GCLK27_M3DQ4_3	IO_L47N_M3A1_3	N6
T1	IO_L41N_GCLK26_M3DQ5_3	IO_L47P_M3A0_3	N7
V4	IO_L42P_GCLK25_TRDY2_M3UDM10_3	IO_L46N_M3CLKN_3	R3
W3	IO_L42N_GCLK24_M3LDM3_3	IO_L46P_M3CLK_3	R4
N8	IO_L43P_GCLK23_M3RASN_3	IO_L45N_M3ODT_3	P6
P8	IO_L43N_GCLK22_IRDY2_M3CASN_3	IO_L45P_M3A3_3	P7
R2	IO_L44P_GCLK21_M3A6_3		
R1	IO_L44N_GCLK20_M3A6_3		

XC6SLX100-FGG676

6. I/O Bank 4

U?J			
K8	IO_L58P_4	IO_L83N_VREF_4	C3
L8	IO_L58N_VREF_4	IO_L83P_4	C4
N2	IO_L59P_M4DQ14_4	IO_L82N_M4A14_4	G7
N1	IO_L59N_M4DQ15_4	IO_L82P_M4A13_4	H8
M3	IO_L80P_M4DQ12_4	IO_L81N_M4A11_4	B1
M1	IO_L80N_M4DQ13_4	IO_L81P_M4RESET_4	B2
L2	IO_L81P_M4UDQS_4	IO_L80N_M4A12_4	E5
L1	IO_L81N_M4UDQSN_4	IO_L80P_M4CKE_4	F5
K3	IO_L82P_M4DQ10_4	IO_L79N_M4A8_4	C1
K1	IO_L82N_M4DQ11_4	IO_L79P_M4A8_4	C2
J2	IO_L83P_M4DQ8_4	IO_L78N_M4A4_4	G5
J1	IO_L83N_M4DQ9_4	IO_L78P_M4A10_4	G6
H3	IO_L84P_M4DQ0_4	IO_L77N_M4B2_4	E3
H1	IO_L84N_M4DQ1_4	IO_L77P_M4WE_4	E4
G2	IO_L85P_M4DQ2_4	IO_L76N_M4A2_4	K9
G1	IO_L85N_M4DQ3_4	IO_L76P_M4A7_4	J10
F3	IO_L86P_M4LDQS_4	IO_L75N_M4B1_4	H5
F1	IO_L86N_M4LDQSN_4	IO_L75P_M4B1_4	H6
E2	IO_L87P_M4DQ6_4	IO_L74N_M4A1_4	L9
E1	IO_L87N_M4DQ7_4	IO_L74P_M4A0_4	K10
D3	IO_L88P_M4DQ4_4	IO_L73N_M4CLKN_4	G3
D1	IO_L88N_M4DQ5_4	IO_L73P_M4CLK_4	G4
J4	IO_L89P_M4UDM_4	IO_L72N_M4ODT_4	H7
J3	IO_L89N_M4LDM_4	IO_L72P_M4A3_4	J7
K7	IO_L70P_M4RASN_4	IO_L71N_M4A6_4	J5
K6	IO_L70N_M4CASN_4	IO_L71P_M4A6_4	K5

XC6SLX100-FGG676

7. GND

U?K			
A1	GND	GND	Y7
A26	GND	GND	Y4
AB12	GND	GND	Y23
AB16	GND	GND	Y11
AB2	GND	GND	W20
AB20	GND	GND	W15
AB25	GND	GND	V25
AC8	GND	GND	V2
AE10	GND	GND	U11
AE14	GND	GND	T5
AE18	GND	GND	T21
AE22	GND	GND	T16
AE6	GND	GND	T14
AF1	GND	GND	T12
AF26	GND	GND	R8
B13	GND	GND	R15
B17	GND	GND	R13
B21	GND	GND	R11
B5	GND	GND	P25
B9	GND	GND	P2
D4	GND	GND	P19
E11	GND	GND	P16
E15	GND	GND	P14
E22	GND	GND	P12
E7	GND	GND	N15
F19	GND	GND	N13
F2	GND	GND	N11
F25	GND	GND	M5
H11	GND	GND	M22
H23	GND	GND	M16
H4	GND	GND	M14
J19	GND	GND	M12
J8	GND	GND	L15
K16	GND	GND	L13
K2	GND	GND	L11
K25	GND	GND	

XC6SLX100-FGG676

8. Power

U?L			
AA10	VCCAUX	VCCINT	U16
AA16	VCCAUX	VCCINT	U10
AA21	VCCAUX	VCCINT	T17
A96	VCCAUX	VCCINT	T15
F21	VCCAUX	VCCINT	T13
F6	VCCAUX	VCCINT	T11
G12	VCCAUX	VCCINT	R16
G15	VCCAUX	VCCINT	R14
J18	VCCAUX	VCCINT	R12
J9	VCCAUX	VCCINT	P15
K13	VCCAUX	VCCINT	P13
L22	VCCAUX	VCCINT	P11
L5	VCCAUX	VCCINT	N16
M17	VCCAUX	VCCINT	N14
N10	VCCAUX	VCCINT	N12
U14	VCCAUX	VCCINT	M15
U6	VCCAUX	VCCINT	M13
V9	VCCAUX	VCCINT	M11
Y19	VCCAUX	VCCINT	L16
		VCCINT	L14
		VCCINT	L12
		VCCINT	L10
		VCCINT	K17
		VCCINT	K11

XC6SLX100-FGG676

U?M			
B11	VCC0_0	VCC0_5	M25
B15	VCC0_0	VCC0_5	K23
B19	VCC0_0	VCC0_5	J21
B3	VCC0_0	VCC0_5	H25
B7	VCC0_0	VCC0_5	F23
C22	VCC0_0	VCC0_5	D25
D17	VCC0_0	VCC0_5	
D9	VCC0_0		M2
E13	VCC0_0	VCC0_4	K4
G10	VCC0_0	VCC0_4	J6
G18	VCC0_0	VCC0_4	H2
H14	VCC0_0	VCC0_4	F4
		VCC0_4	D2
AB23	VCC0_1		Y2
AD25	VCC0_1		W6
M20	VCC0_1	VCC0_3	W4
P23	VCC0_1	VCC0_3	T7
T25	VCC0_1	VCC0_3	T2
U18	VCC0_1	VCC0_3	P9
V21	VCC0_1	VCC0_3	P4
W23	VCC0_1	VCC0_3	M7
Y25	VCC0_1	VCC0_3	AD2
		VCC0_3	AC6
AB14	VCC0_2		
AC10	VCC0_2		
AC18	VCC0_2		
AC21	VCC0_2		
AE12	VCC0_2		
AE16	VCC0_2		
AE20	VCC0_2		
AE8	VCC0_2		
Y12	VCC0_2		
Y17	VCC0_2		

XC6SLX100-FGG676

Notes:

1. The dedicated DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX25, LX45, LX75, LX100 and LX150. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the “Spartan 6 Packaging and Pinouts” User Guide that can be found at:
http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 16, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.