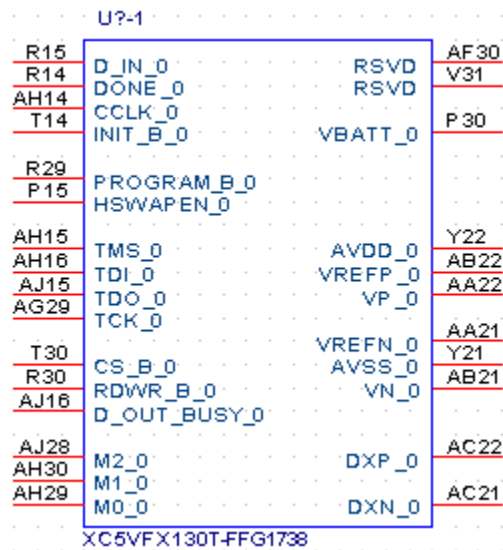


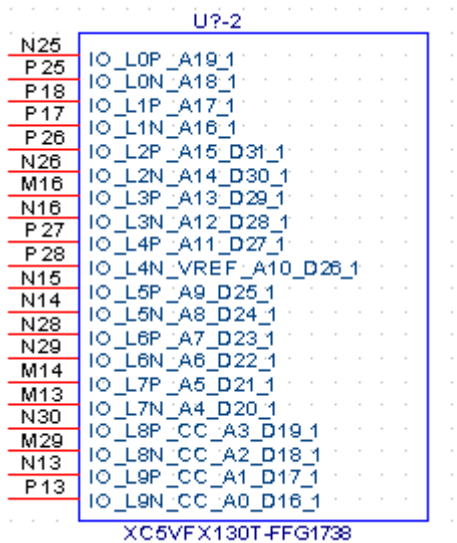
Schematic Symbol for XC5VFX130T-FFG1738

The symbol consists of 42 heterogeneous parts, each of them listed below:

1. Programming Interface



2. I/O Bank 1



3. I/O Bank 2

U?-3

AK12	IO_L0P_CC_RS1_2
AK13	IO_L0N_CC_RS0_2
AJ30	IO_L1P_CC_A25_2
AK30	IO_L1N_CC_A24_2
AK15	IO_L2P_A23_2
AK14	IO_L2N_A22_2
AL30	IO_L3P_A21_2
AM29	IO_L3N_A20_2
AL14	IO_L4P_FCS_B_2
AM13	IO_L4N_VREF_FOE_B_MOSI_2
AM28	IO_L5P_FWE_B_2
AL29	IO_L5N_CSO_B_2
AN13	IO_L6P_D7_2
AP13	IO_L6N_D6_2
AK28	IO_L7P_D5_2
AK29	IO_L7N_D4_2
AN14	IO_L8P_D3_2
AM14	IO_L8N_D2_FS2_2
AK27	IO_L9P_D1_FS1_2
AJ26	IO_L9N_D0_FS0_2

XC5VFX130T-FFG1738

4. I/O Bank 3

U?-4

J16	IO_L0P_CC_GC_3
J15	IO_L0N_CC_GC_3
M26	IO_L1P_CC_GC_3
L27	IO_L1N_CC_GC_3
J17	IO_L2P_GC_VRN_3
K17	IO_L2N_GC_VRP_3
M27	IO_L3P_GC_3
M28	IO_L3N_GC_3
L17	IO_L4P_GC_3
M17	IO_L4N_GC_VREF_3
L29	IO_L5P_GC_3
K28	IO_L5N_GC_3
L16	IO_L6P_GC_3
L15	IO_L6N_GC_3
K29	IO_L7P_GC_3
J30	IO_L7N_GC_3
L14	IO_L8P_GC_3
K15	IO_L8N_GC_3
K30	IO_L9P_GC_3
L30	IO_L9N_GC_3

XC5VFX130T-FFG1738

5. I/O Bank 4

U?-5	
AN30	IO_L0P_GC_D15_4
AP 30	IO_L0N_GC_D14_4
AK17	IO_L1P_GC_D13_4
AL17	IO_L1N_GC_D12_4
AN29	IO_L2P_GC_D11_4
AP 28	IO_L2N_GC_D10_4
AL15	IO_L3P_GC_D9_4
AL16	IO_L3N_GC_D8_4
AP 27	IO_L4P_GC_4
AN28	IO_L4N_GC_VREF_4
AM16	IO_L5P_GC_4
AM17	IO_L5N_GC_4
AM27	IO_L6P_GC_4
AM26	IO_L6N_GC_4
AN15	IO_L7P_GC_VRN_4
AN16	IO_L7N_GC_VRP_4
AL27	IO_L8P_CC_GC_4
AL26	IO_L8N_CC_GC_4
AP 16	IO_L9P_CC_GC_4
AP 15	IO_L9N_CC_GC_4

XC5VFX130T-FFG1738

6. I/O Bank 5

U?-6			
L24	IO_L0P_5	IO_L19N_5	H30
M24	IO_L0N_5	IO_L19P_5	H29
E18	IO_L1P_5	IO_L18N_5	L19
E17	IO_L1N_5	IO_L18P_5	L20
K24	IO_L2P_5	IO_L17N_5	F29
L25	IO_L2N_5	IO_L17P_5	G29
F16	IO_L3P_5	IO_L16N_5	N18
F17	IO_L3N_5	IO_L16P_5	M18
K25	IO_L4P_5	IO_L15N_5	H28
J25	IO_L4N_VREF_5	IO_L15P_5	G28
G16	IO_L5P_5	IO_L14N_VREF_5	N19
H16	IO_L5N_5	IO_L14P_5	M19
H26	IO_L6P_5	IO_L13N_5	F27
J26	IO_L6N_5	IO_L13P_5	G27
G18	IO_L7P_5	IO_L12N_VRP_5	P20
G17	IO_L7N_5	IO_L12P_VRN_5	N20
J28	IO_L8P_CC_5	IO_L11N_CC_5	L26
J27	IO_L8N_CC_5	IO_L11P_CC_5	K27
J18	IO_L9P_CC_5	IO_L10N_CC_5	K19
H18	IO_L9N_CC_5	IO_L10P_CC_5	K18

XC5VFX130T-FFG1738

7. I/O Bank 6

U?-7			
AR29	IO_L0P_6	IO_L19N_6	AL24
AR28	IO_L0N_6	IO_L19P_6	AL25
AT14	IO_L1P_6	IO_L18N_6	AL19
AR14	IO_L1N_6	IO_L18P_6	AM19
AR30	IO_L2P_6	IO_L17N_6	AK25
AT30	IO_L2N_6	IO_L17P_6	AK24
AT15	IO_L3P_6	IO_L16N_6	AK19
AR15	IO_L3N_6	IO_L16P_6	AK18
AT29	IO_L4P_6	IO_L15N_6	AN24
AU29	IO_L4N_VREF_6	IO_L15P_6	AM24
AT17	IO_L5P_6	IO_L14N_VREF_6	AR19
AT16	IO_L5N_6	IO_L14P_6	AT19
AU28	IO_L6P_6	IO_L13N_6	AP 25
AT27	IO_L6N_6	IO_L13P_6	AN25
AP 17	IO_L7P_6	IO_L12N_VRP_6	AP 18
AR17	IO_L7N_6	IO_L12P_VRN_6	AR18
AT26	IO_L8P_CC_6	IO_L11N_CC_6	AP 26
AR27	IO_L8N_CC_6	IO_L11P_CC_6	AN26
AN20	IO_L9P_CC_6	IO_L10N_CC_6	AM18
AN19	IO_L9N_CC_6	IO_L10P_CC_6	AN18

XC5VFX130T-FFG1738

8. I/O Bank 7

U?-8			
M23	IO_L0P_7	IO_L19N_7	G26
M22	IO_L0N_7	IO_L19P_7	H25
M21	IO_L1P_7	IO_L18N_7	H20
L21	IO_L1N_7	IO_L18P_7	H19
N24	IO_L2P_7	IO_L17N_7	F25
N23	IO_L2N_7	IO_L17P_7	F26
N22	IO_L3P_7	IO_L16N_7	G19
N21	IO_L3N_7	IO_L16P_7	F19
H21	IO_L4P_7	IO_L15N_7	H24
J21	IO_L4N_VREF_7	IO_L15P_7	G24
J20	IO_L5P_7	IO_L14N_VREF_7	F20
K20	IO_L5N_7	IO_L14P_7	E 20
L22	IO_L6P_7	IO_L13N_7	H23
K23	IO_L6N_7	IO_L13P_7	J23
K22	IO_L7P_7	IO_L12N_VRP_7	D18
J22	IO_L7N_7	IO_L12P_VRN_7	E 19
F22	IO_L8P_CC_7	IO_L11N_CC_7	G23
G22	IO_L8N_CC_7	IO_L11P_CC_7	F24
G21	IO_L9P_CC_7	IO_L10N_CC_7	E 23
F21	IO_L9N_CC_7	IO_L10P_CC_7	E 22

XC5VFX130T-FFG1738

9. I/O Bank 8

U?-9			
AL21	IO_L0P_8	IO_L19N_8	AM21
AL22	IO_L0N_8	IO_L19P_8	AN21
AK23	IO_L1P_8	IO_L18N_8	AP 21
AK22	IO_L1N_8	IO_L18P_8	AP 20
AJ22	IO_L2P_8	IO_L17N_8	AP 23
AJ21	IO_L2N_8	IO_L17P_8	AN23
AK20	IO_L3P_8	IO_L16N_8	AM23
AL20	IO_L3N_8	IO_L16P_8	AM22
AU26	IO_L4P_8	IO_L15N_8	AT22
AU27	IO_L4N_VREF_8	IO_L15P_8	AR22
AU19	IO_L5P_8	IO_L14N_VREF_8	AP 22
AU18	IO_L5N_8	IO_L14P_8	AR23
AR25	IO_L6P_8	IO_L13N_8	AV24
AT25	IO_L6N_8	IO_L13P_8	AV23
AR20	IO_L7P_8	IO_L12N_VRP_8	AU22
AT20	IO_L7N_8	IO_L12P_VRN_8	AU23
AT24	IO_L8P_CC_8	IO_L11N_CC_8	AV25
AR24	IO_L8N_CC_8	IO_L11P_CC_8	AU24
AU21	IO_L9P_CC_8	IO_L10N_CC_8	AV20
AT21	IO_L9N_CC_8	IO_L10P_CC_8	AV21

XC5VFX130T-FFG1738

10. I/O Bank 11

U?-10			
F42	IO_L0P_11	IO_L19N_SM9N_11	AA41
G42	IO_L0N_11	IO_L19P_SM9P_11	AA42
F41	IO_L1P_11	IO_L18N_SM10N_11	Y42
G41	IO_L1N_11	IO_L18P_SM10P_11	W42
H41	IO_L2P_11	IO_L17N_SM11N_11	W41
J41	IO_L2N_11	IO_L17P_SM11P_11	V40
J42	IO_L3P_11	IO_L16N_SM12N_11	V41
K42	IO_L3N_11	IO_L16P_SM12P_11	U42
L40	IO_L4P_11	IO_L15N_SM13N_11	U41
L41	IO_L4N_VREF_11	IO_L15P_SM13P_11	T42
L42	IO_L5P_11	IO_L14N_VREF_11	T41
M41	IO_L5N_11	IO_L14P_11	T40
M42	IO_L6P_11	IO_L13N_11	R40
N41	IO_L6N_11	IO_L13P_11	P41
N40	IO_L7P_11	IO_L12N_VRP_11	P42
P40	IO_L7N_11	IO_L12P_VRN_11	R42
W40	IO_L8P_CC_11	IO_L11N_CC_SM14N_11	AA37
Y40	IO_L8N_CC_11	IO_L11P_CC_SM14P_11	Y37
AA40	IO_L9P_CC_11	IO_L10N_CC_SM15N_11	Y38
AA39	IO_L9N_CC_11	IO_L10P_CC_SM15P_11	Y39

XC5VFX130T-FFG1738

11. I/O Bank 12

U?-11			
AA7			Y8
AA6	IO_L0P_12	IO_L19N_12	Y9
GB	IO_L0N_12	IO_L19P_12	W10
H5	IO_L1P_12	IO_L18N_12	W11
WV5	IO_L1N_12	IO_L18P_12	Y10
WV6	IO_L2P_12	IO_L17N_12	AA9
H6	IO_L2N_12	IO_L17P_12	AA10
J5	IO_L3P_12	IO_L16N_12	AA11
Y7	IO_L3N_12	IO_L16P_12	T4
WV7	IO_L4P_12	IO_L15N_12	T5
J6	IO_L4N_VREF_12	IO_L15P_12	R5
K5	IO_L5P_12	IO_L14N_VREF_12	R4
WV8	IO_L5N_12	IO_L14P_12	T6
V8	IO_L6P_12	IO_L13N_12	T7
K4	IO_L6N_12	IO_L13P_12	P6
L5	IO_L7P_12	IO_L12N_VRP_12	P5
V5	IO_L7N_12	IO_L12P_VRN_12	U6
V6	IO_L8P_CC_12	IO_L11N_CC_12	U7
L6	IO_L8N_CC_12	IO_L11P_CC_12	N6
M6	IO_L9P_CC_12	IO_L10N_CC_12	N5
	IO_L9N_CC_12	IO_L10P_CC_12	

XC5VFX130T-FFG1738

12. I/O Bank 13

U?-12			
AB41	IO_L0P_SM8P_13	IO_L19N_13	AV41
AB42	IO_L0N_SM8N_13	IO_L19P_13	AU42
AC41	IO_L1P_SM7P_13	IO_L18N_13	AU41
AD42	IO_L1N_SM7N_13	IO_L18P_13	AT41
AE42	IO_L2P_SM6P_13	IO_L17N_13	AT42
AD41	IO_L2N_SM6N_13	IO_L17P_13	AR42
AF41	IO_L3P_SM5P_13	IO_L16N_13	AP 41
AF42	IO_L3N_SM5N_13	IO_L16P_13	AP 42
AF40	IO_L4P_13	IO_L15N_13	AN41
AG41	IO_L4N_VREF_13	IO_L15P_13	AM41
AG42	IO_L5P_SM4P_13	IO_L14N_VREF_13	AM42
AH41	IO_L5N_SM4N_13	IO_L14P_13	AL42
AJ42	IO_L6P_SM3P_13	IO_L13N_13	AK42
AJ41	IO_L6N_SM3N_13	IO_L13P_13	AL41
AH40	IO_L7P_SM2P_13	IO_L12N_VRP_13	AL40
AJ40	IO_L7N_SM2N_13	IO_L12P_VRN_13	AK40
AB37	IO_L8P_CC_SM1P_13	IO_L11N_CC_13	AC39
AB38	IO_L8N_CC_SM1N_13	IO_L11P_CC_13	AC40
AB39	IO_L9P_CC_SM0P_13	IO_L10N_CC_13	AD40
AC38	IO_L9N_CC_SM0N_13	IO_L10P_CC_13	AE 40

XC5VFX130T-FFG1738

13. I/O Bank 15

U?-13			
H38			WV37
H39	IO_L0P_15	IO_L19N_15	WV36
G38	IO_L0N_15	IO_L19P_15	WV35
G39	IO_L1P_15	IO_L18N_15	Y35
F39	IO_L1N_15	IO_L18P_15	Y34
F40	IO_L2P_15	IO_L17N_15	AA34
E39	IO_L2N_15	IO_L17P_15	AA36
E40	IO_L3P_15	IO_L16N_15	AA35
R39	IO_L3N_15	IO_L16P_15	WV38
R38	IO_L4P_15	IO_L15N_15	V39
R37	IO_L4N_VREF_15	IO_L15P_15	U39
P37	IO_L5P_15	IO_L14N_VREF_15	T39
P38	IO_L5N_15	IO_L14P_15	U38
N38	IO_L6P_15	IO_L13N_15	T37
N39	IO_L6N_15	IO_L13P_15	U37
M39	IO_L7P_15	IO_L12N_VRP_15	V38
M38	IO_L7N_15	IO_L12P_VRN_15	K39
L39	IO_L8P_CC_15	IO_L11N_CC_15	K40
K38	IO_L8N_CC_15	IO_L11P_CC_15	J40
J38	IO_L9P_CC_15	IO_L10N_CC_15	H40
	IO_L9N_CC_15	IO_L10P_CC_15	

XC5VFX130T-FFG1738

14. I/O Bank 17

U?-14			
AB34			AL37
AC34	IO_L0P_17	IO_L19N_17	AM37
AC35	IO_L0N_17	IO_L19P_17	AM38
AB36	IO_L1P_17	IO_L18N_17	AN38
AC36	IO_L1N_17	IO_L18P_17	AP 38
AD35	IO_L2P_17	IO_L17N_17	AN39
AD36	IO_L2N_17	IO_L17P_17	AM39
AD37	IO_L3P_17	IO_L16N_17	AL39
AE 37	IO_L3N_17	IO_L16P_17	AH38
AD38	IO_L4P_17	IO_L15N_17	AJ37
AE 39	IO_L4N_VREF_17	IO_L15P_17	AK37
AE 38	IO_L5P_17	IO_L14N_VREF_17	AK38
AF39	IO_L5N_17	IO_L14P_17	AK39
AG38	IO_L6P_17	IO_L13N_17	AJ38
AG37	IO_L6N_17	IO_L13P_17	AH39
AF37	IO_L7P_17	IO_L12N_VRP_17	AG39
AN40	IO_L7N_17	IO_L12P_VRN_17	AR39
AP 40	IO_L8P_CC_17	IO_L11N_CC_17	AT39
AR40	IO_L8N_CC_17	IO_L11P_CC_17	AU39
AT40	IO_L9P_CC_17	IO_L10N_CC_17	AV40
	IO_L9N_CC_17	IO_L10P_CC_17	

XC5VFX130T-FFG1738

15. I/O Bank 18

U?-15			
AJ7	IO_L0P_18	IO_L19N_18	AF12
AK7	IO_L0N_18	IO_L19P_18	AF11
AB11	IO_L1P_18	IO_L18N_18	AE10
AC10	IO_L1N_18	IO_L18P_18	AE9
AL5	IO_L2P_18	IO_L17N_18	AF10
AK5	IO_L2N_18	IO_L17P_18	AF9
AB9	IO_L3P_18	IO_L16N_18	AE8
AB8	IO_L3N_18	IO_L16P_18	AD8
AJ6	IO_L4P_18	IO_L15N_18	AE7
AJ5	IO_L4N_VREF_18	IO_L15P_18	AF7
AC8	IO_L5P_18	IO_L14N_VREF_18	AD5
AC9	IO_L5N_18	IO_L14P_18	AE5
AH6	IO_L6P_18	IO_L13N_18	AG7
AH5	IO_L6N_18	IO_L13P_18	AG6
AD10	IO_L7P_18	IO_L12N_VRP_18	AD7
AD11	IO_L7N_18	IO_L12P_VRN_18	AD6
AG4	IO_L8P_CC_18	IO_L11N_CC_18	AF6
AH4	IO_L8N_CC_18	IO_L11P_CC_18	AF5
AB7	IO_L9P_CC_18	IO_L10N_CC_18	AC6
AB6	IO_L9N_CC_18	IO_L10P_CC_18	AC5

XC5VFX130T-FFG1738

16. I/O Bank 19

U?-16			
R34	IO_L0P_19	IO_L19N_19	AA32
P35	IO_L0N_19	IO_L19P_19	Y32
N35	IO_L1P_19	IO_L18N_19	W32
M36	IO_L1N_19	IO_L18P_19	Y33
L37	IO_L2P_19	IO_L17N_19	W33
M37	IO_L2N_19	IO_L17P_19	V33
N36	IO_L3P_19	IO_L16N_19	V34
P36	IO_L3N_19	IO_L16P_19	V35
L36	IO_L4P_19	IO_L15N_19	D37
L35	IO_L4N_VREF_19	IO_L15P_19	E38
K35	IO_L5P_19	IO_L14N_VREF_19	E37
J35	IO_L5N_19	IO_L14P_19	F37
H35	IO_L6P_19	IO_L13N_19	G36
J36	IO_L6N_19	IO_L13P_19	F36
K37	IO_L7P_19	IO_L12N_VRP_19	G37
J37	IO_L7N_19	IO_L12P_VRN_19	H36
U34	IO_L8P_CC_19	IO_L11N_CC_19	V36
T35	IO_L8N_CC_19	IO_L11P_CC_19	U36
T34	IO_L9P_CC_19	IO_L10N_CC_19	T36
U33	IO_L9N_CC_19	IO_L10P_CC_19	R35

XC5VFX130T-FFG1738

17. I/O Bank 20

U?-17			
N9	IO_L0P_20	IO_L19N_20	L9
N8	IO_L0N_20	IO_L19P_20	M9
E9	IO_L1P_20	IO_L18N_20	M8
E8	IO_L1N_20	IO_L18P_20	M7
P7	IO_L2P_20	IO_L17N_20	L7
P8	IO_L2N_20	IO_L17P_20	K7
D7	IO_L3P_20	IO_L16N_20	K9
E7	IO_L3N_20	IO_L16P_20	K8
R7	IO_L4P_20	IO_L15N_20	V11
R8	IO_L4N_VREF_20	IO_L15P_20	U11
F7	IO_L5P_20	IO_L14N_VREF_20	J7
F6	IO_L5N_20	IO_L14P_20	J8
R9	IO_L6P_20	IO_L13N_20	T11
T9	IO_L6N_20	IO_L13P_20	T10
E5	IO_L7P_20	IO_L12N_VRP_20	H9
F5	IO_L7N_20	IO_L12P_VRN_20	H8
V9	IO_L8P_CC_20	IO_L11N_CC_20	U9
V10	IO_L8N_CC_20	IO_L11P_CC_20	U8
F9	IO_L9P_CC_20	IO_L10N_CC_20	G8
G9	IO_L9N_CC_20	IO_L10P_CC_20	G7

XC5VFX130T-FFG1738

18. I/O Bank

U?-18			
AB33	IO_L0P_21	IO_L19N_21	AK34
AB32	IO_L0N_21	IO_L19P_21	AL34
AC33	IO_L1P_21	IO_L18N_21	AL35
AD32	IO_L1N_21	IO_L18P_21	AL36
AD33	IO_L2P_21	IO_L17N_21	AK35
AE32	IO_L2N_21	IO_L17P_21	AJ35
AE33	IO_L3P_21	IO_L16N_21	AJ36
AE34	IO_L3N_21	IO_L16P_21	AH36
AV39	IO_L4P_21	IO_L15N_21	AM34
AV38	IO_L4N_VREF_21	IO_L15P_21	AN34
AU38	IO_L5P_21	IO_L14N_VREF_21	AN35
AU37	IO_L5N_21	IO_L14P_21	AM36
AT37	IO_L6P_21	IO_L13N_21	AN36
AR38	IO_L6N_21	IO_L13P_21	AP35
AR37	IO_L7P_21	IO_L12N_VRP_21	AP36
AT36	IO_L7N_21	IO_L12P_VRN_21	AP37
AE35	IO_L8P_CC_21	IO_L11N_CC_21	AG36
AF34	IO_L8N_CC_21	IO_L11P_CC_21	AH35
AF35	IO_L9P_CC_21	IO_L10N_CC_21	AG34
AF36	IO_L9N_CC_21	IO_L10P_CC_21	AH34

XC5VFX130T-FFG1738

19. I/O Bank 23

U?-19			
N33	IO_L0P_23	IO_L19N_23	T31
N34	IO_L0N_23	IO_L19P_23	U31
M34	IO_L1P_23	IO_L18N_23	U32
M33	IO_L1N_23	IO_L18P_23	T32
M32	IO_L2P_23	IO_L17N_23	R32
M31	IO_L2N_23	IO_L17P_23	R33
N31	IO_L3P_23	IO_L16N_23	P32
P31	IO_L3N_23	IO_L16P_23	P33
H34	IO_L4P_23	IO_L15N_23	L31
G34	IO_L4N_VREF_23	IO_L15P_23	L32
G33	IO_L5P_23	IO_L14N_VREF_23	J32
H33	IO_L5N_23	IO_L14P_23	K32
G32	IO_L6P_23	IO_L13N_23	J33
G31	IO_L6N_23	IO_L13P_23	K33
H31	IO_L7P_23	IO_L12N_VRP_23	K34
J31	IO_L7N_23	IO_L12P_VRN_23	L34
F35	IO_L8P_CC_23	IO_L11N_CC_23	E33
E35	IO_L8N_CC_23	IO_L11P_CC_23	E32
E34	IO_L9P_CC_23	IO_L10N_CC_23	F32
F34	IO_L9N_CC_23	IO_L10P_CC_23	F31

XC5VFX130T-FFG1738

20. I/O Bank 24

U?-20			
J12	IO_L0P_24	IO_L19N_24	F15
H11	IO_L0N_24	IO_L19P_24	E15
G12	IO_L1P_24	IO_L18N_24	P10
G11	IO_L1N_24	IO_L18P_24	R10
F12	IO_L2P_24	IO_L17N_24	E14
F11	IO_L2N_24	IO_L17P_24	D13
E10	IO_L3P_24	IO_L16N_24	N10
F10	IO_L3N_24	IO_L16P_24	P11
K14	IO_L4P_24	IO_L15N_24	D12
K13	IO_L4N_VREF_24	IO_L15P_24	E12
K12	IO_L5P_24	IO_L14N_VREF_24	P12
J11	IO_L5N_24	IO_L14P_24	N11
J13	IO_L6P_24	IO_L13N_24	E13
H13	IO_L6N_24	IO_L13P_24	F14
H10	IO_L7P_24	IO_L12N_VRP_24	M12
J10	IO_L7N_24	IO_L12P_VRN_24	M11
H14	IO_L8P_CC_24	IO_L11N_CC_24	G14
H15	IO_L8N_CC_24	IO_L11P_CC_24	G13
K10	IO_L9P_CC_24	IO_L10N_CC_24	L11
L10	IO_L9N_CC_24	IO_L10P_CC_24	L12

XC5VFX130T-FFG1738

21. I/O Bank 25

U?-21			
AG31	IO_L0P_25	IO_L19N_25	AM31
AF31	IO_L0N_25	IO_L19P_25	AL31
AF32	IO_L1P_25	IO_L18N_25	AM32
AG33	IO_L1N_25	IO_L18P_25	AL32
AH33	IO_L2P_25	IO_L17N_25	AK32
AG32	IO_L2N_25	IO_L17P_25	AJ32
AH31	IO_L3P_25	IO_L16N_25	AJ33
AJ31	IO_L3N_25	IO_L16P_25	AK33
AV35	IO_L4P_25	IO_L15N_25	AM33
AV36	IO_L4N_VREF_25	IO_L15P_25	AN33
AU36	IO_L5P_25	IO_L14N_VREF_25	AP33
AT35	IO_L5N_25	IO_L14P_25	AR33
AU34	IO_L6P_25	IO_L13N_25	AP32
AT34	IO_L6N_25	IO_L13P_25	AR32
AR35	IO_L7P_25	IO_L12N_VRP_25	AN31
AR34	IO_L7N_25	IO_L12P_VRN_25	AP31
AU32	IO_L8P_CC_25	IO_L11N_CC_25	AT31
AU33	IO_L8N_CC_25	IO_L11P_CC_25	AT32
AV33	IO_L9P_CC_25	IO_L10N_CC_25	AU31
AV34	IO_L9N_CC_25	IO_L10P_CC_25	AV31

XC5VFX130T-FFG1738

22. I/O Bank 26

U?-22			
AT7	IO_L0P_26	IO_L19N_26	AM8
AR7	IO_L0N_26	IO_L19P_26	AM7
AG12	IO_L1P_26	IO_L18N_26	AM6
AG11	IO_L1N_26	IO_L18P_26	AN6
AT6	IO_L2P_26	IO_L17N_26	AN5
AR5	IO_L2N_26	IO_L17P_26	AN4
AG9	IO_L3P_26	IO_L16N_26	AL7
AH9	IO_L3N_26	IO_L16P_26	AL6
AT5	IO_L4P_26	IO_L15N_26	AP5
AU6	IO_L4N_VREF_26	IO_L15P_26	AP6
AH10	IO_L5P_26	IO_L14N_VREF_26	AL10
AH11	IO_L5N_26	IO_L14P_26	AL9
AV6	IO_L6P_26	IO_L13N_26	AR8
AV5	IO_L6N_26	IO_L13P_26	AP7
AJ11	IO_L7P_26	IO_L12N_VRP_26	AK10
AJ10	IO_L7N_26	IO_L12P_VRN_26	AK9
AM9	IO_L8P_CC_26	IO_L11N_CC_26	AP8
AN9	IO_L8N_CC_26	IO_L11P_CC_26	AN8
AG8	IO_L9P_CC_26	IO_L10N_CC_26	AJ8
AH8	IO_L9N_CC_26	IO_L10P_CC_26	AK8

XC5VFX130T-FFG1738

23. I/O Bank 27

U?-23			
D31			D42
C31	IO_L0P_27	IO_L19N_27	E42
C30	IO_L0N_27	IO_L19P_27	D41
B31	IO_L1P_27	IO_L18N_27	D40
A30	IO_L1N_27	IO_L18P_27	C41
A31	IO_L2P_27	IO_L17N_27	B42
A32	IO_L2N_27	IO_L17P_27	B41
B32	IO_L3P_27	IO_L16N_27	A41
B33	IO_L3N_27	IO_L16P_27	A40
C33	IO_L4P_27	IO_L15N_27	A39
D32	IO_L4N_VREF_27	IO_L15P_27	B38
D33	IO_L5P_27	IO_L14N_VREF_27	B39
C34	IO_L5N_27	IO_L14P_27	C40
B34	IO_L6P_27	IO_L13N_27	C39
A34	IO_L6N_27	IO_L13P_27	D38
A35	IO_L7P_27	IO_L12N_VRP_27	C38
D35	IO_L7N_27	IO_L12P_VRN_27	B36
D36	IO_L8P_CC_27	IO_L11N_CC_27	B37
C36	IO_L8N_CC_27	IO_L11P_CC_27	A36
C35	IO_L9P_CC_27	IO_L10N_CC_27	A37
	IO_L9N_CC_27	IO_L10P_CC_27	

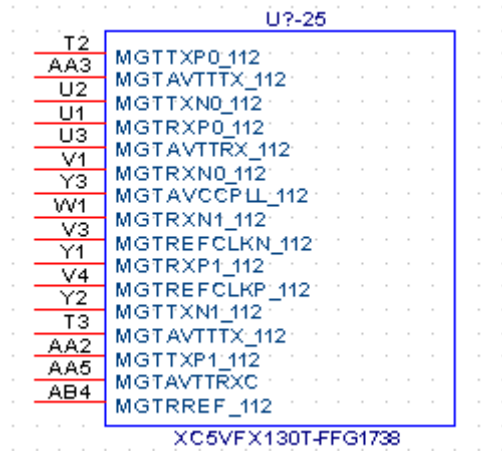
XC5VFX130T-FFG1738

24. I/O Bank 29

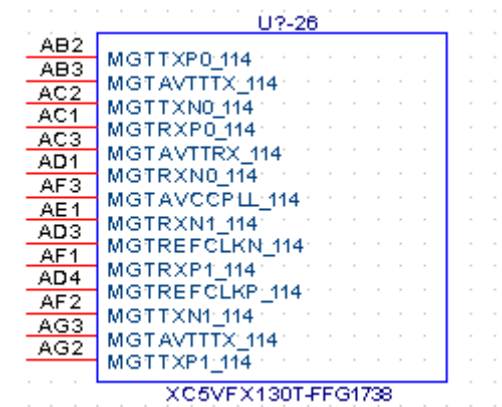
U?-24			
AY42			AW31
AW42	IO_L0P_29	IO_L19N_29	AY30
AW41	IO_L0N_29	IO_L19P_29	BA31
AW40	IO_L1P_29	IO_L18N_29	BA30
AY40	IO_L1N_29	IO_L18P_29	BB31
BA41	IO_L2P_29	IO_L17N_29	BB32
BA42	IO_L2N_29	IO_L17P_29	BA32
BB41	IO_L3P_29	IO_L16N_29	AY32
BA40	IO_L3N_29	IO_L16P_29	AW32
BB39	IO_L4P_29	IO_L15N_29	AW33
BB38	IO_L4N_VREF_29	IO_L15P_29	AY34
BA39	IO_L5P_29	IO_L14N_VREF_29	AY33
AY38	IO_L5N_29	IO_L14P_29	BA34
AW37	IO_L6P_29	IO_L13N_29	BB33
AW38	IO_L6N_29	IO_L13P_29	BA35
AY39	IO_L7P_29	IO_L12N_VRP_29	BB34
BB37	IO_L7N_29	IO_L12P_VRN_29	AW35
BA37	IO_L8P_CC_29	IO_L11N_CC_29	AY35
AY37	IO_L8N_CC_29	IO_L11P_CC_29	BA36
AW36	IO_L9P_CC_29	IO_L10N_CC_29	BB36
	IO_L9N_CC_29	IO_L10P_CC_29	

XC5VFX130T-FFG1738

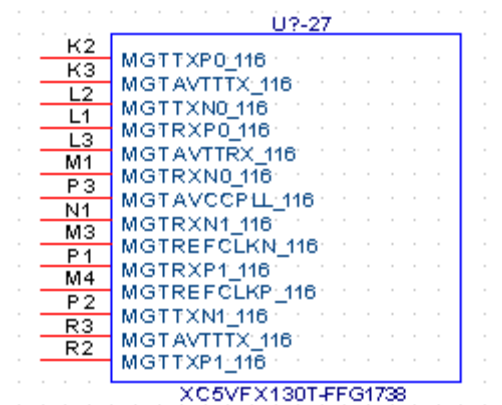
25. MGT 112



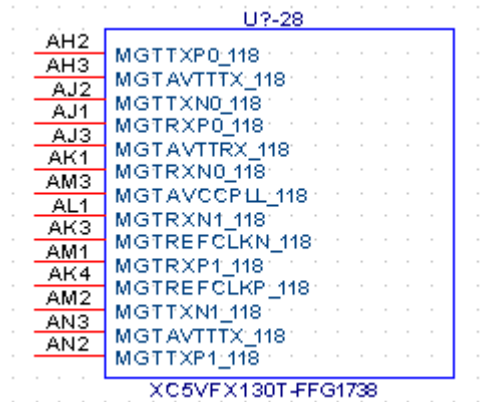
26. MGT 114



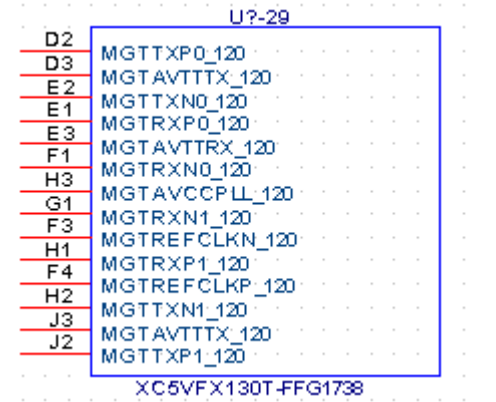
27. MGT 116



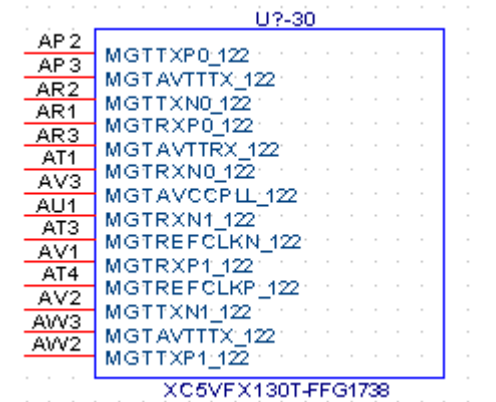
28. MGT 118



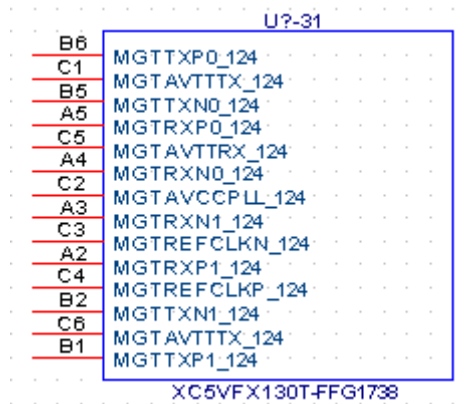
29. MGT 120



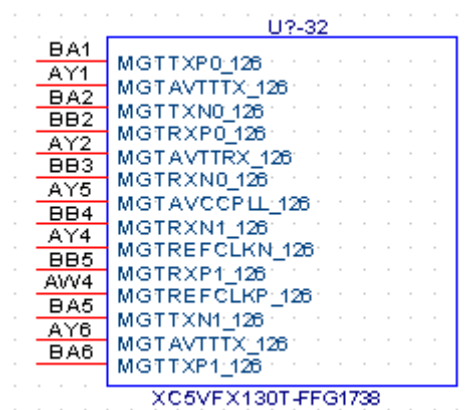
30. MGT 122



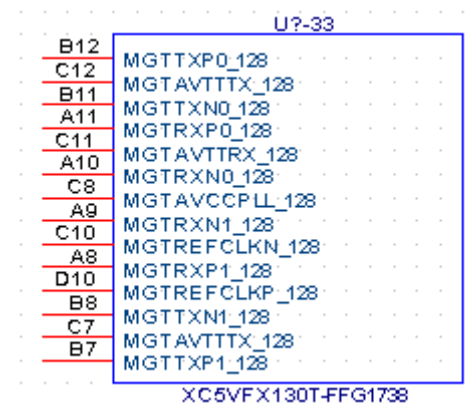
31. MGT 124



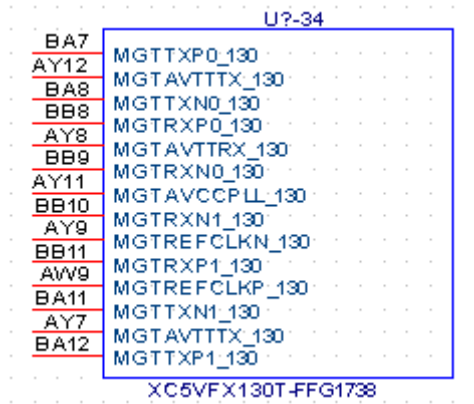
32. MGT 126



33. MGT 128



34. MGT 130



35. Power 1

U?-35			
F2	GND	GND	AJ13
G2	GND	GND	AG13
M2	GND	GND	AE13
N2	GND	GND	AC13
V2	GND	GND	AA13
W2	GND	GND	W13
AD2	GND	GND	U13
AE2	GND	GND	R13
AK2	GND	GND	L13
AL2	GND	GND	AV12
AT2	GND	GND	AN12
AU2	GND	GND	AH12
B3	GND	GND	AD12
BA3	GND	GND	AB12
B4	GND	GND	Y12
E4	GND	GND	V12
H4	GND	GND	T12
J4	GND	GND	N12
L4	GND	GND	H12
P4	GND	GND	AV11
U4	GND	GND	AR11
Y4	GND	GND	AE11
AC4	GND	GND	AC11
AF4	GND	GND	Y11
AJ4	GND	GND	R11
AM4	GND	GND	K11
AP4	GND	GND	E11
AR4	GND	GND	D11
AV4	GND	GND	BA10
BA4	GND	GND	AM10
G5	GND	GND	AB10
M5	GND	GND	M10
U5	GND	GND	B10
AB5	GND	GND	BA9
AG5	GND	GND	AJ9
AM5	GND	GND	W9
AU5	GND	GND	J9
D6	GND	GND	B9
K6	GND	GND	AV8
Y6	GND	GND	AT8
AK6	GND	GND	AF8
AR6	GND	GND	T8
AV6	GND	GND	F8
N7	GND	GND	D8
AC7	GND	GND	AN7
	GND	GND	

XC5VFX130T-FFG1738

36. Power 2

U?-36			
AL13	GND	GND	AG21
D14	GND	GND	AE 21
P14	GND	GND	W21
V14	GND	GND	U21
Y14	GND	GND	R21
AB14	GND	GND	K21
AD14	GND	GND	E 21
AF14	GND	GND	AM20
AJ14	GND	GND	AH20
AP14	GND	GND	AF20
AW14	GND	GND	AD20
B15	GND	GND	AB20
G15	GND	GND	Y20
M15	GND	GND	V20
U15	GND	GND	T 20
W15	GND	GND	M20
AA15	GND	GND	B 20
AC15	GND	GND	BB19
AE15	GND	GND	AW19
AG15	GND	GND	AJ19
AM15	GND	GND	AG19
AU15	GND	GND	AE19
BA15	GND	GND	AC19
B16	GND	GND	AA19
E16	GND	GND	W19
K16	GND	GND	U19
P16	GND	GND	R19
T16	GND	GND	P 19
V16	GND	GND	J19
Y16	GND	GND	C19
AB16	GND	GND	B19
AD16	GND	GND	A19
AF16	GND	GND	AT18
AK16	GND	GND	AL18
BA16	GND	GND	AH18
D17	GND	GND	AF18
N17	GND	GND	AD18
R17	GND	GND	AB18
U17	GND	GND	Y18
W17	GND	GND	V18
AA17	GND	GND	T18
AC17	GND	GND	L18
AE17	GND	GND	F18
AG17	GND	GND	AW17
AJ17	GND	GND	AN17
	GND	GND	

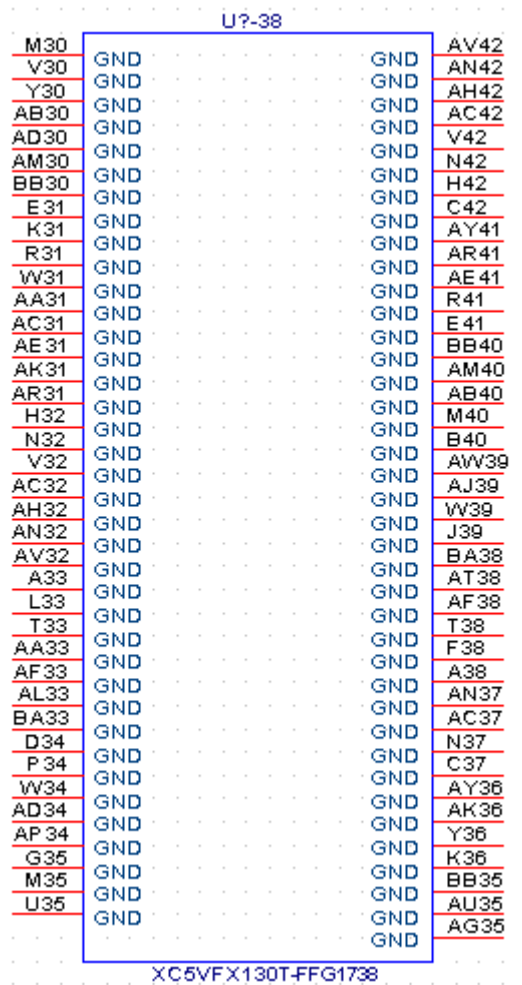
XC5VFX130T-FFG1738

37. Power 3

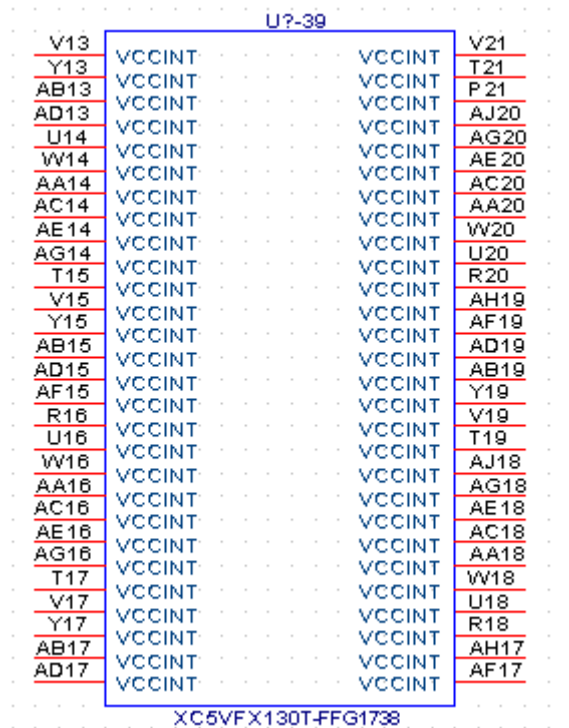
U?-37			
AK21	GND	GND	B30
AR21	GND	GND	AW29
H22	GND	GND	AP 29
P 22	GND	GND	AJ29
T22	GND	GND	AE 29
V22	GND	GND	AC29
AD22	GND	GND	AA29
AF22	GND	GND	W29
AH22	GND	GND	U29
AN22	GND	GND	P 29
AV22	GND	GND	J29
A23	GND	GND	AT28
L23	GND	GND	AH28
R23	GND	GND	AF28
U23	GND	GND	AD28
W23	GND	GND	AB28
AA23	GND	GND	Y28
AC23	GND	GND	V28
AE23	GND	GND	T28
AG23	GND	GND	L28
AJ23	GND	GND	F28
AL23	GND	GND	A28
BA23	GND	GND	AN27
D24	GND	GND	AJ27
J24	GND	GND	AG27
P 24	GND	GND	AE 27
T24	GND	GND	AC27
V24	GND	GND	AA27
Y24	GND	GND	W27
AB24	GND	GND	U27
AD24	GND	GND	R27
AF24	GND	GND	N27
AH24	GND	GND	C27
AP 24	GND	GND	AY26
G25	GND	GND	AK26
M25	GND	GND	AH26
R25	GND	GND	AF26
U25	GND	GND	AD26
W25	GND	GND	AB26
AA25	GND	GND	Y26
AC25	GND	GND	V26
AE25	GND	GND	T26
AG25	GND	GND	K26
AJ25	GND	GND	BB25
AM25	GND	GND	AU25
	GND	GND	

XC5VFX130T-FFG1738

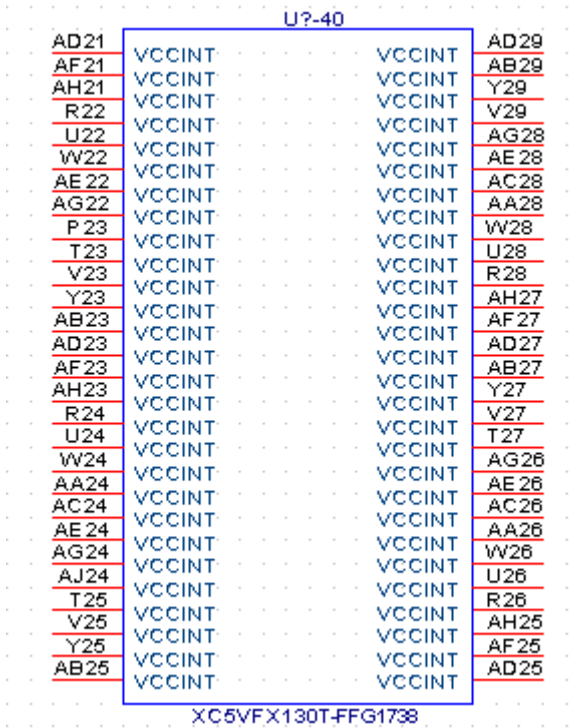
38. Power 4



39. Power 5



40. Power 6

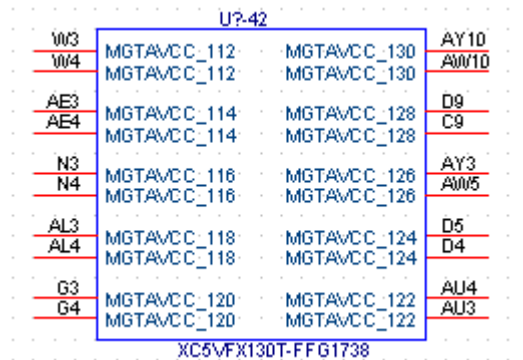


41. Power 7

U?-41			
R12	VCCAUX	VCCO_29	AV37
U12	VCCAUX	VCCO_29	AM34
W12	VCCAUX	VCCO_29	AT33
AA12	VCCAUX	VCCO_29	
AC12	VCCAUX		B35
AE12	VCCAUX	VCCO_27	J34
AJ12	VCCAUX	VCCO_27	F33
T13	VCCAUX	VCCO_27	
AF13	VCCAUX		AK11
AH13	VCCAUX	VCCO_26	AL8
T29	VCCAUX	VCCO_26	AH7
AF29	VCCAUX	VCCO_26	
U30	VCCAUX		AU40
W30	VCCAUX	VCCO_25	AP39
AA30	VCCAUX	VCCO_25	AR36
AC30	VCCAUX	VCCO_25	
AE30	VCCAUX		G10
Y31	VCCAUX	VCCO_24	H7
AB31	VCCAUX	VCCO_24	E6
AD31	VCCAUX	VCCO_24	
			D39
AL28	VCCO_0	VCCO_23	H37
AG30	VCCO_0	VCCO_23	E36
F13	VCCO_1	VCCO_21	AL38
J14	VCCO_1	VCCO_21	AM35
			AJ34
AR26	VCCO_2	VCCO_21	
AV27	VCCO_2		P9
		VCCO_20	L8
E26	VCCO_3	VCCO_20	R6
H27	VCCO_3	VCCO_20	
			K41
AT13	VCCO_4	VCCO_19	G40
AR16	VCCO_4	VCCO_19	L38
C22	VCCO_5	VCCO_18	AG10
F23	VCCO_5	VCCO_18	AD9
B25	VCCO_5	VCCO_18	AE6
AY21	VCCO_6	VCCO_17	AK41
AT23	VCCO_6	VCCO_17	AG40
AM24	VCCO_6	VCCO_17	AH37
H17	VCCO_7	VCCO_15	U40
D19	VCCO_7	VCCO_15	P39
G20	VCCO_7	VCCO_15	R36
AV17	VCCO_8	VCCO_13	AD39
AP19	VCCO_8	VCCO_13	AE36
AJ20	VCCO_8	VCCO_13	AB35
V37	VCCO_11	VCCO_12	U10
AA38	VCCO_11	VCCO_12	AA8
Y41	VCCO_11	VCCO_12	V7

XC5VFX130T-FFG1738

42. Power 8



Notes:

1. All the unconnected pins are not present on the symbol
2. All the pins and associated names were imported in Orcad from the Xilinx provided V5 ASCII files released on 12/19/2008.
3. All the I/O pins are assigned as “passive” to avoid warnings when running Orcad DRC.
4. If you plan on being able to migrate to a higher density component, please select the corresponding symbol for your design. Using the current one will not allow the direct migration due to the unconnected VCCO pins when migrating to the higher density component.
5. Please connect all the power pins in the design. This will allow proper operation of the FPGA device.

Document Revision History

	Revision	Date	By	Comments
1	1.01	March 20, 09	MD	Initial Release