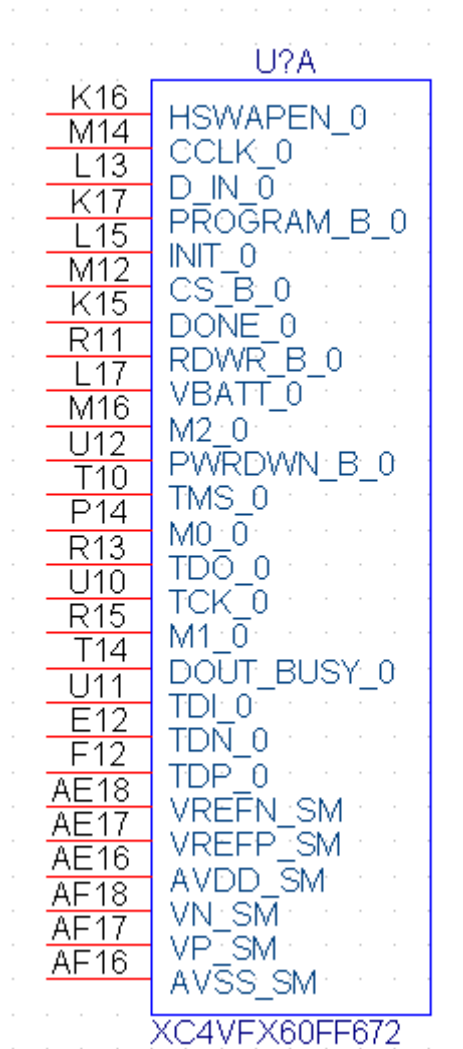


## Schematic Symbol for XC4VFX60FF672

The Schematic symbol consists of 18 heterogeneous parts that are listed below:

### 1. Programming Interface



## 2. I/O Banks 1 and 2

U?B	
J15	IO_L1P_D31_LC_1
J14	IO_L1N_D30_LC_1
K13	IO_L2P_D29_LC_1
J13	IO_L2N_D28_LC_1
H14	IO_L3P_D27_LC_1
G14	IO_L3N_D26_LC_1
H13	IO_L4P_D25_LC_1
H12	IO_L4N_D24_VREF_LC_1
J16	IO_L5P_D23_LC_1
H16	IO_L5N_D22_LC_1
K12	IO_L6P_D21_LC_1
K11	IO_L6N_D20_LC_1
G16	IO_L7P_D19_LC_1
G15	IO_L7N_D18_LC_1
H11	IO_L8P_D17_CC_LC_1
J11	IO_L8N_D16_CC_LC_1
V16	IO_L1P_D15_CC_LC_2
W16	IO_L1N_D14_CC_LC_2
Y12	IO_L2P_D13_LC_2
Y11	IO_L2N_D12_LC_2
U16	IO_L3P_D11_LC_2
U15	IO_L3N_D10_LC_2
W11	IO_L4P_D9_LC_2
V11	IO_L4N_D8_VREF_LC_2
W15	IO_L5P_D7_LC_2
W14	IO_L5N_D6_LC_2
Y13	IO_L6P_D5_LC_2
W13	IO_L6N_D4_LC_2
U14	IO_L7P_D3_LC_2
V14	IO_L7N_D2_LC_2
V13	IO_L8P_D1_LC_2
V12	IO_L8N_D0_LC_2

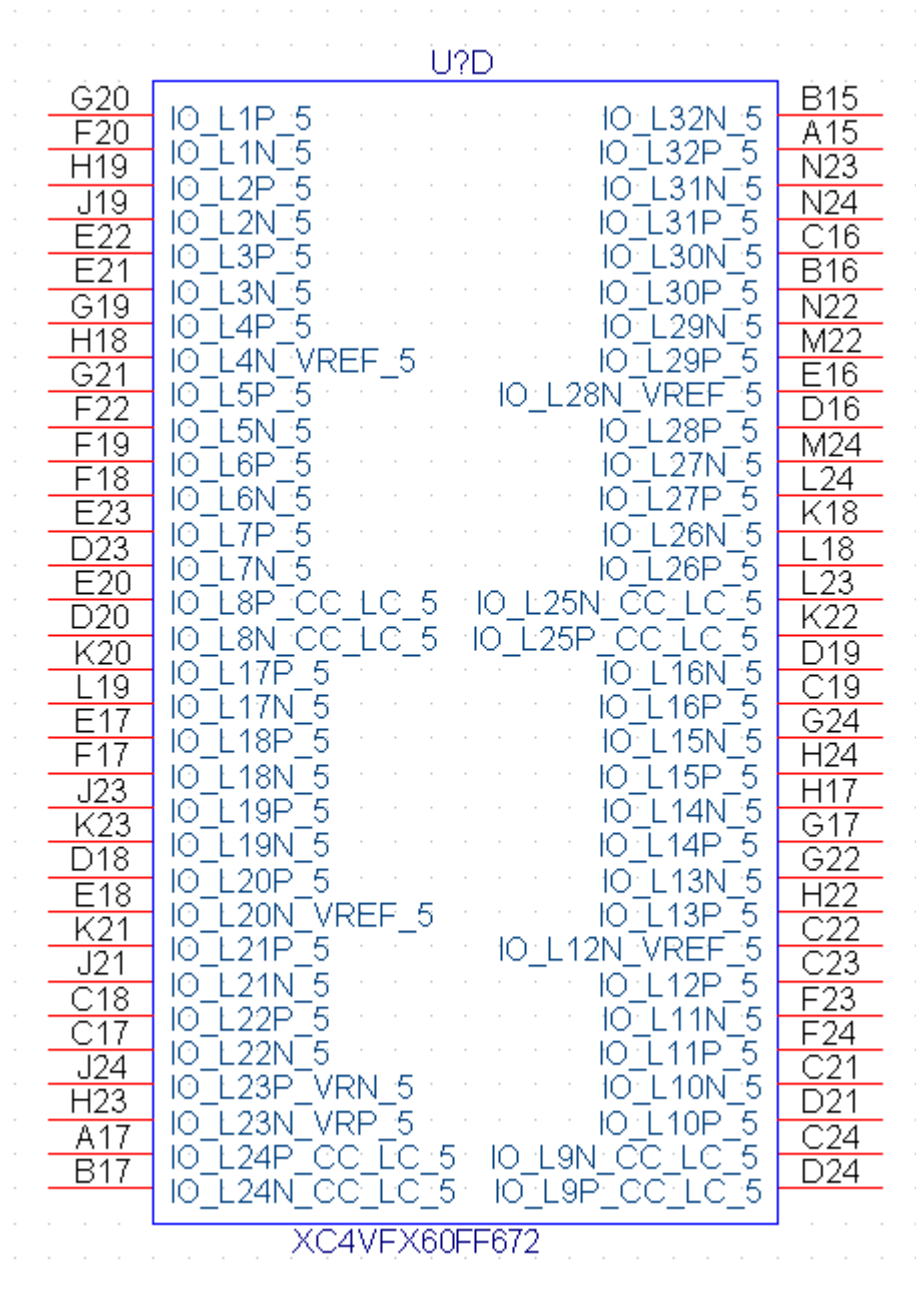
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### 3. I/O Banks 3 and 4

U?C	
<u>F15</u>	IO_L1P_GC_CC_LC_3
<u>E15</u>	IO_L1N_GC_CC_LC_3
<u>F14</u>	IO_L2P_GC_VRN_LC_3
<u>F13</u>	IO_L2N_GC_VRP_LC_3
<u>D15</u>	IO_L3P_GC_LC_3
<u>D14</u>	IO_L3N_GC_LC_3
<u>D13</u>	IO_L4P_GC_LC_3
<u>E13</u>	IO_L4N_GC_VREF_LC_3
<u>C14</u>	IO_L5P_GC_LC_3
<u>B14</u>	IO_L5N_GC_LC_3
<u>C13</u>	IO_L6P_GC_LC_3
<u>C12</u>	IO_L6N_GC_LC_3
<u>A14</u>	IO_L7P_GC_LC_3
<u>A13</u>	IO_L7N_GC_LC_3
<u>A12</u>	IO_L8P_GC_LC_3
<u>B12</u>	IO_L8N_GC_LC_3
<u>AE15</u>	IO_L1P_GC_LC_4
<u>AF15</u>	IO_L1N_GC_LC_4
<u>AF14</u>	IO_L2P_GC_LC_4
<u>AF13</u>	IO_L2N_GC_LC_4
<u>AD15</u>	IO_L3P_GC_LC_4
<u>AD14</u>	IO_L3N_GC_LC_4
<u>AE13</u>	IO_L4P_GC_LC_4
<u>AD13</u>	IO_L4N_GC_VREF_LC_4
<u>AB14</u>	IO_L5P_GC_LC_4
<u>AC14</u>	IO_L5N_GC_LC_4
<u>AC13</u>	IO_L6P_GC_LC_4
<u>AC12</u>	IO_L6N_GC_LC_4
<u>AA14</u>	IO_L7P_GC_VRN_LC_4
<u>AA13</u>	IO_L7N_GC_VRP_LC_4
<u>AB12</u>	IO_L8P_GC_CC_LC_4
<u>AA12</u>	IO_L8N_GC_CC_LC_4

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4. I/O Bank 5



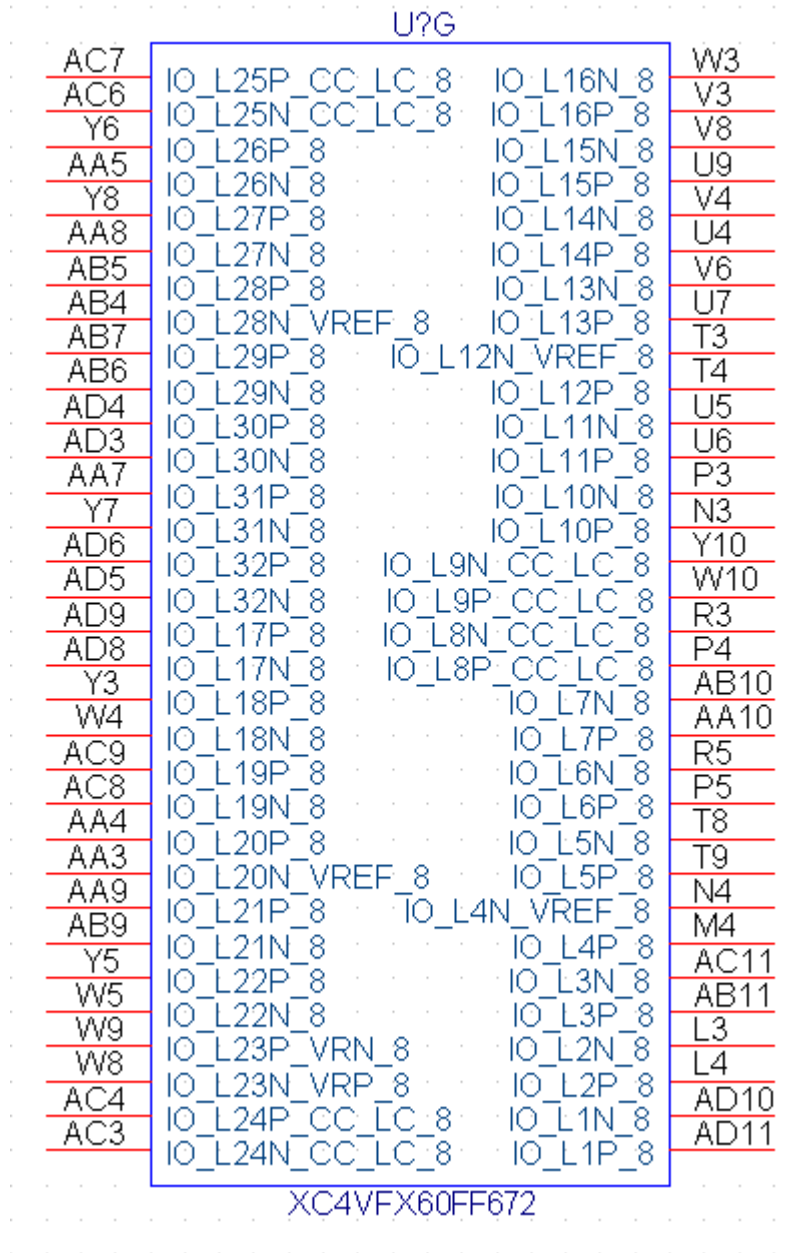


6. I/O Bank 7

U?F			
AB24	IO_L25P_CC_LC_7	IO_L16N_7	AC17
AC24	IO_L25N_CC_LC_7	IO_L16P_7	AD18
AB20	IO_L26P_7	IO_L15N_7	T20
AB19	IO_L26N_7	IO_L15P_7	U20
W20	IO_L27P_7	IO_L14N_7	Y17
W19	IO_L27N_7	IO_L14P_7	AA17
W18	IO_L28P_7	IO_L13N_7	Y23
V18	IO_L28N_VREF_7	IO_L13P_7	W23
AD24	IO_L29P_7	IO_L12N_VREF_7	T18
AD23	IO_L29N_7	IO_L12P_7	U19
AA20	IO_L30P_7	IO_L11N_7	V22
AA19	IO_L30N_7	IO_L11P_7	V23
AC23	IO_L31P_7	IO_L10N_7	AB16
AC22	IO_L31N_7	IO_L10P_7	AB17
AB22	IO_L32P_7	IO_L9N_CC_LC_7	V24
AB21	IO_L32N_7	IO_L9P_CC_LC_7	W24
V21	IO_L17P_7	IO_L8N_CC_LC_7	Y15
U21	IO_L17N_7	IO_L8P_CC_LC_7	Y16
AC19	IO_L18P_7	IO_L7N_7	T24
AC18	IO_L18N_7	IO_L7P_7	U24
AA24	IO_L19P_7	IO_L6N_7	U17
AA23	IO_L19N_7	IO_L6P_7	T17
AA18	IO_L20P_7	IO_L5N_7	T22
Y18	IO_L20N_VREF_7	IO_L5P_7	T23
Y22	IO_L21P_7	IO_L4N_VREF_7	AC16
AA22	IO_L21N_7	IO_L4P_7	AD16
AD20	IO_L22P_7	IO_L3N_7	R20
AD19	IO_L22N_7	IO_L3P_7	R21
W21	IO_L23P_VRN_7	IO_L2N_7	AA15
Y20	IO_L23N_VRP_7	IO_L2P_7	AB15
AC21	IO_L24P_CC_LC_7	IO_L1N_7	R23
AD21	IO_L24N_CC_LC_7	IO_L1P_7	P24

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**7. I/O Bank 8**

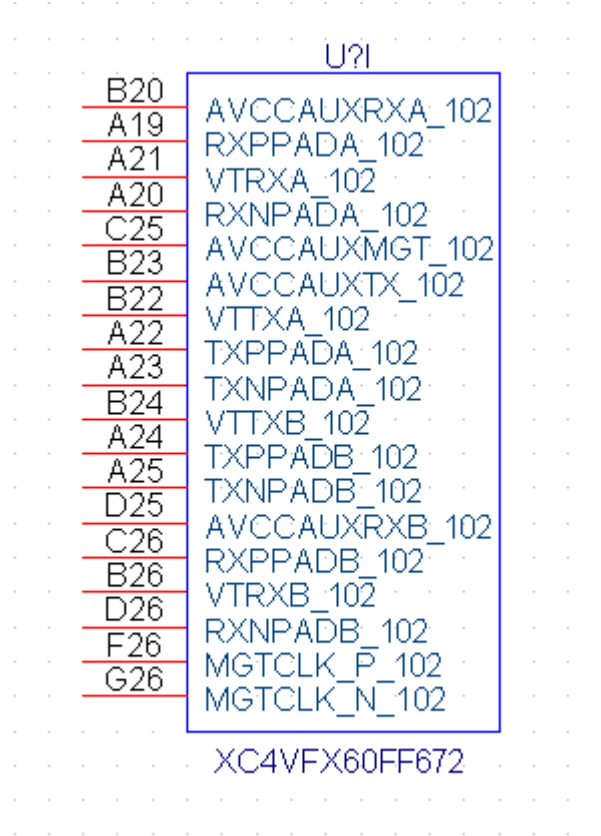


**8. I/O Banks 9 and 10**

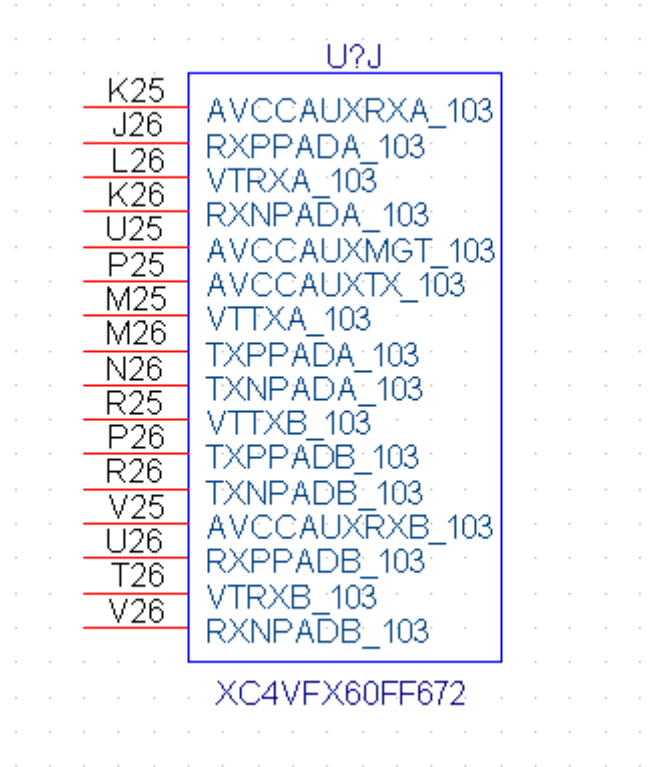
U?H	
M20	IO_L17P_9
M19	IO_L17N_9
P16	IO_L18P_9
N16	IO_L18N_9
N21	IO_L19P_9
M21	IO_L19N_9
N18	IO_L20P_9
N17	IO_L20N_VREF_9
P19	IO_L21P_9
N19	IO_L21N_9
R17	IO_L22P_9
R16	IO_L22N_9
P21	IO_L23P_VRN_9
P20	IO_L23N_VRP_9
R18	IO_L24P_CC_LC_9
P18	IO_L24N_CC_LC_9
M10	IO_L17P_10
M9	IO_L17N_10
N8	IO_L18P_10
N7	IO_L18N_10
M11	IO_L19P_10
N11	IO_L19N_10
P8	IO_L20P_10
R8	IO_L20N_VREF_10
P11	IO_L21P_10
P10	IO_L21N_10
P6	IO_L22P_10
N6	IO_L22N_10
N9	IO_L23P_VRN_10
P9	IO_L23N_VRP_10
R7	IO_L24P_CC_LC_10
R6	IO_L24N_CC_LC_10

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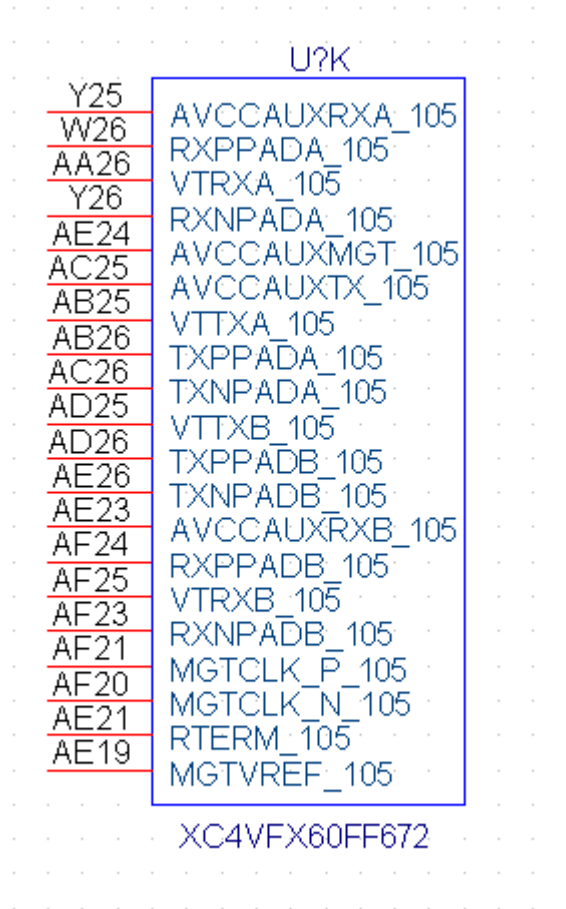
### 9. MGT 102



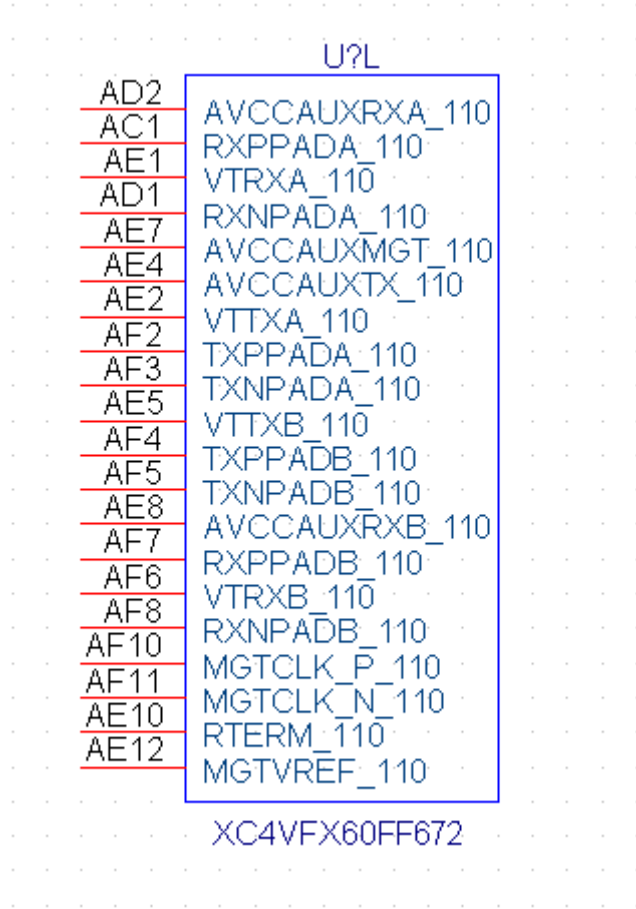
### 10. MGT 103



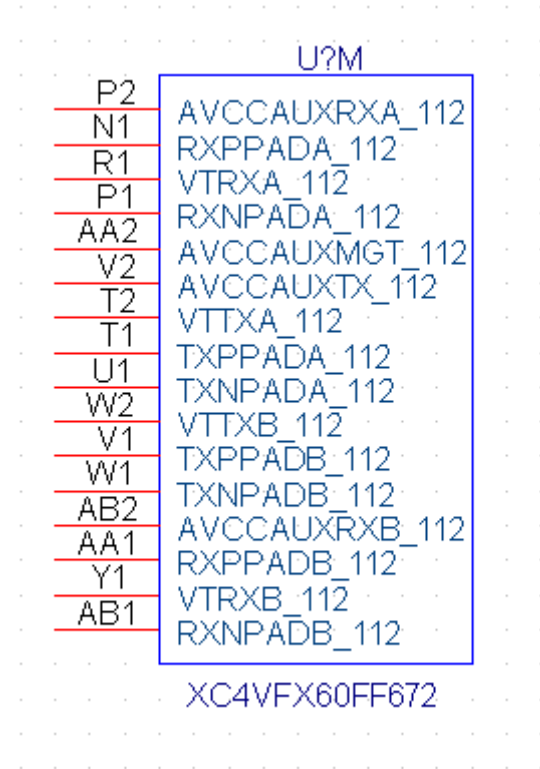
## 11. MGT 105



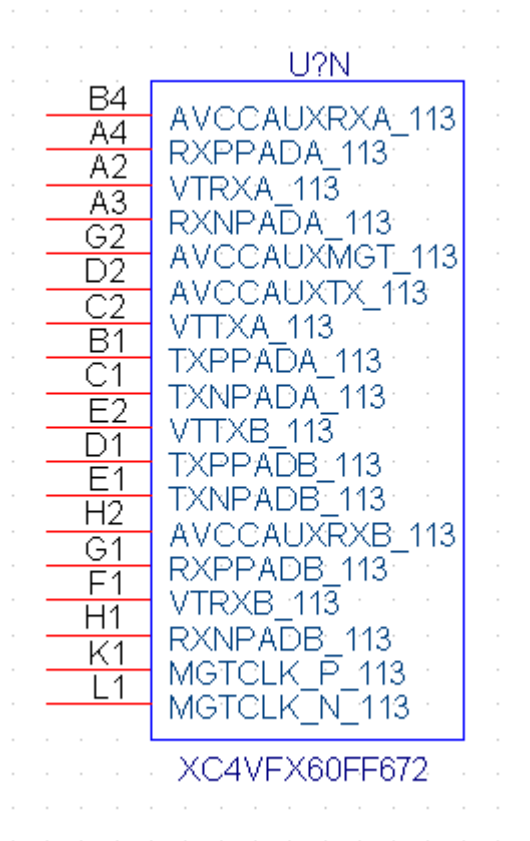
## 12. MGT 110



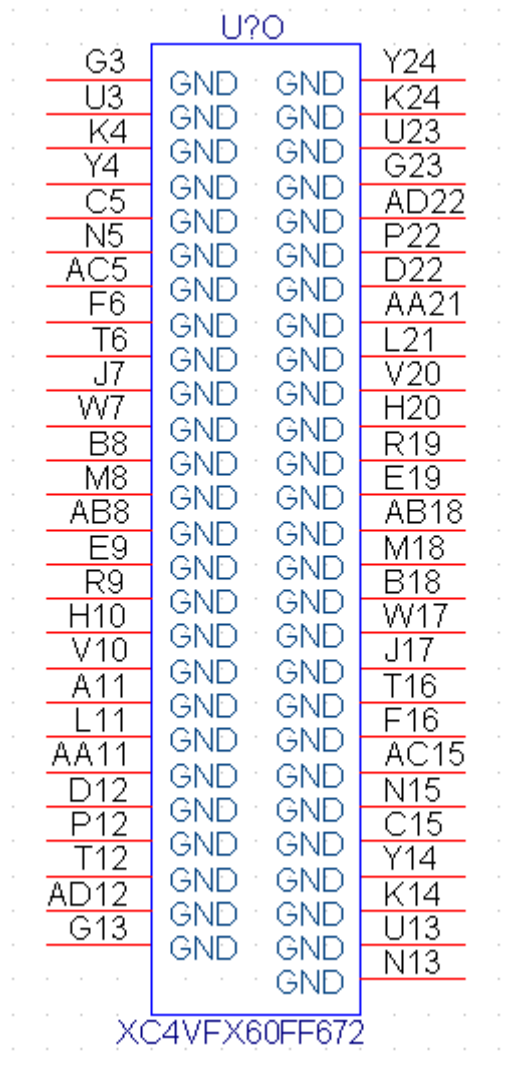
### 13. MGT 112



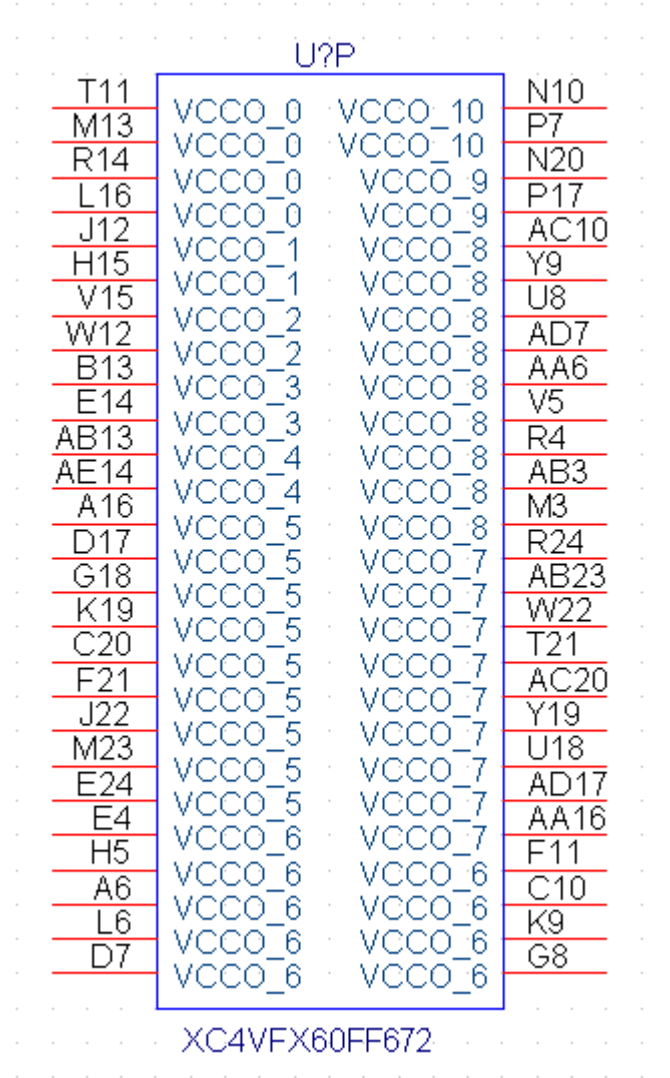
### 14. MGT 113



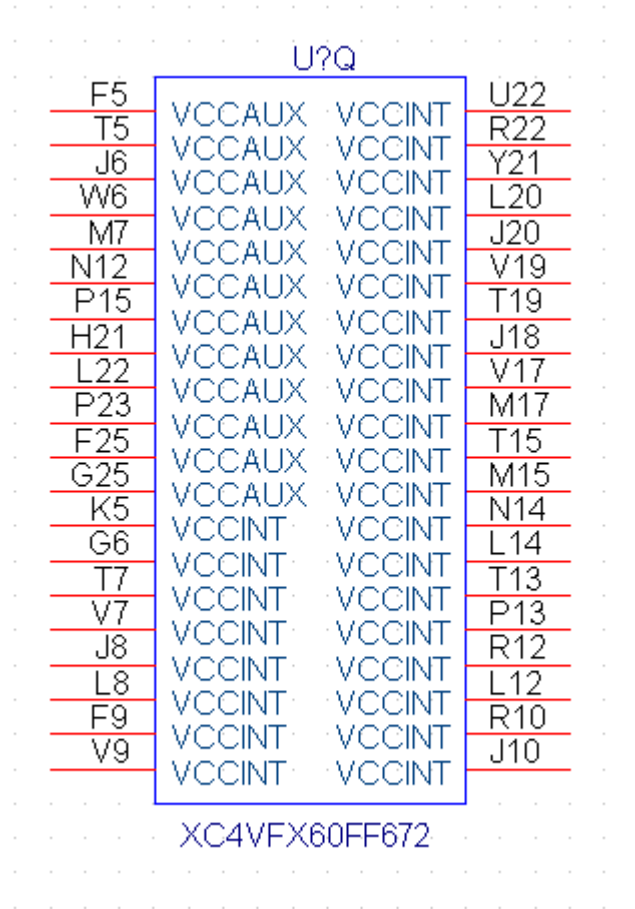
### 15. Ground



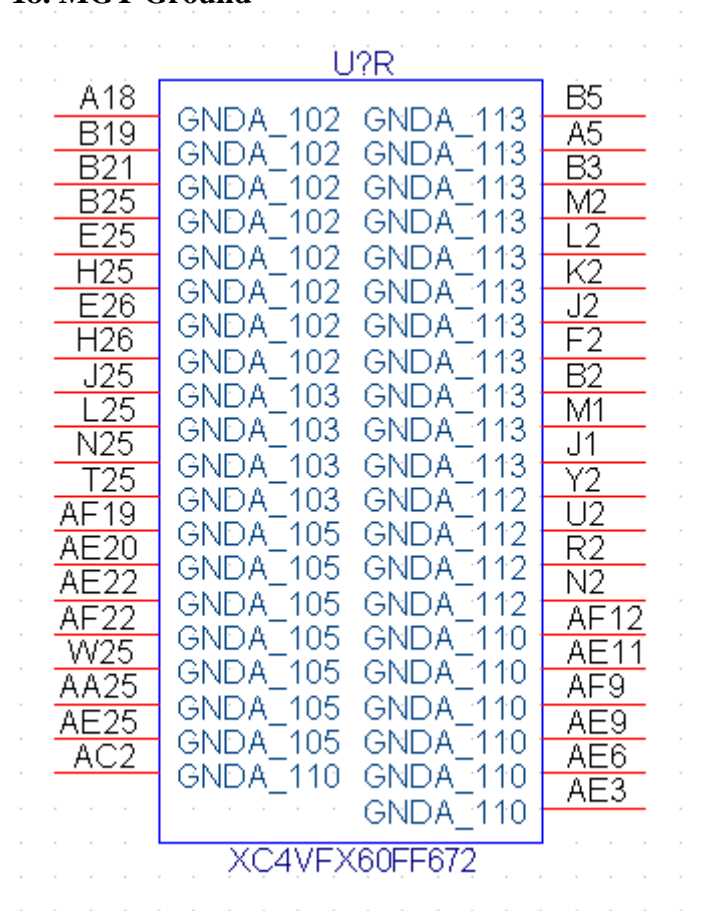
**16. Power 1**



**17. Power 2**



### 18. MGT Ground



### 19. Revision History

	Revision	Date	By	Comments
1	1.01	Jan 29, 2007	MD	Initial Release