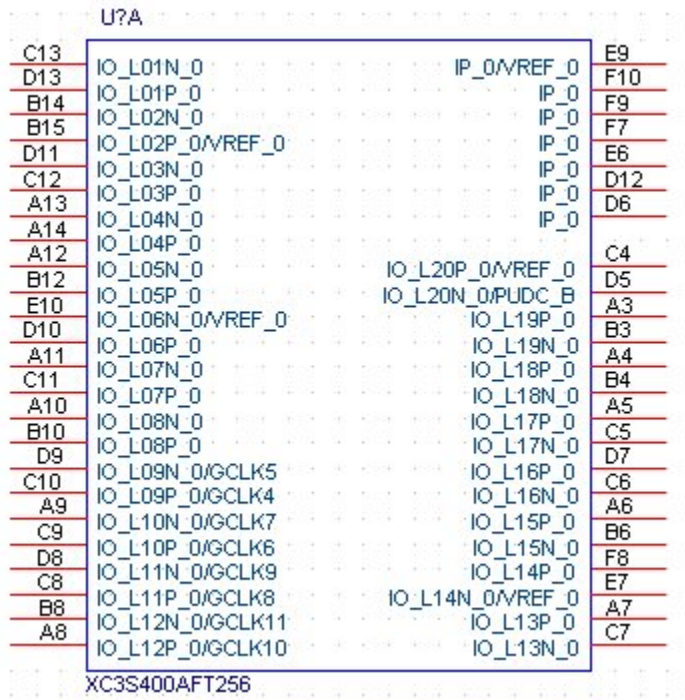


Schematic Symbol for XC3S400AFT256

The symbol consists of 6 heterogeneous parts, each of them listed below:

1. I/O Bank 0

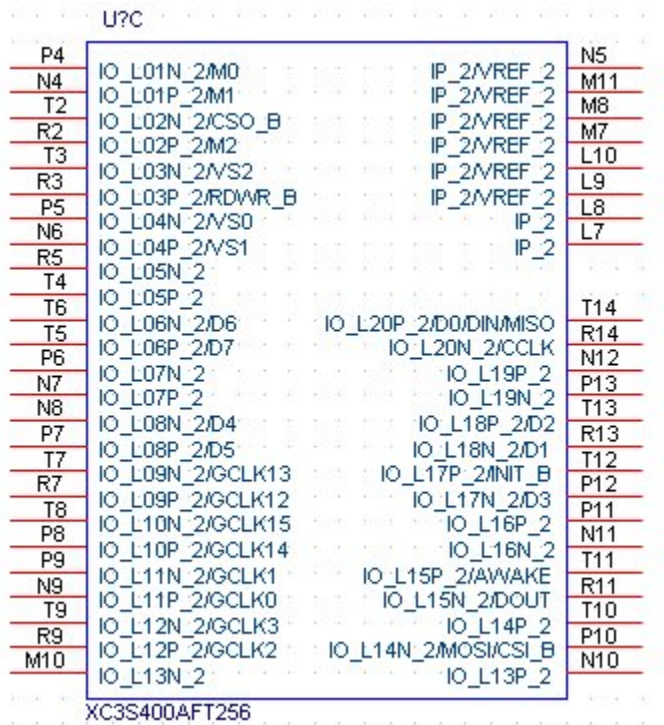


2. I/O Bank 1

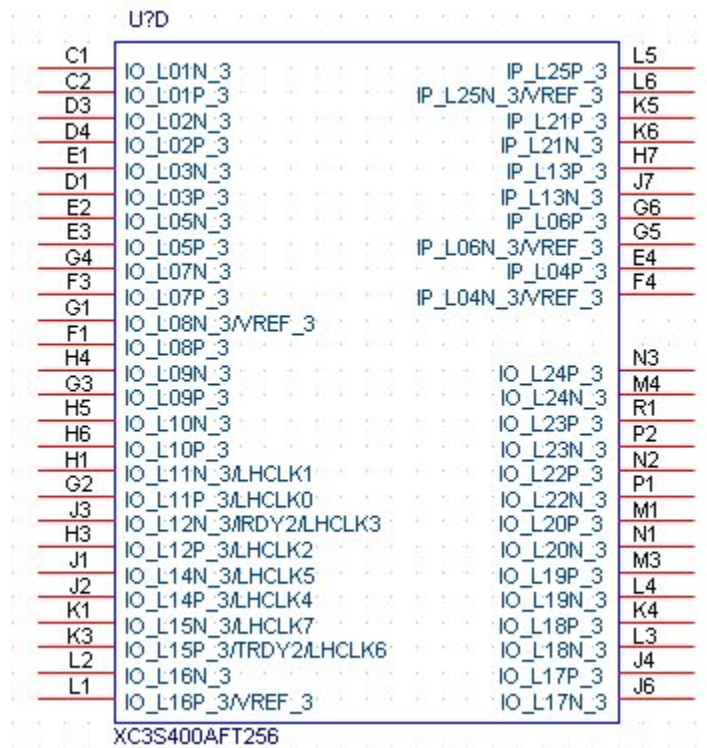
U?B			
N14	IO_L01N_1/LDC2	IP_L25P_1/VREF_1	F12
N13	IO_L01P_1/HDC	IP_L25N_1	F11
P15	IO_L02N_1/LDC0	IP_L21P_1/VREF_1	G12
R15	IO_L02P_1/LDC1	IP_L21N_1	G11
N16	IO_L03N_1/A1	IP_L13P_1	H10
P16	IO_L03P_1/A0	IP_L13N_1	H11
M14	IO_L05N_1/VREF_1	IP_L09P_1/VREF_1	J10
M13	IO_L05P_1	IP_L09N_1	J11
K13	IO_L06N_1/A3	IP_L04P_1	K11
L13	IO_L06P_1/A2	IP_L04N_1/VREF_1	K12
M16	IO_L07N_1/A5		
M15	IO_L07P_1/A4		
L16	IO_L08N_1/A7	IO_L24P_1/A24	C16
L14	IO_L08P_1/A6	IO_L24N_1/A25	C15
J13	IO_L10N_1/A9	IO_L23P_1/A22	E13
J12	IO_L10P_1/A8	IO_L23N_1/A23	D14
K14	IO_L11N_1/RHCLK1	IO_L22P_1/A20	D16
K15	IO_L11P_1/RHCLK0	IO_L22N_1/A21	D15
J16	IO_L12N_1/TRDY1/RHCLK3	IO_L20P_1/A18	E14
K16	IO_L12P_1/RHCLK2	IO_L20N_1/A19	F13
H14	IO_L14N_1/RHCLK5	IO_L19P_1/A16	G13
J14	IO_L14P_1/RHCLK4	IO_L19N_1/A17	F14
H16	IO_L15N_1/RHCLK7	IO_L18P_1/A14	E16
H15	IO_L15P_1/RDY1/RHCLK6	IO_L18N_1/A15	F15
F16	IO_L16N_1/A11	IO_L17P_1/A12	H13
G16	IO_L16P_1/A10	IO_L17N_1/A13	G14

XC3S400AFT256

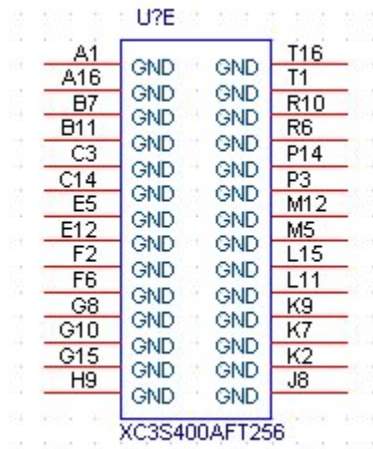
3. I/O Bank2 (Contains the Programming Interface)



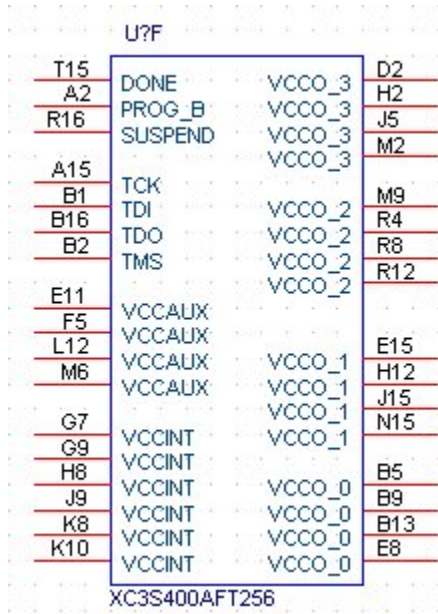
4. I/O Bank 3



5. GND



6. Power, JTAG and Power Management



Document Revision History

Revision	Date	By	Comments	
1	1.00	Feb 5, 09	LD	Initial Release