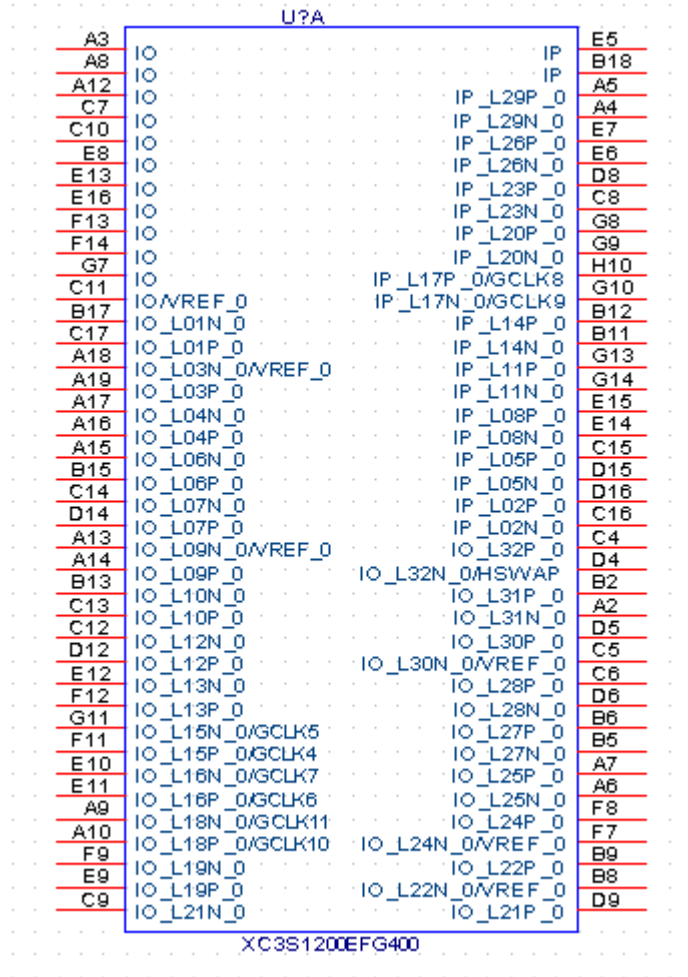


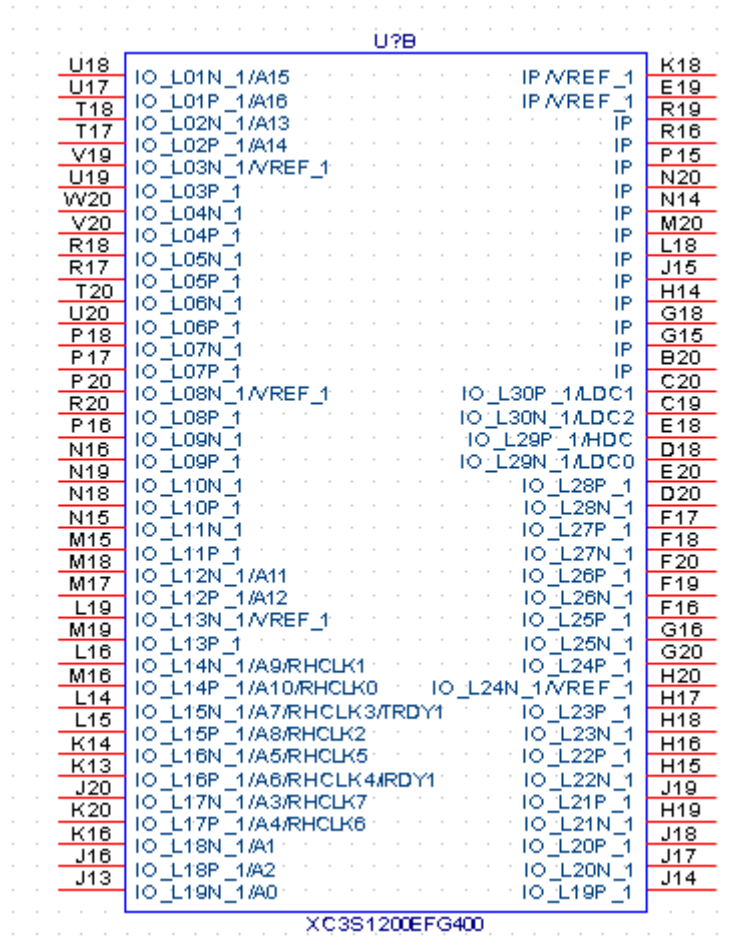
Schematic Symbol for XC3S1200EFG400

The Schematic symbol consists of 6 heterogeneous parts that are listed below:

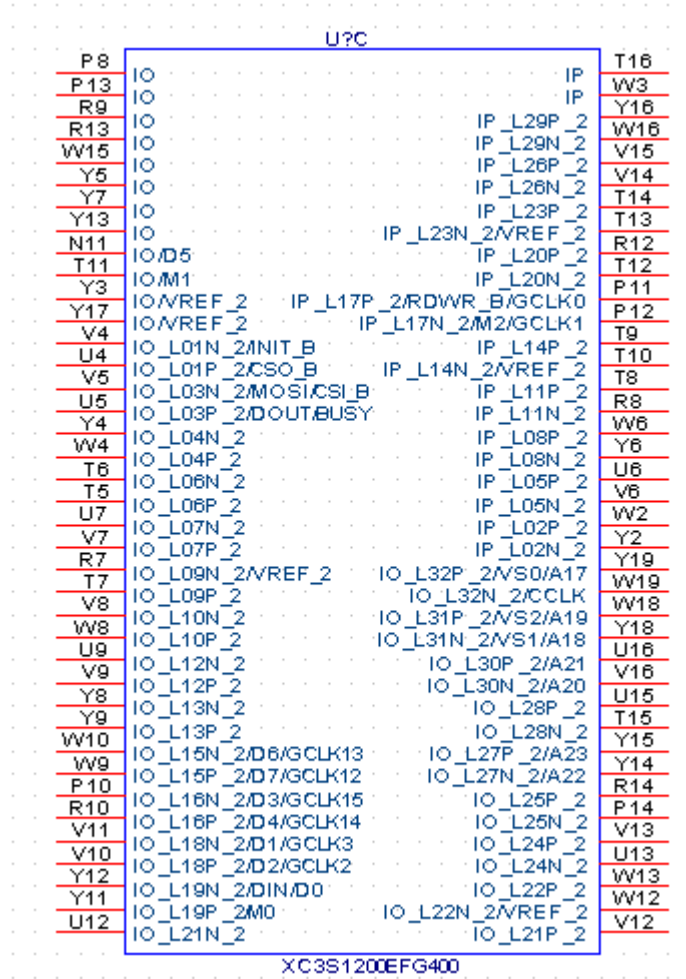
1. IO bank 0



2. IO bank 1



3. IO bank 2



4. IO bank 3

U?D			
D2			W1
D3	IO_L01N_3		T4
E3	IO_L01P_3		P3
E4	IO_L02N_3/VREF_3		N5
C1	IO_L02P_3		M2
B1	IO_L03N_3		L8
E1	IO_L03P_3		L5
D1	IO_L04N_3		J5
F3	IO_L04P_3		H1
F4	IO_L05N_3		G6
F4	IO_L05P_3		G1
F1	IO_L06N_3		F5
F2	IO_L06P_3		K5
G4	IO_L07N_3	IP/VREF_3	P6
G3	IO_L07P_3	IP/VREF_3	V2
G5	IO_L08N_3	IO_L30P_3	V1
H5	IO_L08P_3	IO_L30N_3	U3
H3	IO_L09N_3/VREF_3	IO_L29P_3	T3
H2	IO_L09P_3	IO_L29N_3	U1
H7	IO_L10N_3	IO_L28P_3	T1
H6	IO_L10P_3	IO_L28N_3/VREF_3	R3
J4	IO_L11N_3	IO_L27P_3	R4
J3	IO_L11P_3	IO_L27N_3	R2
J1	IO_L12N_3	IO_L26P_3	T2
J2	IO_L12P_3	IO_L26N_3	P5
J6	IO_L13N_3	IO_L25P_3	R5
K6	IO_L13P_3	IO_L25N_3	P1
K2	IO_L14N_3/LHCLK1	IO_L24P_3	R1
K3	IO_L14P_3/LHCLK0	IO_L24N_3	N3
L7	IO_L15N_3/LHCLK3/TRDY2	IO_L23P_3	N4
K7	IO_L15P_3/LHCLK2	IO_L23N_3	N7
L1	IO_L16N_3/LHCLK5	IO_L22P_3	P7
M1	IO_L16P_3/LHCLK4/TRDY2	IO_L22N_3	N1
L3	IO_L17N_3/LHCLK7	IO_L21P_3	N2
M3	IO_L17P_3/LHCLK6	IO_L21N_3	M6
M7	IO_L18N_3	IO_L20P_3	N6
M8	IO_L18P_3	IO_L20N_3/VREF_3	M5
M4	IO_L19N_3	IO_L19P_3	

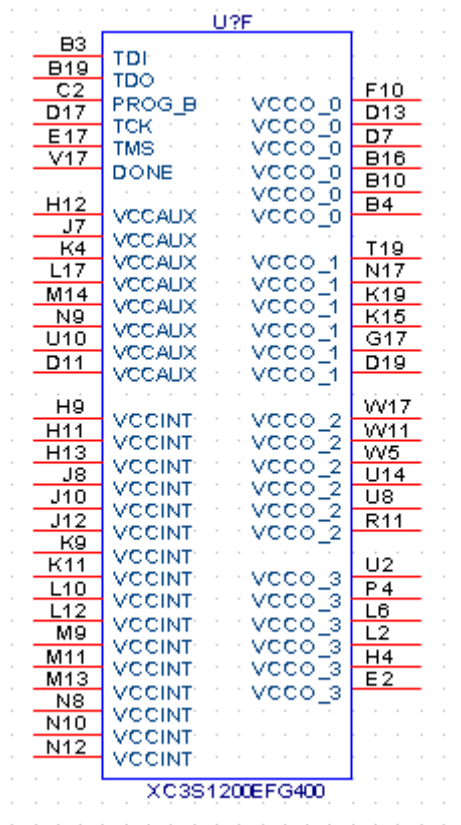
XC3S1200EFG400

5. Ground Power

U?E			
A1	GND	GND	Y20
A11	GND	GND	Y10
A20	GND	GND	Y1
B7	GND	GND	W14
B14	GND	GND	W7
C3	GND	GND	V18
C18	GND	GND	V3
D10	GND	GND	U11
F6	GND	GND	R15
F15	GND	GND	R6
G2	GND	GND	P19
G12	GND	GND	P9
G19	GND	GND	P2
H8	GND	GND	N13
J9	GND	GND	M12
J11	GND	GND	M10
K1	GND	GND	L20
K8	GND	GND	L13
K10	GND	GND	L11
K12	GND	GND	L9
K17	GND	GND	L4
	GND	GND	

XC3S1200EFG400

6. Power



7. Revision History

	Revision	Date	By	Comments
1	1.01	March 15, 2007	MD	Initial Release