

### Schematic Symbol for XC2C256-VQ(G)100

The Orcad symbol consists of 2 heterogeneous parts, each of them listed below:

#### 1. I/O Pins (BANK1 and BANK2)

U?A			
1	INOUT/GTS(2)	INOUT(2)	99
2	INOUT/GTS(2)	INOUT(2)	97
3	INOUT/GTS(2)	INOUT(2)	96
4	INOUT/GTS(2)	INOUT(2)	95
6	INOUT/GTS(2)	INOUT(2)	94
7	INOUT(2)	INOUT(2)	93
8	INOUT(2)	INOUT(2)	92
9	INOUT(2)	INOUT(2)	91
10	INOUT(2)	INOUT(2)	90
11	INOUT(2)	INOUT(2)	89
12	INOUT(2)	INOUT(2)	87
13	INOUT(2)	INOUT(2)	86
14	INOUT(2)	INOUT(2)	85
15	INOUT(1)	INOUT(2)	82
16	INOUT(1)	INOUT(2)	81
17	INOUT(1)	INOUT(2)	80
18	INOUT(1)	INOUT(2)	79
19	INOUT(1)	INOUT(2)	78
22	INOUT(1)	INOUT(2)	77
23	GCK(1)	INOUT(2)	76
24	GCK(1)	INOUT(2)	74
27	CDR(1)	INOUT(2)	73
28	GCK(1)	INOUT(2)	72
29	DGE(1)	INOUT(2)	71
30	INOUT(1)	INOUT(2)	70
32	INOUT(1)	INOUT(2)	68
33	INOUT(1)	INOUT(2)	67
34	INOUT(1)	INOUT(2)	66
35	INOUT(1)	INOUT(2)	65
36	INOUT(1)	INOUT(2)	64
37	INOUT(1)	INOUT(1)	63
39	INOUT(1)	INOUT(1)	61
40	INOUT(1)	INOUT(1)	60
41	INOUT(1)	INOUT(1)	59
42	INOUT(1)	INOUT(1)	58
43	INOUT(1)	INOUT(1)	56
44	INOUT(1)	INOUT(1)	55
46	INOUT(1)	INOUT(1)	54
49	INOUT(1)	INOUT(1)	53
50	INOUT(1)	INOUT(1)	52

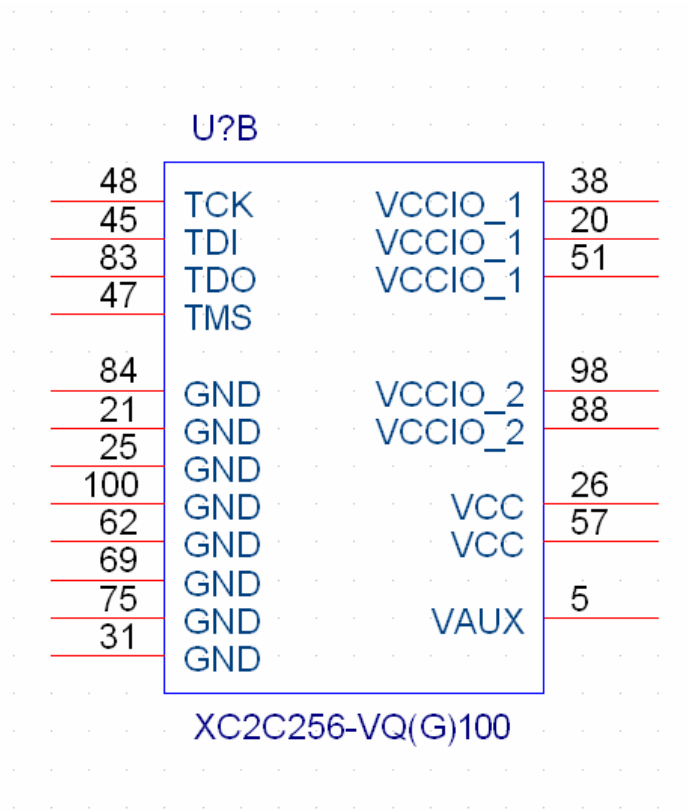
XC2C256-VQ(G)100

Notes:

-GTS = global output enable, GSR = global reset/set, GCK = global clock, CDRST = clock divide reset, DGE = DataGATE enable.

-GTS, GSR and GCK pins can be used for general purpose I/O.

## 2. Power and Programming Interface



### Document Revision History

	Revision	Date	By	Comments
1	1.01	Oct 5, 06	LD	Initial Release