

Schematic Symbol for 6SLX9-CS(G)225

The Schematic symbol consists of 5 heterogeneous parts that are listed below:

1. I/O Bank 0

U?A			
A2	IO_L1N_VREF_0	IO_L7P_0	D7
B2	IO_L1P_HSWAPEN_0	IO_L7N_0	C7
A3	IO_L2N_0	IO_L6P_0	B5
B3	IO_L2P_0	IO_L6N_0	A5
A6	IO_L33N_0	IO_L66P_SCP1_0	C12
C6	IO_L33P_0	IO_L66N_SCP0_0	A12
D8	IO_L34N_GCLK18_0	IO_L65P_SCP3_0	B13
E7	IO_L34P_GCLK19_0	IO_L65N_SCP2_0	A13
A7	IO_L35N_GCLK16_0	IO_L64P_SCP5_0	D11
B7	IO_L35P_GCLK17_0	IO_L64N_SCP4_0	C11
A8	IO_L36N_GCLK14_0	IO_L63P_SCP7_0	B11
C8	IO_L36P_GCLK15_0	IO_L63N_SCP6_0	A11
A9	IO_L37N_GCLK12_0	IO_L62P_0	C10
B9	IO_L37P_GCLK13_0	IO_L62N_VREF_0	A10
E8	IO_L38N_VREF_0	IO_L5P_0	E6
F8	IO_L38P_0	IO_L5N_0	D6
C9	IO_L39N_0	IO_L4P_0	C4
D10	IO_L39P_0	IO_L4N_0	A4
C5	IO_L3N_0	IO_L40P_0	F10
D5	IO_L3P_0	IO_L40N_0	E9

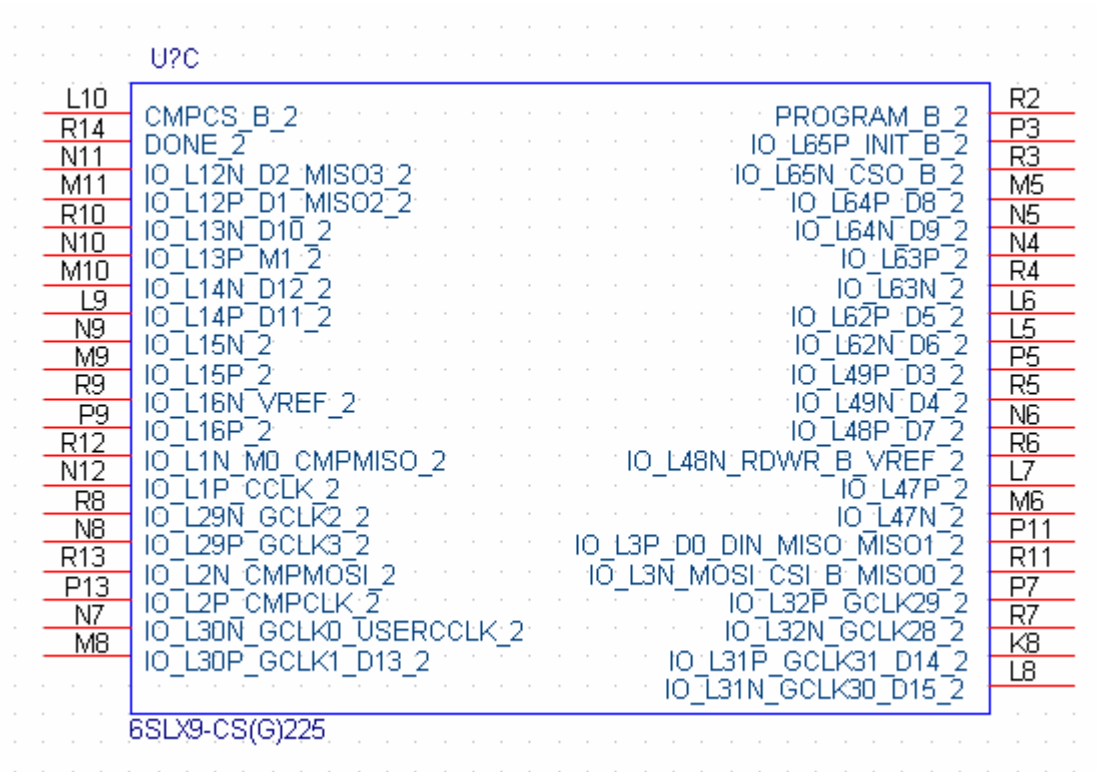
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2. I/O Bank 1

U?B		
B15	IO_L1N_A24_VREF_1	IO_L74P_AWAKE_1 P14
B14	IO_L1P_A25_1	IO_L74N_DOUT_BUSY_1 P15
G12	IO_L30N_A20_M1A11_1	IO_L47P_FWE_B_M1DQ0_1 N14
G11	IO_L30P_A21_M1RESET_1	IO_L47N_LDC_M1DQ1_1 N15
F12	IO_L31N_A18_M1A12_1	IO_L46P_FCS_B_M1DQ2_1 M13
F11	IO_L31P_A19_M1CKE_1	IO_L46N_FOE_B_M1DQ3_1 M15
H11	IO_L32N_A16_M1A9_1	IO_L45P_AT_M1LDQS_1 L14
H10	IO_L32P_A17_M1A8_1	IO_L45N_A0_M1LDQSN_1 L15
C15	IO_L33N_A14_M1A4_1	IO_L44P_A3_M1DQ6_1 K13
C14	IO_L33P_A15_M1A10_1	IO_L44N_A2_M1DQ7_1 K15
G13	IO_L34N_A12_M1BA2_1	IO_L43P_GCLK5_M1DQ4_1 J14
H12	IO_L34P_A13_M1WE_1	IO_L43N_GCLK4_M1DQ5_1 J15
D15	IO_L35N_A10_M1A2_1	IO_L42P_GCLK7_M1UDM_1 H13
D13	IO_L35P_A11_M1A7_1	IO_L42N_GCLK6_TRDY1_M1LDM_1 H15
J13	IO_L36N_A8_M1BA1_1	IO_L41P_GCLK9_TRDY1_M1RASN_1 G14
J11	IO_L36P_A9_M1BA0_1	IO_L41N_GCLK8_M1CASN_1 G15
E15	IO_L37N_A6_M1A1_1	IO_L40P_GCLK11_M1A5_1 K12
E14	IO_L37P_A7_M1A0_1	IO_L40N_GCLK10_M1A6_1 L12
K11	IO_L38N_A4_M1CLKN_1	IO_L39P_M1A3_1 F13
K10	IO_L38P_A5_M1CLK_1	IO_L39N_M1ODT_1 F15

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3. I/O Bank 2



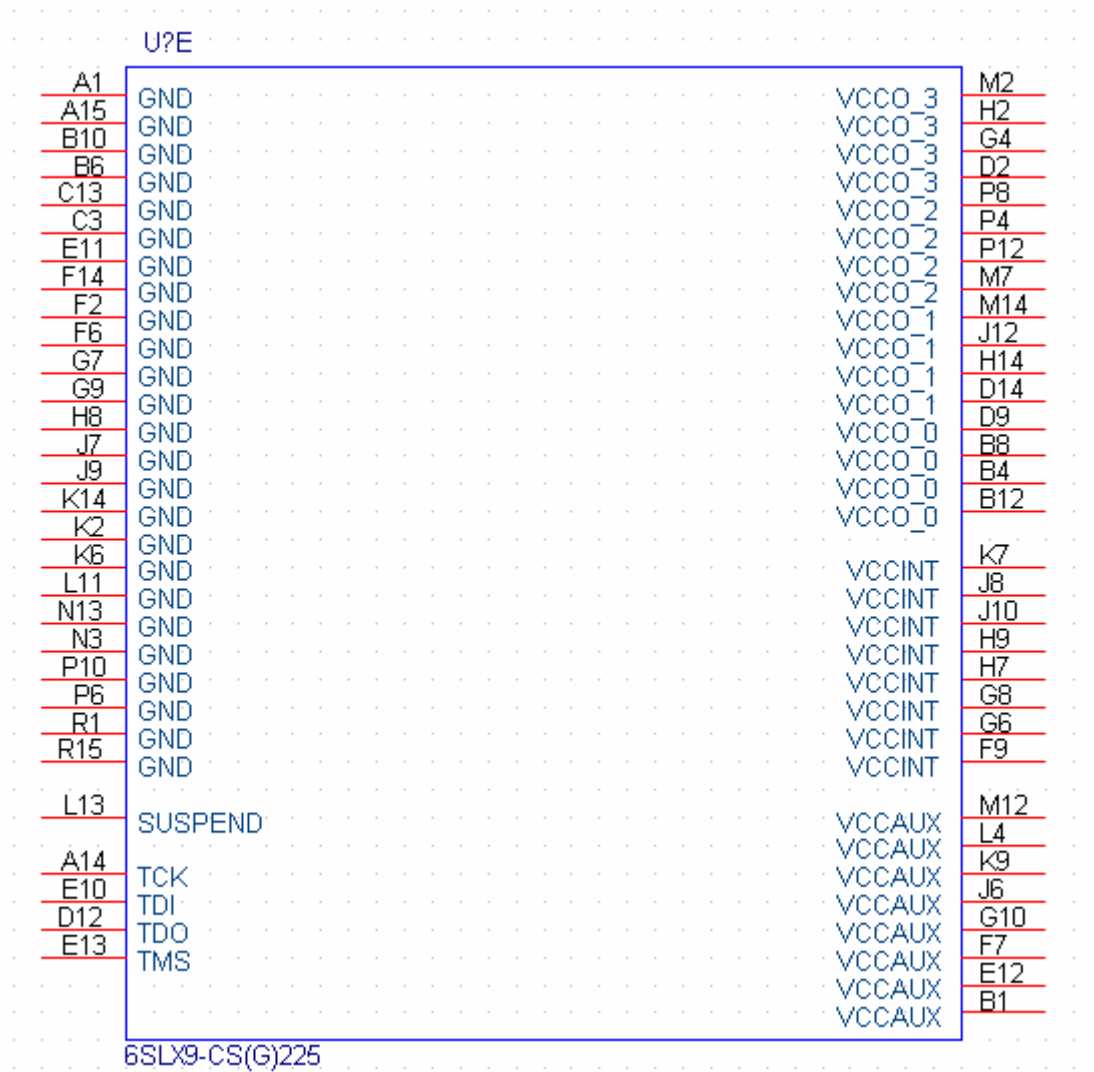
4. I/O Bank 3

U?D

L3	IO_L1N_VREF_3	IO_L83P_3	C2
M4	IO_L1P_3	IO_L83N_VREF_3	C1
P1	IO_L2N_3	IO_L54P_M3RESET_3	D3
P2	IO_L2P_3	IO_L54N_M3A11_3	D1
N1	IO_L37N_M3DQ1_3	IO_L53P_M3CKE_3	D4
N2	IO_L37P_M3DQ0_3	IO_L53N_M3A12_3	E3
M1	IO_L38N_M3DQ3_3	IO_L52P_M3A8_3	E2
M3	IO_L38P_M3DQ2_3	IO_L52N_M3A9_3	E1
L1	IO_L39N_M3LDQSN_3	IO_L51P_M3A10_3	E5
L2	IO_L39P_M3LDQS_3	IO_L51N_M3A4_3	E4
K1	IO_L40N_M3DQ7_3	IO_L50P_M3WE_3	F5
K3	IO_L40P_M3DQ6_3	IO_L50N_M3BA2_3	F4
J1	IO_L41N_GCLK26_M3DQ5_3	IO_L49P_M3A7_3	H6
J2	IO_L41P_GCLK27_M3DQ4_3	IO_L49N_M3A2_3	H5
H1	IO_L42N_GCLK24_M3LDM_3	IO_L48P_M3BA0_3	G5
H3	IO_L42P_GCLK25_TRDY2_M3UDM_3	IO_L48N_M3BA1_3	G3
J3	IO_L43N_GCLK22_IRDY2_M3CASN_3	IO_L47P_M3A0_3	J5
K4	IO_L43P_GCLK23_M3RASN_3	IO_L47N_M3A1_3	H4
G1	IO_L44N_GCLK20_M3A6_3	IO_L46P_M3CLK_3	F3
G2	IO_L44P_GCLK21_M3A5_3	IO_L46N_M3CLKN_3	F1
		IO_L45P_M3A3_3	K5
		IO_L45N_M3ODT_3	J4

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5. Power and Programming Interface



6. Revision History

	Revision	Date	By	Comments
1	1	July 20, 2009	AC	Initial Release