

Schematic Symbol for 6SLX25-TCS(G)324

The Schematic symbol consists of 7 heterogeneous parts that are listed below:

1. I/O Bank 0 and Not Connected pins

U?A			
A2	IO_L1N_VREF_0	N.C.	A12
B2	IO_L1P_HSWAPEN_0	N.C.	E11
A3	IO_L2N_0	N.C.	B11
B3	IO_L2P_0	N.C.	F10
F7	IO_L34N_GCLK18_0	N.C.	A14
E6	IO_L34P_GCLK19_0	N.C.	B14
E8	IO_L35N_GCLK16_0	N.C.	B10
G8	IO_L35P_GCLK17_0	N.C.	D13
G11	IO_L36N_GCLK14_0	N.C.	B12
G9	IO_L36P_GCLK15_0	N.C.	E10
E12	IO_L37N_GCLK12_0	N.C.	A10
F12	IO_L37P_GCLK13_0	N.C.	C11
A15	IO_L64N_SCP4_0	N.C.	C13
C15	IO_L64P_SCP5_0	N.C.	D11
A16	IO_L65N_SCP2_0	N.C.	E9
B16	IO_L65P_SCP3_0	N.C.	D12
D15	IO_L66N_SCP0_0	N.C.	A13
E14	IO_L66P_SCP1_0	N.C.	

6SLX25-TCS(G)324

2. I/O Bank 1

U?B			
F16	IO_L1N_A24_VREF_1	IO_L74P_AWAKE_1	P15
F15	IO_L1P_A25_1	IO_L74N_DOUT_BUSY_1	P16
C18	IO_L29N_A22_M1A14_1	IO_L61P_1	L14
C17	IO_L29P_A23_M1A13_1	IO_L61N_1	M13
G14	IO_L30N_A20_M1A11_1	IO_L53P_1	M14
F14	IO_L30P_A21_M1RESET_1	IO_L53N_VREF_1	N14
D18	IO_L31N_A18_M1A12_1	IO_L52P_M1DQ14_1	U17
D17	IO_L31P_A19_M1CKE_1	IO_L52N_M1DQ15_1	U18
G13	IO_L32N_A16_M1A9_1	IO_L51P_M1DQ12_1	T17
H12	IO_L32P_A17_M1A8_1	IO_L51N_M1DQ13_1	T18
E18	IO_L33N_A14_M1A4_1	IO_L50P_M1UDQS_1	N15
E16	IO_L33P_A15_M1A10_1	IO_L50N_M1UDQSN_1	N16
K13	IO_L34N_A12_M1BA2_1	IO_L49P_M1DQ10_1	P17
K12	IO_L34P_A13_M1WE_1	IO_L49N_M1DQ11_1	P18
F18	IO_L35N_A10_M1A2_1	IO_L48P_HDC_M1DQ8_1	N17
F17	IO_L35P_A11_M1A7_1	IO_L48N_M1DQ9_1	N18
H14	IO_L36N_A8_M1BA1_1	IO_L47P_FWE_B_M1DQ0_1	M16
H13	IO_L36P_A9_M1BA0_1	IO_L47N_LDC_M1DQ1_1	M18
H16	IO_L37N_A6_M1A1_1	IO_L46P_FCS_B_M1DQ2_1	L17
H15	IO_L37P_A7_M1A0_1	IO_L46N_FOE_B_M1DQ3_1	L18
G18	IO_L38N_A4_M1CLKN_1	IO_L45P_A1_M1LDQS_1	K17
G16	IO_L38P_A5_M1CLK_1	IO_L45N_A0_M1LDQSN_1	K18
K14	IO_L39N_M1ODT_1	IO_L44P_A3_M1DQ6_1	J16
J13	IO_L39P_M1A3_1	IO_L44N_A2_M1DQ7_1	J18
L13	IO_L40N_GCLK10_M1A6_1	IO_L43P_GCLK5_M1DQ4_1	H17
L12	IO_L40P_GCLK11_M1A5_1	IO_L43N_GCLK4_M1DQ5_1	H18
		IO_L42P_GCLK7_M1UDM_1	L15
		IO_L42N_GCLK6_TRDY1_M1LDM_1	L16
		IO_L41P_GCLK9_IRDY1_M1RASN_1	K15
		IO_L41N_GCLK8_M1CASN_1	K16

6SLX25-TCS(G)324

3. I/O Bank 2

U?C		
P13	CMPCS_B_2	PROGRAM_B_2
V17	DONE_2	IO_L65P_INIT_B_2
V14	IO_L12N_D2_MISO3_2	IO_L65N_CSO_B_2
T14	IO_L12P_D1_MISO2_2	IO_L64P_D8_2
P12	IO_L13N_D10_2	IO_L64N_D9_2
N12	IO_L13P_M1_2	IO_L63P_2
V13	IO_L14N_D12_2	IO_L63N_2
U13	IO_L14P_D11_2	IO_L62P_D5_2
N11	IO_L15N_2	IO_L62N_D6_2
M11	IO_L15P_2	IO_L5P_2
T11	IO_L16N_VREF_2	IO_L5N_2
R11	IO_L16P_2	IO_L49P_D3_2
V12	IO_L19N_2	IO_L49N_D4_2
T12	IO_L19P_2	IO_L48P_D7_2
T15	IO_L1N_M0_CMPMISO_2	IO_L48N_RDWR_B_VREF_2
R15	IO_L1P_CCLK_2	IO_L47P_2
P11	IO_L20N_2	IO_L47N_2
N10	IO_L20P_2	IO_L46P_2
N9	IO_L22N_2	IO_L46N_2
M10	IO_L22P_2	IO_L45P_2
V11	IO_L23N_2	IO_L45N_2
U11	IO_L23P_2	IO_L44P_2
T10	IO_L29N_GCLK2_2	IO_L44N_2
R10	IO_L29P_GCLK3_2	IO_L43P_2
V16	IO_L2N_CMPMOSI_2	IO_L43N_2
U16	IO_L2P_CMPCLK_2	IO_L41P_2
V10	IO_L30N_GCLK0_USERCCLK_2	IO_L41N_VREF_2
U10	IO_L30P_GCLK1_D13_2	IO_L40P_2
T8	IO_L31N_GCLK30_D15_2	IO_L40N_2
R8	IO_L31P_GCLK31_D14_2	IO_L3P_D0_DIN_MISO_MISO1_2
V9	IO_L32N_GCLK28_2	IO_L3N_MOSI_CSI_B_MISO0_2
T9	IO_L32P_GCLK29_2	

6SLX25-TCS(G)324

4. I/O Bank 3

U?D		
N3	IO_L1N_VREF_3	IO_L83P_3
N4	IO_L1P_3	IO_L83N_VREF_3
P3	IO_L2N_3	IO_L55P_M3A13_3
P4	IO_L2P_3	IO_L55N_M3A14_3
M5	IO_L31N_VREF_3	IO_L54P_M3RESET_3
L6	IO_L31P_3	IO_L54N_M3A11_3
U1	IO_L32N_M3DQ15_3	IO_L53P_M3CKE_3
U2	IO_L32P_M3DQ14_3	IO_L53N_M3A12_3
T1	IO_L33N_M3DQ13_3	IO_L52P_M3A8_3
T2	IO_L33P_M3DQ12_3	IO_L52N_M3A9_3
P1	IO_L34N_M3UDQSN_3	IO_L51P_M3A10_3
P2	IO_L34P_M3UDQS_3	IO_L51N_M3A4_3
N1	IO_L35N_M3DQ11_3	IO_L50P_M3WE_3
N2	IO_L35P_M3DQ10_3	IO_L50N_M3BA2_3
M1	IO_L36N_M3DQ9_3	IO_L49P_M3A7_3
M3	IO_L36P_M3DQ8_3	IO_L49N_M3A2_3
L1	IO_L37N_M3DQ1_3	IO_L48P_M3BA0_3
L2	IO_L37P_M3DQ0_3	IO_L48N_M3BA1_3
K1	IO_L38N_M3DQ3_3	IO_L47P_M3A0_3
K2	IO_L38P_M3DQ2_3	IO_L47N_M3A1_3
L3	IO_L39N_M3LDQSN_3	IO_L46P_M3CLK_3
L4	IO_L39P_M3LDQS_3	IO_L46N_M3CLKN_3
J1	IO_L40N_M3DQ7_3	IO_L45P_M3A3_3
J3	IO_L40P_M3DQ6_3	IO_L45N_M3ODT_3
H1	IO_L41N_GCLK26_M3DQ5_3	IO_L44P_GCLK21_M3A5_3
H2	IO_L41P_GCLK27_M3DQ4_3	IO_L44N_GCLK20_M3A6_3
		IO_L43P_GCLK23_M3RASN_3
		IO_L43N_GCLK22_IRDY2_M3CASN_3
		IO_L42P_GCLK25_TRDY2_M3UDM_3
		IO_L42N_GCLK24_M3LDM_3

6SLX25-TCS(G)324

5. MGT 101



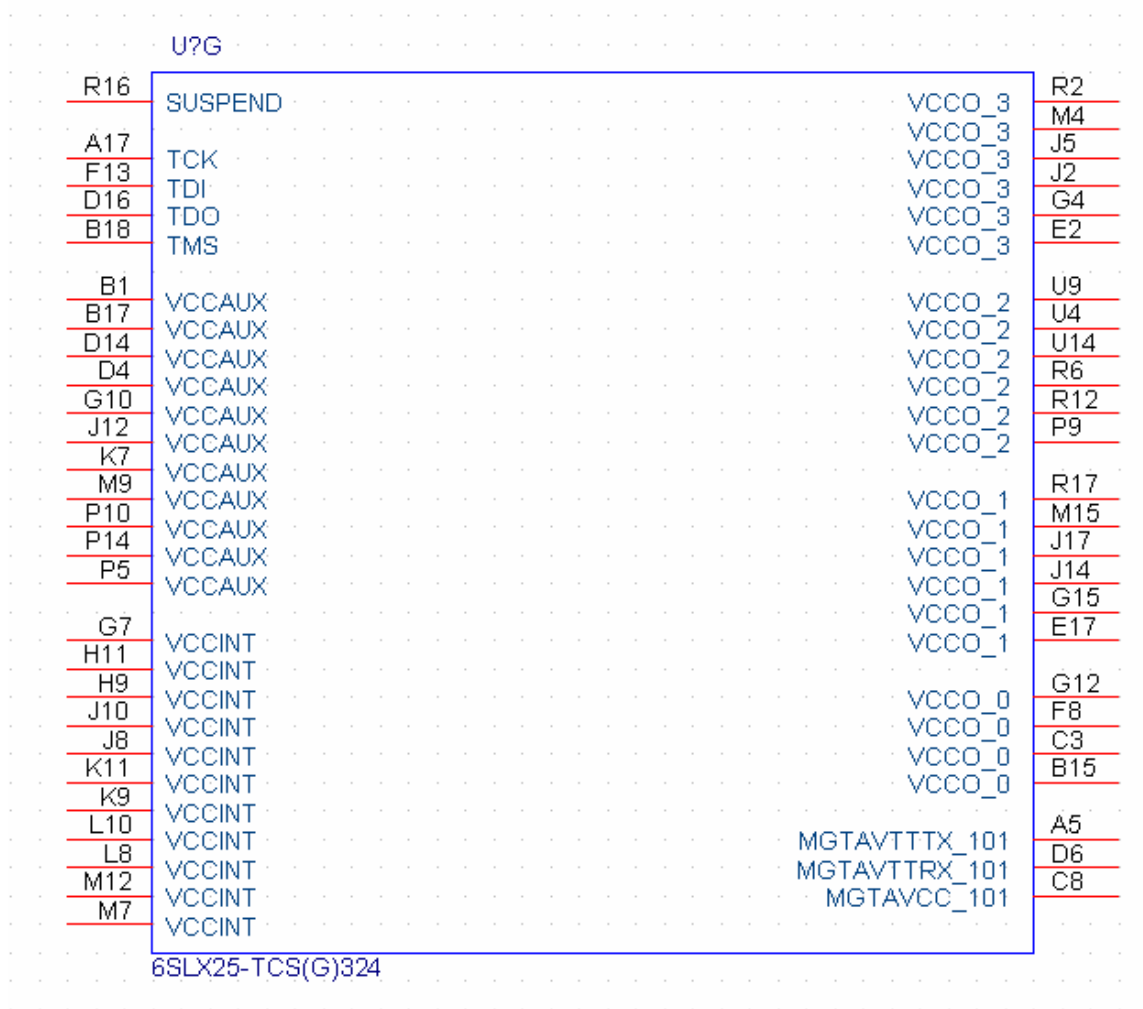
6. GND

U?F

A1	GND	GND	V18
A11	GND	GND	V1
A18	GND	GND	U6
A7	GND	GND	U12
A9	GND	GND	T16
B13	GND	GND	R9
B5	GND	GND	R4
B9	GND	GND	R18
C10	GND	GND	R14
C12	GND	GND	R1
C14	GND	GND	N13
C16	GND	GND	M6
C4	GND	GND	M2
C6	GND	GND	M17
D8	GND	GND	L9
E13	GND	GND	L11
E15	GND	GND	K8
F11	GND	GND	K10
F9	GND	GND	J9
G17	GND	GND	J4
G2	GND	GND	J15
G5	GND	GND	J11
H10	GND	GND	H8

6SLX25-TCS(G)324

7. Power and Programming Interface



8. Revision History

	Revision	Date	By	Comments
1	1	July 20, 2009	AC	Initial Release