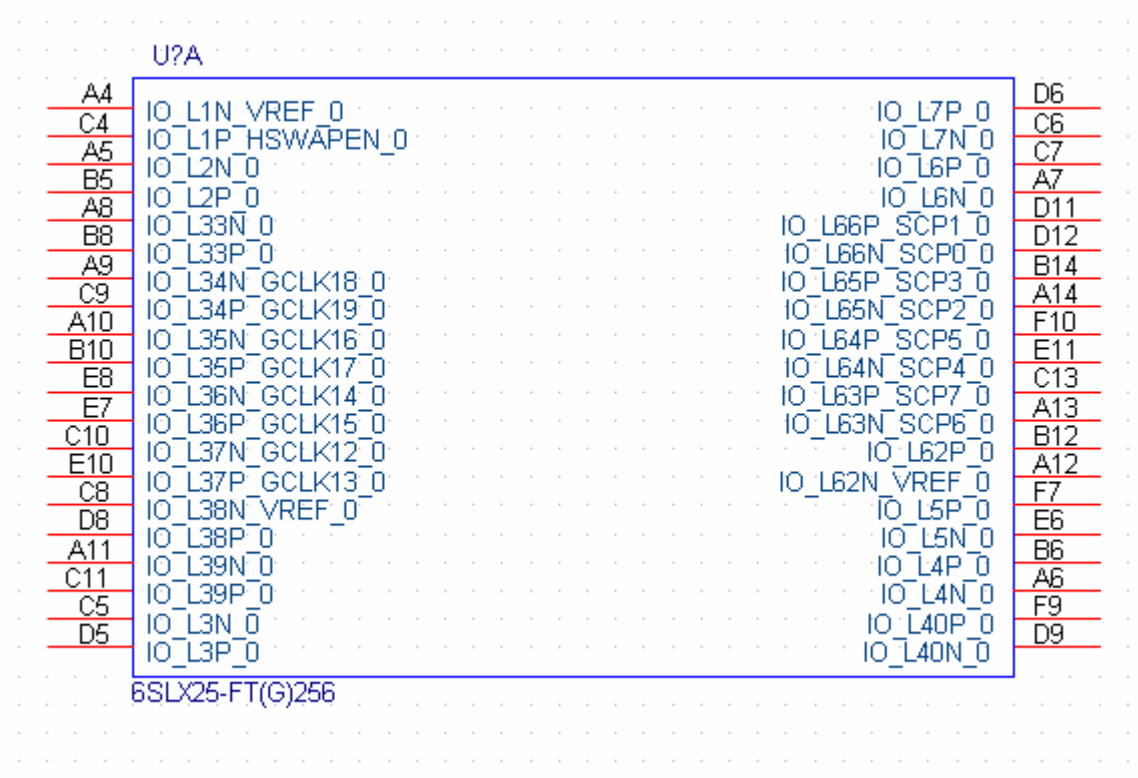
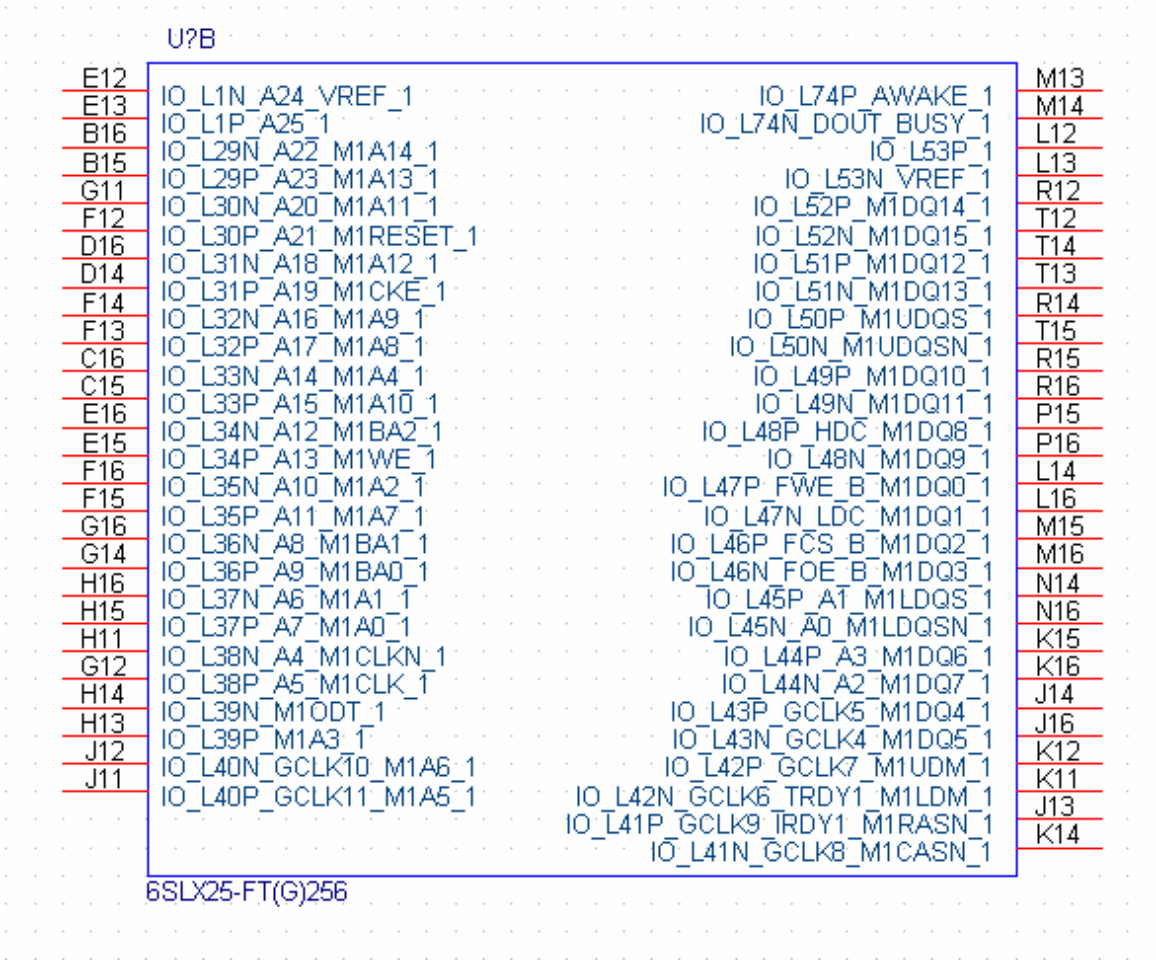
Schematic Symbol for 6SLX25-FT(G)256

The Schematic symbol consists of 6 heterogeneous parts that are listed below:

1. **I/O Bank 0**



2. I/O Bank 1


U?B
E12
E13
B16
B15
G11
F12
D16
D14
F14
F13
C16
C15
E16
E15
F16
F15
G16
G14
H16
H15
H11
G12
H14
H13
J12
J11
IO_L1N_A24_VREF_1
IO_L1P_A25_1
IO_L29N_A22_M1A14_1
IO_L29P_A23_M1A13_1
IO_L30N_A20_M1A11_1
IO_L30P_A21_M1RESET_1
IO_L31N_A18_M1A12_1
IO_L31P_A19_M1CKE_1
IO_L32N_A16_M1A9_1
IO_L32P_A17_M1A8_1
IO_L33N_A14_M1A4_1
IO_L33P_A15_M1A10_1
IO_L34N_A12_M1BA2_1
IO_L34P_A13_M1WE_1
IO_L35N_A10_M1A2_1
IO_L35P_A11_M1A7_1
IO_L36N_A8_M1BA1_1
IO_L36P_A9_M1BA0_1
IO_L37N_A6_M1A1_1
IO_L37P_A7_M1A0_1
IO_L38N_A4_M1CLKN_1
IO_L38P_A5_M1CLK_1
IO_L39N_M1ODT_1
IO_L39P_M1A3_1
IO_L40N_GCLK10_M1A6_1
IO_L40P_GCLK11_M1A5_1
IO_L74P_AWAKE_1
IO_L74N_DOUT_BUSY_1
IO_L53P_1
IO_L53N_VREF_1
IO_L52P_M1DQ14_1
IO_L52N_M1DQ15_1
IO_L51P_M1DQ12_1
IO_L51N_M1DQ13_1
IO_L50P_M1UDQS_1
IO_L50N_M1UDQSN_1
IO_L49P_M1DQ10_1
IO_L49N_M1DQ11_1
IO_L48P_HDC_M1DQ8_1
IO_L48N_M1DQ9_1
IO_L47P_FWE_B_M1DQ0_1
IO_L47N_LDC_M1DQ1_1
IO_L46P_FCS_B_M1DQ2_1
IO_L46N_FOE_B_M1DQ3_1
IO_L45P_A1_M1LDQS_1
IO_L45N_A0_M1LDQSN_1
IO_L44P_A3_M1DQ6_1
IO_L44N_A2_M1DQ7_1
IO_L43P_GCLK5_M1DQ4_1
IO_L43N_GCLK4_M1DQ5_1
IO_L42P_GCLK7_M1UDM_1
IO_L42N_GCLK6_TRDY1_M1LDM_1
IO_L41P_GCLK9_IRDY1_M1RASN_1
IO_L41N_GCLK8_M1CASN_1
M13
M14
L12
L13
R12
T12
T14
T13
R14
T15
R15
R16
P15
P16
L14
L16
M15
M16
N14
N16
K15
K16
J14
J16
K12
K11
J13
K14
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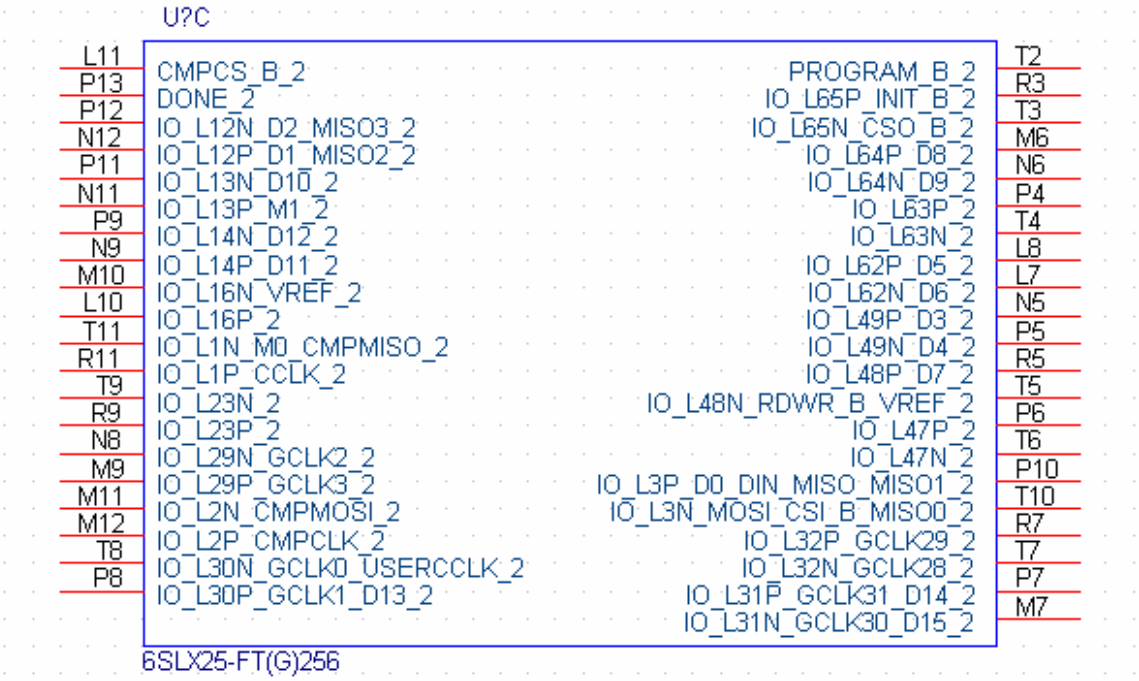
3. I/O Bank 2


U?C
L11
P13
P12
N12
P11
N11
P9
N9
M10
L10
T11
R11
T9
R9
N8
M9
M11
M12
T8
P8
CMPCS_B_2
DONE_2
IO_L12N_D2_MISO3_2
IO_L12P_D1_MISO2_2
IO_L13N_D10_2
IO_L13P_M1_2
IO_L14N_D12_2
IO_L14P_D11_2
IO_L16N_VREF_2
IO_L16P_2
IO_L1N_M0_CMPMISO_2
IO_L1P_CCLK_2
IO_L23N_2
IO_L23P_2
IO_L29N_GCLK2_2
IO_L29P_GCLK3_2
IO_L2N_CMPMOSI_2
IO_L2P_CMPCLK_2
IO_L30N_GCLK0_USERCCLK_2
IO_L30P_GCLK1_D13_2
PROGRAM_B_2
IO_L65P_INIT_B_2
IO_L65N_CSO_B_2
IO_L64P_D8_2
IO_L64N_D9_2
IO_L63P_2
IO_L63N_2
IO_L62P_D5_2
IO_L62N_D6_2
IO_L49P_D3_2
IO_L49N_D4_2
IO_L48P_D7_2
IO_L48N_RDWR_B_VREF_2
IO_L47P_2
IO_L47N_2
IO_L3P_D0_DIN_MISO_MISO1_2
IO_L3N_MOSI_CSI_B_MISO0_2
IO_L32P_GCLK29_2
IO_L32N_GCLK28_2
IO_L31P_GCLK31_D14_2
IO_L31N_GCLK30_D15_2
T2
R3
T3
M6
N6
P4
T4
L8
L7
N5
P5
R5
T5
P6
T6
P10
T10
R7
T7
P7
M7
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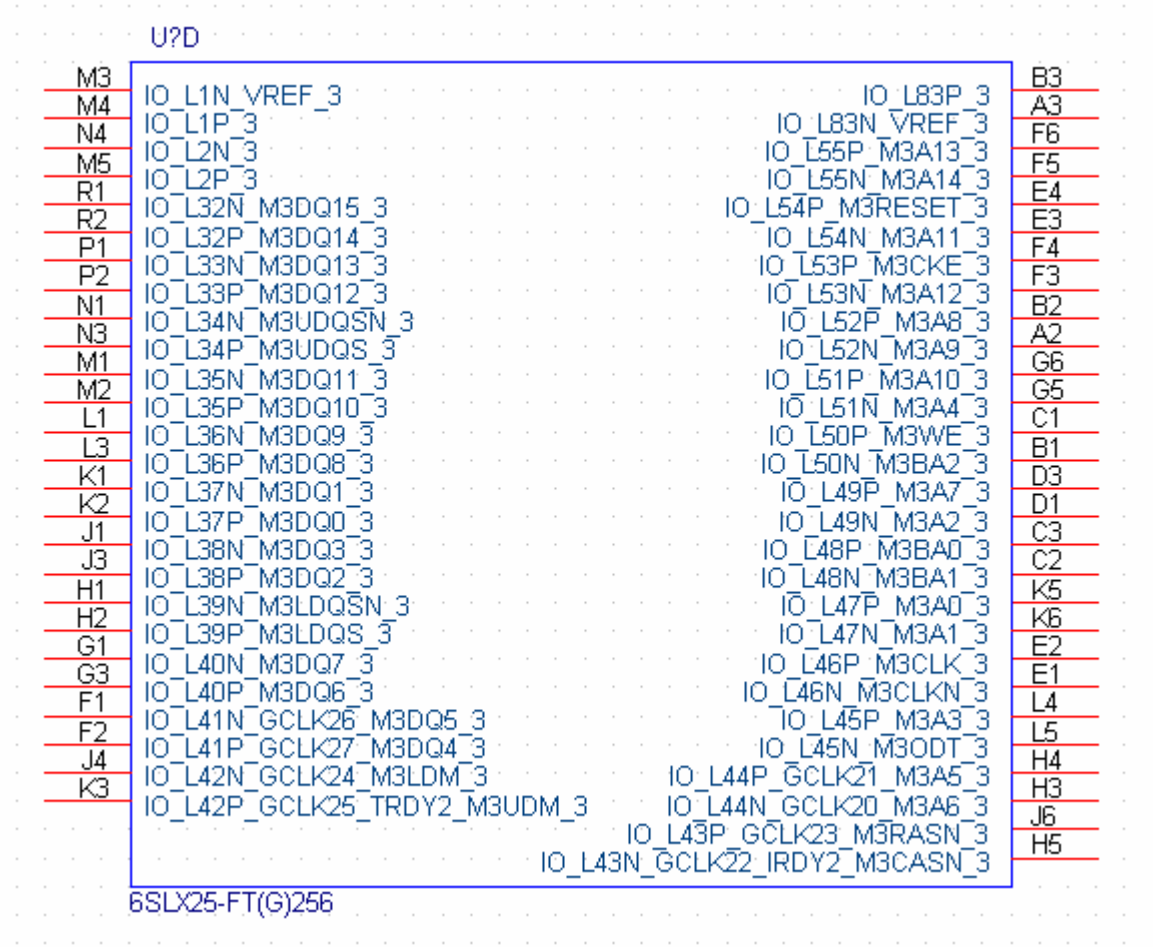
4. I/O Bank 3


U?D
M3
M4
N4
M5
R1
R2
P1
P2
N1
N3
M1
M2
L1
L3
K1
K2
J1
J3
H1
H2
G1
G3
F1
F2
J4
K3
IO_L1N_VREF_3
IO_L1P_3
IO_L2N_3
IO_L2P_3
IO_L32N_M3DQ15_3
IO_L32P_M3DQ14_3
IO_L33N_M3DQ13_3
IO_L33P_M3DQ12_3
IO_L34N_M3UDQSN_3
IO_L34P_M3UDQS_3
IO_L35N_M3DQ11_3
IO_L35P_M3DQ10_3
IO_L36N_M3DQ9_3
IO_L36P_M3DQ8_3
IO_L37N_M3DQ1_3
IO_L37P_M3DQ0_3
IO_L38N_M3DQ3_3
IO_L38P_M3DQ2_3
IO_L39N_M3LDQSN_3
IO_L39P_M3LDQS_3
IO_L40N_M3DQ7_3
IO_L40P_M3DQ6_3
IO_L41N_GCLK26_M3DQ5_3
IO_L41P_GCLK27_M3DQ4_3
IO_L42N_GCLK24_M3LDM_3
IO_L42P_GCLK25_TRDY2_M3UDM_3
IO_L83P_3
IO_L83N_VREF_3
IO_L55P_M3A13_3
IO_L55N_M3A14_3
IO_L54P_M3RESET_3
IO_L54N_M3A11_3
IO_L53P_M3CKE_3
IO_L53N_M3A12_3
IO_L52P_M3A8_3
IO_L52N_M3A9_3
IO_L51P_M3A10_3
IO_L51N_M3A4_3
IO_L50P_M3WE_3
IO_L50N_M3BA2_3
IO_L49P_M3A7_3
IO_L49N_M3A2_3
IO_L48P_M3BA0_3
IO_L48N_M3BA1_3
IO_L47P_M3A0_3
IO_L47N_M3A1_3
IO_L46P_M3CLK_3
IO_L46N_M3CLKN_3
IO_L45P_M3A3_3
IO_L45N_M3ODT_3
IO_L44P_GCLK21_M3A5_3
IO_L44N_GCLK20_M3A6_3
IO_L43P_GCLK23_M3RASN_3
IO_L43N_GCLK22_IRDY2_M3CASN_3
B3
A3
F6
F5
E4
E3
F4
F3
B2
A2
G6
G5
C1
B1
D3
D1
C3
C2
K5
K6
E2
E1
L4
L5
H4
H3
J6
H5
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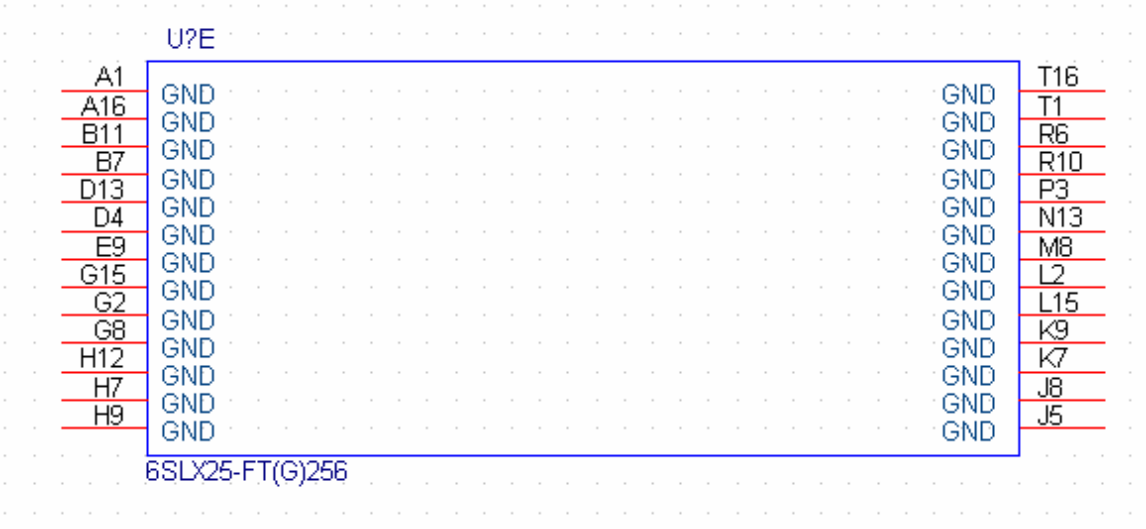
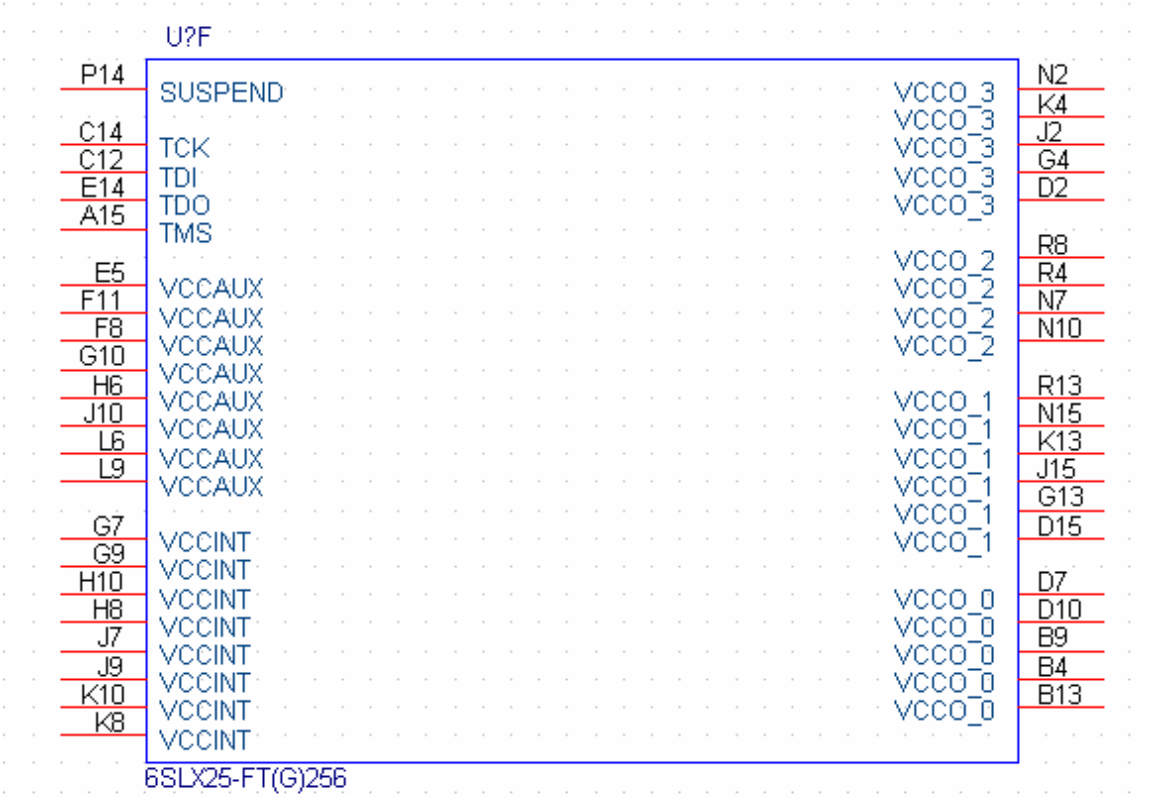
5. GND


U?E
A1
A16
B11
B7
D13
D4
E9
G15
G2
G8
H12
H7
H9
GND
T16
T1
R6
R10
P3
N13
M8
L2
L15
K9
K7
J8
J5
6SLX25-FT(G)256

6. Power and Programming Interface



7. Revision History

	Revision	Date	By	Comments
1	1	July 20, 2009	AC	Initial Release